



# CFP2000 Result

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## Supermicro

Motherboard X7DBR-8( 1.60GHz, Intel Xeon E5310 processor)

SPECfp2000 = 1731

SPECfp\_base2000 = 1727

SPEC license #01176 Tested by: Supermicro Test date: Sep-2006 Hardware Avail: Nov-2006 Software Avail: Mar-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
168.wupwise	1600	64.1	2497	64.1	2497	
171.swim	3100	131	2370	131	2370	
172.mgrid	1800	170	1057	170	1057	
173.applu	2100	152	1386	152	1386	
177.mesa	1400	88.5	1582	88.5	1582	
178.galgel	2900	74.7	3882	74.7	3883	
179.art	2600	46.8	5561	46.8	5561	
183.quake	1300	59.5	2183	57.6	2255	
187.facerec	1900	108	1758	108	1756	
188.amp	2200	186	1180	184	1194	
189.lucas	2000	110	1825	110	1824	
191.fma3d	2100	164	1277	164	1277	
200.sixtrack	1100	172	641	174	633	
301.apsi	2600	248	1050	248	1050	

### Hardware

CPU: Intel Xeon E5310 processor ( 1.60 GHz,1066MHz bus)  
CPU MHz: 1600  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
CPU(s) orderable: 1 chips  
Parallel: N/A  
Primary Cache: 32KBI + 32KBD on chip per core  
Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared per 2 cores)  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 8 X 2GB DDR2 ECC CL5 667MHz FBDIMM  
Disk Subsystem: 1 X Seagate Barracuda 120GB 7200 RPM  
Other Hardware: N/A

### Software

Operating System: Windows Server 2003 Enterprise Edition 32-bits w/ SP1  
Compiler: Intel C++ and Fortran Compiler 9.1 for 32-bit applications  
Build 20060323Z Package ID: W\_FC\_P\_9.1.020  
Microsoft Visual Studio 2005(for libraries)  
SmartHeap Library Version 8.0 from <http://www.microquill.com/>  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for Fortran programs: -fast -Qansi_alias +FDO
Base tuning for 177.mesa: -fast +FDO shlw32M.lib
Base tuning for 179.art: -fast +FDO shlw32M.lib
Base tuning for 183.quake: -fast +FDO shlw32M.lib
Base tuning for 188.amp: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -QxP -Oa -Qrcd -Qipo +FDO shlw32M.lib
```



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## Notes/Tuning Information (Continued)

```
187.facerec: -fast -Qunroll11 -Qscalar_rep- +FDO
188.amp: -fast -Oa +FDO shlw32M.lib
189.lucas: -fast -Qprefetch- +FDO
191.fma3d: basepeak=yes
200.sixtrack: -Qipo -QxP +FDO
301.apsi: -fast +FDO
```

Tested systems can be used with SC816S-R700 case,  
To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]  
Product description located as of <http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DBR-8.cfm>  
The system bus runs at 1066 MHz  
shlw32m-80.lib is the V8.0 SmartHeap library renamed to co-exist with older versions