



CINT2000 Result

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Itautec

Servidor Itautec LX201 (Intel Xeon 5160 processor)

SPECint_rate2000 = 113

SPECint_rate_base2000 = 113

SPEC license #: 9001 | Tested by: Itautec | Test date: Oct-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	4	78.5	82.7	4	78.5	82.7
175.vpr	4	81.1	80.1	4	81.1	80.1
176.gcc	4	48.3	106	4	48.3	106
181.mcf	4	110	76.0	4	110	76.0
186.crafty	4	36.4	128	4	36.4	128
197.parser	4	83.0	101	4	83.0	101
252.eon	4	33.6	180	4	33.6	180
253.perlbnk	4	54.7	153	4	54.7	153
254.gap	4	56.3	90.6	4	56.3	90.6
255.vortex	4	45.5	194	4	45.5	194
256.bzip2	4	83.2	83.6	4	83.2	83.6
300.twolf	4	88.8	157	4	88.8	157

Hardware

CPU: Intel Xeon 5160 processor (3.00 GHz, 4MB L2 shared, 1333 MHz system bus)
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4MB I+D on chip, per chip (shared)
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 4x2GB DDR2-RAM PC2-5300F (CAS 5-5-5)ECC
 Disk Subsystem: 1 x 120GB Serial ata 7200rpm
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler 9.1
 Build 20060519Z (for 32-bit applications),
 Microsoft Visual Studio .NET 7.0.9466 (for libraries),
 MicroQuill SmartHeap for SMP Library 8.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

PORTABILITY FLAGS

```
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
```

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning flags

```
for C programs: -fast +FDO sh1SMPMt.lib
for C++ programs: -fast -Qcxx_features +FDO
```

EXTRA LIBRARIES

MicroQuill SmartHeap for SMP Library 8.0 available from
<http://www.microquill.com/>