



CINT2000 Result

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Acer Incorporated

Acer Altos R510Mk2 (3.6 GHz Intel Xeon processor, 2MB L2)

SPECint_rate2000 = 21.8

SPECint_rate_base2000 = 21.7

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Jan-2006 | Hardware Avail: Jan-2006 | Software Avail: Nov-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	177	18.4	2	177	18.4
175.vpr	2	235	13.8	2	228	14.2
176.gcc	2	88.8	28.7	2	88.8	28.7
181.mcf	2	216	19.3	2	216	19.3
186.crafty	2	146	15.9	2	146	15.9
197.parser	2	203	20.6	2	201	20.8
252.eon	2	115	26.1	2	117	25.9
253.perlbnk	2	181	23.1	2	181	23.0
254.gap	2	101	25.4	2	101	25.4
255.vortex	2	130	33.9	2	130	33.9
256.bzip2	2	187	18.6	2	188	18.5
300.twolf	2	278	25.0	2	278	25.0

Hardware

CPU: Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)
CPU MHz: 3600
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology enabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache: 2048KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 6 x 512MB ECC DDR2-400 SDRAM
Disk Subsystem: 1 x 160GB 7200RPM SATA HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler: Intel C++ Compiler 9.0 Build 20051130Z for IA32
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
shlW32M.lib is Microquill SmartHeap Library Version 7.4
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlW32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
256.bzip2: -fast -Oa -Qunroll1 +FDO
```



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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib

Enable hardware prefetching and adjacent cache line prefetch BIOS setting

Product description located at <http://www.acer.com>