



# CFP2000 Result

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Acer Incorporated  
Altos G530 (3.8 GHz Intel Xeon)

SPECfp\_rate2000 = 30.3  
SPECfp\_rate\_base2000 = 29.8

SPEC license #: 97 Tested by: Acer Incorporated Test date: Sep-2005 Hardware Avail: Aug-2005 Software Avail: Oct-2004

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	76.1	48.8	2	76.1	48.8
171.swim	2	320	22.4	2	320	22.5
172.mgrid	2	200	20.9	2	199	20.9
173.applu	2	241	20.2	2	241	20.2
177.mesa	2	85.0	38.2	2	85.0	38.2
178.galgel	2	100	67.1	2	99.9	67.4
179.art	2	87.7	68.8	2	87.7	68.8
183.quake	2	94.3	32.0	2	94.1	32.0
187.facerec	2	124	35.5	2	124	35.5
188.amp	2	207	24.7	2	174	29.4
189.lucas	2	217	21.4	2	218	21.3
191.fma3d	2	206	23.6	2	206	23.6
200.sixtrack	2	160	15.9	2	159	16.0
301.apsi	2	250	24.2	2	249	24.3

### Hardware

CPU: Intel Xeon (3.8 GHz, 2MB L2, 800MHz system bus)  
CPU MHz: 3800  
FPU: Integrated  
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip(Hyper-Threading Technology disabled)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip  
Secondary Cache: 2048KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 4 x 1024MB ECC DDR2-400 SDRAM  
Disk Subsystem: 1 x 146GB 10000RPM U320 SCSI HDD  
Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)  
Compiler: Intel C++ Build 20041019Z for IA32 and Fortran Compiler 8.1 Build 20040802Z for IA32  
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)  
shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for all Fortran Programs : -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd +FDO shlw32M.lib
187.facerec: basepeak=yes
188.amp: -Oa -QxP
189.lucas: -fast -Qprefetch- +FDO
```



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## Notes/Tuning Information (Continued)

```
191.fma3d:    basepeak=yes
200.sixtrack: -Qipo -QxP +FDO
301.apsi:     -fast +FDO
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```