



CINT2000 Result

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Acer Incorporated
Altos R510 (3.6 GHz Intel Xeon)

SPECint_rate2000 = 37.6
SPECint_rate_base2000 = 37.6

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2005 | Hardware Avail: May-2005 | Software Avail: Oct-2004

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	119	27.2	2	119	27.2
175.vpr	2	126	25.8	2	124	26.2
176.gcc	2	56.2	45.4	2	56.2	45.4
181.mcf	2	123	33.9	2	123	33.9
186.crafty	2	74.7	31.1	2	74.7	31.1
197.parser	2	125	33.4	2	125	33.4
252.eon	2	57.7	52.3	2	57.5	52.4
253.perlbnk	2	87.7	47.6	2	87.2	47.9
254.gap	2	66.4	38.4	2	66.4	38.4
255.vortex	2	70.3	62.7	2	70.3	62.7
256.bzip2	2	124	28.0	2	124	28.0
300.twolf	2	167	41.7	2	167	41.7

Hardware

CPU: Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)
CPU MHz: 3600
FPU: Integrated
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache: 2048KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 6 x 512MB ECC DDR-333 SDRAM
Disk Subsystem: 1 x 120GB 7200RPM SATA HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler: Intel C++ Compiler 8.1 Build 20041019Z
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
SmartHeap Library Version 7.4 from <http://www.microquill.com/>
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
252.eon: Approved stdcpp src.alt used
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlW32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
```



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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll1 +FDO
300.twolf: -fast -O3 +FDO shlw32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at <http://www.acer.com>