CINT2000 Result

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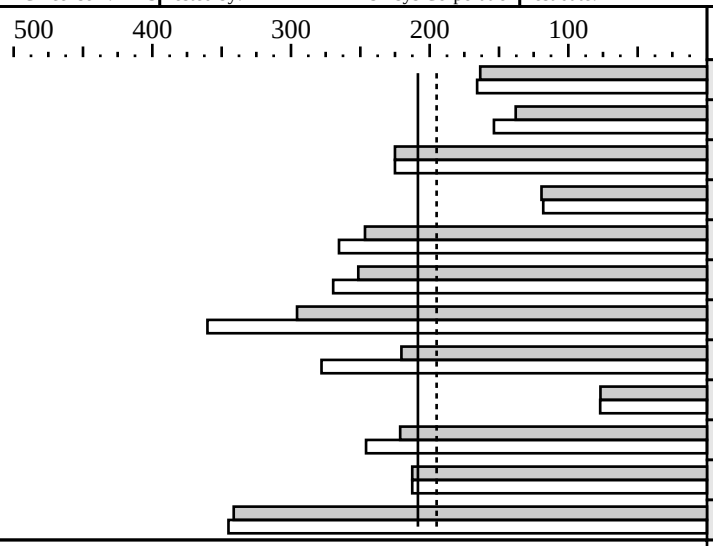
Unisys Corporation

Unisys ES7000 ORION 540 (3.0GHz Xeon MP)

SPECint_rate2000 = 208

SPECint_rate_base2000 = 195

SPEC license #: 15 | Tested by: Unisys Corporation | Test date: Nov-2004 | Hardware Avail: Apr-2004 | Software Avail: Mar-2004



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	20	199	164	20	196	166
175.vpr	20	235	138	20	211	154
176.gcc	20	113	225	20	113	225
181.mcf	20	350	119	20	354	118
186.crafty	20	94.1	247	20	87.4	265
197.parser	20	166	251	20	155	270
252.eon	20	102	296	20	83.7	360
253.perlbmk	20	190	220	20	150	278
254.gap	20	332	76.8	20	331	77.0
255.vortex	20	199	221	20	179	246
256.bzip2	20	164	213	20	164	213
300.twolf	20	204	341	20	202	345

Hardware

CPU:	Intel Xeon MP (400MHz system bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	20 cores, 20 chips, 1 core/chip(HT Technology disabled)
CPU(s) orderable:	16,20,24,28,32
Parallel:	No
Primary Cache:	12K(I) micro-ops+8KBD on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	4096KB(I+D) on chip
Other Cache:	32MB L4 Cache off chip per 4 CPUs
Memory:	8GB
Disk Subsystem:	2X36GB SCSI RAID0 DISKs (15K rpm each)
Other Hardware:	

Software

Operating System:	MS Windows 2003 Server Datacenter Edition Build 3790
Compiler:	Intel C++ Compiler 7.1 Build 20031229Z Microsoft .NET Visual 7.0.9466 (for libraries) SmartHeap Library Version 7.1
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs  : -Qxi -Qipo -Qfp_port
Base tuning for C++ programs: -QxW -Qipo -GX -GR
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     -QxW -Qipo -Oa -Oi- +FDO
175.vpr:      -QxW -Qipo     -O3  +FDO
176.gcc:      basepeak=yes
181.mcf:      -QxW -Qipo     -O3 +FDO
186.crafty:   -QxW -Qipo -Oa -O3 +FDO
197.parser:   -QxW -Qipo     -O3 +FDO
252.eon:      -QxW -Qipo     -O3 +FDO
253.perlbmk:  -QxW -Qipo     -O3 +FDO shlW32M.lib
254.gap:
255.vortex    -QxW -Qipo     -O3 +FDO
256.bzip2:    basepeak=yes
```

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Notes/Tuning Information (Continued)

```
300.twolf:    -QxW -Qipo     -O3 +FDO shlW32M.lib

Enabled two user settings in the BIOS :
1."Adjacent Sector Prefetch"
2."Prefetch Queue"
```