



CINT2000 Result

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Dell
PowerEdge 6600 (3.0 GHz Xeon MP)

SPECint2000 = **1408**
SPECint_base2000 = **1379**

SPEC license #: 55 Tested by: Dell, Round Rock, TX Test date: Dec-2003 Hardware Avail: Jan-2004 Software Avail: Apr-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	120	1165	120	1165	
175.vpr	1400	126	1115	126	1115	
176.gcc	1100	68.6	1604	68.8	1598	
181.mcf	1800	128	1403	130	1388	
186.crafty	1000	85.3	1172	85.2	1174	
197.parser	1800	142	1268	142	1268	
252.eon	1300	98.2	1324	84.7	1535	
253.perlbmk	1800	129	1391	127	1421	
254.gap	1100	73.0	1507	73.0	1507	
255.vortex	1900	91.8	2069	91.8	2069	
256.bzip2	1500	117	1283	111	1347	
300.twolf	3000	204	1473	193	1552	

Hardware

CPU: Intel Xeon MP (400 MHz system bus)
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip (Hyper-Threading Technology disabled)
 CPU(s) orderable: 1,2,4
 Parallel: No
 Primary Cache: 12K(I) micro-ops + 8KB(D) on chip
 Secondary Cache: 512KB(I+D) on chip
 L3 Cache: 4096KB(I+D) on chip
 Other Cache: N/A
 Memory: 4 x 1024MB DDR200
 Disk Subsystem: 1 x 36GB SEAGATE SCSI
 Other Hardware:

Software

Operating System: Windows 2000 Advanced Server (SP2)
 Compiler: Intel C++ Compiler 7.1 (20030402Z)
 Microsoft Visual Studio .NET (7.0.9466)
 MicroQuill SmartHeap Library 6.01
 File System: NTFS
 System State: Default

Notes/Tuning Information

PORTABILITY FLAGS

176.gcc: -Dalloca=_alloca /F10000000
 186.crafy: -DNT_i386
 253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

FEEDBACK DIRECTED OPTIMIZATION

FDO: PASS1= -Qprof_gen PASS2=-Qprof_use

BASE TUNING

C: -QxW -Qipo -O3 +FDO
 C++: -QxW -Qipo -GX -GR

PEAK TUNING

164.gzip: -QxW -Qipo -Oa -O3 +FDO
 175.vpr: -QxW -Qipo -O3 +FDO
 176.gcc: -QxW -Qipo -O3 +FDO
 181.mcf: -QxW -Qipo -O3 +FDO shlw32M.lib
 186.crafty: PASS1= -Qprof_gen PASS2= -Qipo -Qprof_use -QxW -O3
 197.parser: -QxW -Qipo -O3 +FDO
 252.eon: PASS1= -Qprof_gen PASS2= -Qipo -Qprof_use -QxW -O3
 253.perlbmk: -QxW -Qipo -O3 +FDO shlw32M.lib
 254.gap: basepeak=yes



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Notes/Tuning Information (Continued)

```
255.vortex    basepeak=yes
256.bzip2:    -Qipo -Oa -Qunroll1 +FDO
300.twolf:    -Qipo -QxW -O3 -Oa      shlW32M.lib
EXTRA LIBRARIES
shlW32M.lib:  MicroQuill SmartHeap Library 6.01
              www.microquill.com
ONESTEP=YES is set for all benchmarks
Hyper-Threading Technology disabled in BIOS
```