



CINT2000 Result

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RackSaver RSN-1164/op (1.8 GHz Opteron)

SPECint_rate2000 = 25.1
SPECint_rate_base2000 = 24.0

SPEC license #: 2686 | Tested by: RackSaver/Newisys | Test date: Apr-2003 | Hardware Avail: Apr-2003 | Software Avail: Apr-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	147	22.0	2	143	22.7
175.vpr	2	157	20.6	2	157	20.6
176.gcc	2	107	23.8	2	88.5	28.9
181.mcf	2	341	12.2	2	342	12.2
186.crafty	2	83.0	28.0	2	83.0	27.9
197.parser	2	180	23.2	2	191	21.8
252.eon	2	103	29.2	2	91.1	33.1
253.perlbnk	2	157	26.6	2	145	28.9
254.gap	2	97.9	26.1	2	97.7	26.1
255.vortex	2	119	37.0	2	119	36.9
256.bzip2	2	169	20.5	2	158	22.0
300.twolf	2	257	27.1	2	230	30.3

Hardware

CPU: AMD Opteron 244, 1.8 GHz
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 2 chips, 1 core/chip
 CPU(s) orderable: 1, 2
 Parallel: No
 Primary Cache: 64KBI + 64KBD on chip
 Secondary Cache: 1024KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 8x256MB PC2700 DDR SDRAM CL2.5
 Disk Subsystem: 1 x 36 GB Maxtor Atlas 10K III U320
 Other Hardware: None

Software

Operating System: Windows Server 2003, Enterprise Edition
 Compiler: Intel C/C++ 7.0 build 20021212Z
 Microsoft Visual Studio .NET Version 7.0.9466 (libraries)
 MicroQuill SmartHeap Library 6.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

shlw32M6.lib is the SmartHeap library V6.0 from MicroQuill www.microquill.com

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability:

176.gcc: -Dalloca=_alloca /F10000000
 186.crafty: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Baseline C:

+FD0 -O3 -QxW -Qipo

Baseline C++:

+FD0 -Qipo -GX -GR

Peak tuning:

164.gzip: +FD0 -O3 -QaxK -Qipo -Oi-
 175.vpr: +FD0 -O3 -QxW -Qipo
 176.gcc: +FD0 -O3 -QxK -Qipo -Oi-
 181.mcf: +FD0 -Qipo -Oa
 186.crafty: +FD0 -O3 -QxW -Qipo
 197.parser: +FD0 -O3 -QxW -Qipo -Oa
 252.eon: +FD0 -O3 -QaxW -Qipo -Zp4
 253.perlbnk: +FD0 -O3 -Qipo -Oa shlw32M6.lib
 254.gap: +FD0 -O3 -QxW -Qipo
 255.vortex: +FD0 -Qipo -Qunroll1



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Notes/Tuning Information (Continued)

256.bzip2: +FD0 -Qipo -Oa -Qunroll1
300.twolf: +FD0 -Qxi -Qipo shlw32M6.lib
ONESTEP is used for all base and peak runs