OMPL2001 Result

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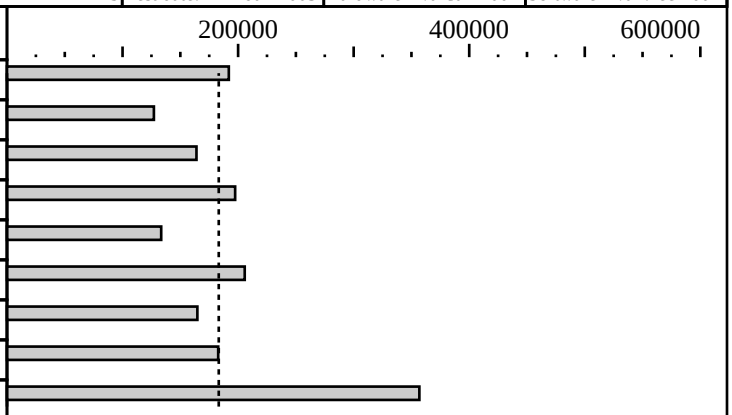
NEC
NEC TX7 i9510 (1500MHz, Itanium 2)

SPECompLpeak2001 = --

SPECompLbase2001 = 183248

SPEC license #:	HPG0057A	Tested by:	HLRS	Test site:	HLRS	Test date:	Jan-2005	Hardware Avail:	Jan-2004	Software Avail:	Dec-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Peak Runtime	Peak Ratio
311.wupwise_l	9200	767	191950		
313.swim_l	12500	1573	127116		
315.mgrid_l	13500	1317	163968		
317.applu_l	13500	1094	197431		
321.equake_l	13000	1559	133439		
325.apsi_l	10500	817	205720		
327.gafort_l	11000	1068	164746		
329.fma3d_l	23500	2057	182772		
331.art_l	25000	1121	356858		



Hardware

CPU:	Intel Itanium 2
CPU MHz:	1500
FPU:	Integrated
CPU(s) enabled:	32 cores, 32 chips, 1 core/chip
CPU(s) orderable:	8-32
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	6.0MB (on chip) per CPU
Other Cache:	N/A
Memory:	512 GB
Disk Subsystem:	Seagate ST373307LC
Other Hardware:	None

Software

OpenMP Threads:	32
Parallel:	OpenMP
Operating System:	NEC Linux 3.4
Compiler:	Intel(R) Fortran Compiler for Linux 8.1 Build 20041123 Intel(R) C++ Compiler for Linux 8.1 Build 20041123
File System:	ext2 filesystem
System State:	Multi-user

Notes/Tuning Information

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Tested by HLRS

 Portability Flags:
   318.galgel_m: -FI -132

 Extra Flags:
   330.art_m: -DINTS_PER_CACHELINE=32 -DDBLS_PER_CACHELINE=16

 Baseline user environment:
   OMP_NUM_THREADS 32
   limit stacksize 64000
   KMP_STACKSIZE 31M
   KMP_LIBRARY TURNAROUND
   OMP_DYNAMIC FALSE
   KMP_SCHEDULE static,balanced

 For all benchmarks threads were bound to cores using the following submit command:
 fplace -r -o 1,2 $command,
 This  instructs fplace to skip placement of the lightweight
 OpenMP monitor thread, which is created prior to the

 For a description of HLRS's compiler flags, portability flags, and
 system parameters used to generate this result, please refer to the
 HLRS-20050119-Linux-Intel8.1-IPF.txt file in the flags directory.
```