SPEC CFP95rate Results

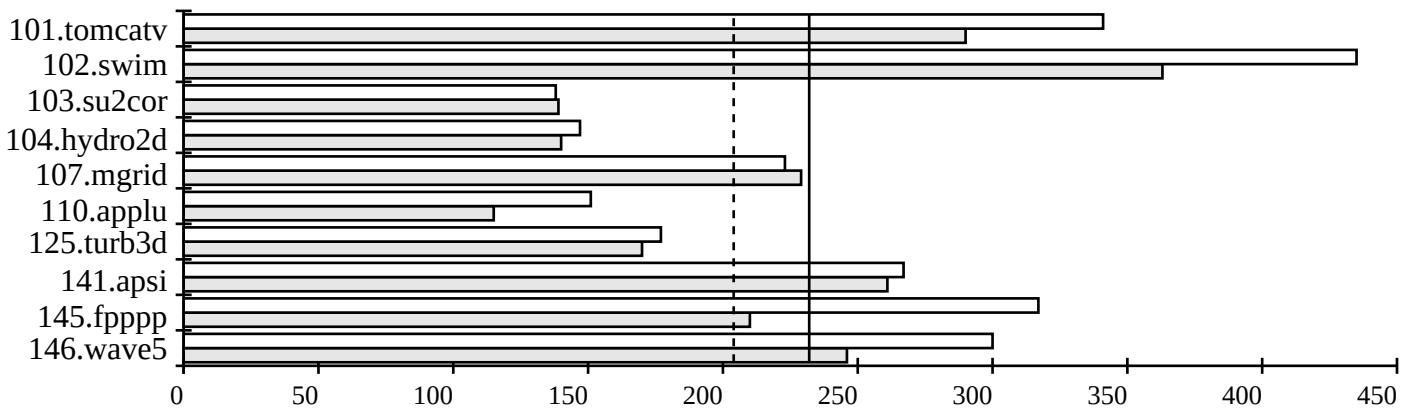
©Copyright 1995, Standard Performance Evaluation Corporation

Sun Microsystems
Sun Enterprise 450

SPECfp_rate95 = 232

SPECfp_rate_base95 = 204

SPEC license # 6	Tested By: Sun, Palo Alto	Test Date: Oct-98	Hardware Avail: Nov-98	Software Avail: Feb-99



Hardware/Software Configuration for:
Sun Enterprise 450

Hardware

Model Name:	Sun Enterprise 450
CPU:	400 MHz UltraSPARC-II
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	4MB (I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2 x 4.2GB
Other Hardware:	Fast Ethernet

Software

Operating System:	Solaris 7 (kernel/unix)
Compiler:	Sun f77 5.0 Early Access
File System:	UFS
System State:	Single User
Kernel Extensions:	consistent_coloring=1

Benchmark # and Name	Base Copies	Base Run Time	Base SPEC Ratio	Copies	Run Time	SPEC Ratio
101.tomcatv	1	115	290	1	98	341
102.swim	1	213	363	1	178	435
103.su2cor	1	90.7	139	1	92	138
104.hydro2d	1	154	140	1	147	147
107.mgrid	1	98.3	229	1	101	223
110.applu	1	171	115	1	131	151
125.turb3d	1	217	170	1	209	177
141.apsi	1	72.5	261	1	71	267
145.fpppp	1	412	210	1	272	317
146.wave5	1	110	246	1	90	300
SPECfp_rate_base95 (G. Mean)			204			
			SPECfp_rate95 (G. Mean)			232

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback; P2= -xprofile=use:‘pwd‘/../feedback
BCOPY=-Qoption iropt -O4+bcopy; INV=-Qoption iropt -O4+invccexp
Baseline:    -fast -xarch=v8plusa -fsimple=2 -xprefetch
101.tomcatv: -fast -xarch=v8plusa -xpad=local:23 -xprefetch -Qoption iropt -O4+data_access
102.swim:    -fast -xarch=v8plusa -xpad=common:15 -xprefetch -fsimple=2
103.su2cor:  -fast -xarch=v8plusa -xvector -Qoption cg -Qms_pipe+nfll=5 -fsimple=2  ${BCOPY}
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
104.hydro2d: -fast -xarch=v8plusa -xprefetch -Qoption iropt -O4+pde -fsimple=2
104.hydro2d:  PASS1= ${P1}; PASS2= ${P2};
107.mgrid:   -fast -xarch=v8plusa -xprefetch -fsimple=2  ${BCOPY}
107.mgrid:   PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
110.applu:   -fast -xarch=v8plusa -Qoption iropt -O4+scalarrep+ddint+perfect+loop_dist+pde+algassoc,-Mt6000,-Mr20000,-Ma140,-ldstr -fsimple=2
125.turb3d:  -fast -xarch=v8plusa -stackvar -xpad -fsimple=2 -Qoption iropt -04+bcopy ${INV} -xvector=yes
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
141.apsi:    -fast -xarch=v8plusa -xchip=ultra3 -fsimple=2 -stackvar  -Qoption iropt -Si36
141.apsi: ${INV} -Qoption iropt -O4+algassoc; PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
145.fpppp:   -fast -xarch=v8plusa -xO5 -xsafe=mem -fsimple=2 -nodepend -Qoption iropt -O4+algassoc -Qoption iropt -reroll=1
145.fpppp:   PASS1= ${P1}; PASS2= ${P2};
146.wave5:   -fast -xarch=v8plusa  -stackvar -fsimple=2 -Qoption iropt -O4+unroll ${BCOPY}
Shell limits: # ulimit -n 1024  or % limit descriptors 1024
Config file: E450_DT.pub.int.fp_2.cfg
```

For More Information Contact:
SPEC
10754 Ambassador Drive, Suite 201
Manassas, VA 20109
(703) 331-0180
info@specbench.org
http://www.specbench.org/

--

Prepared By: Sun Palo Alto