



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

CPU2026 License: 9019

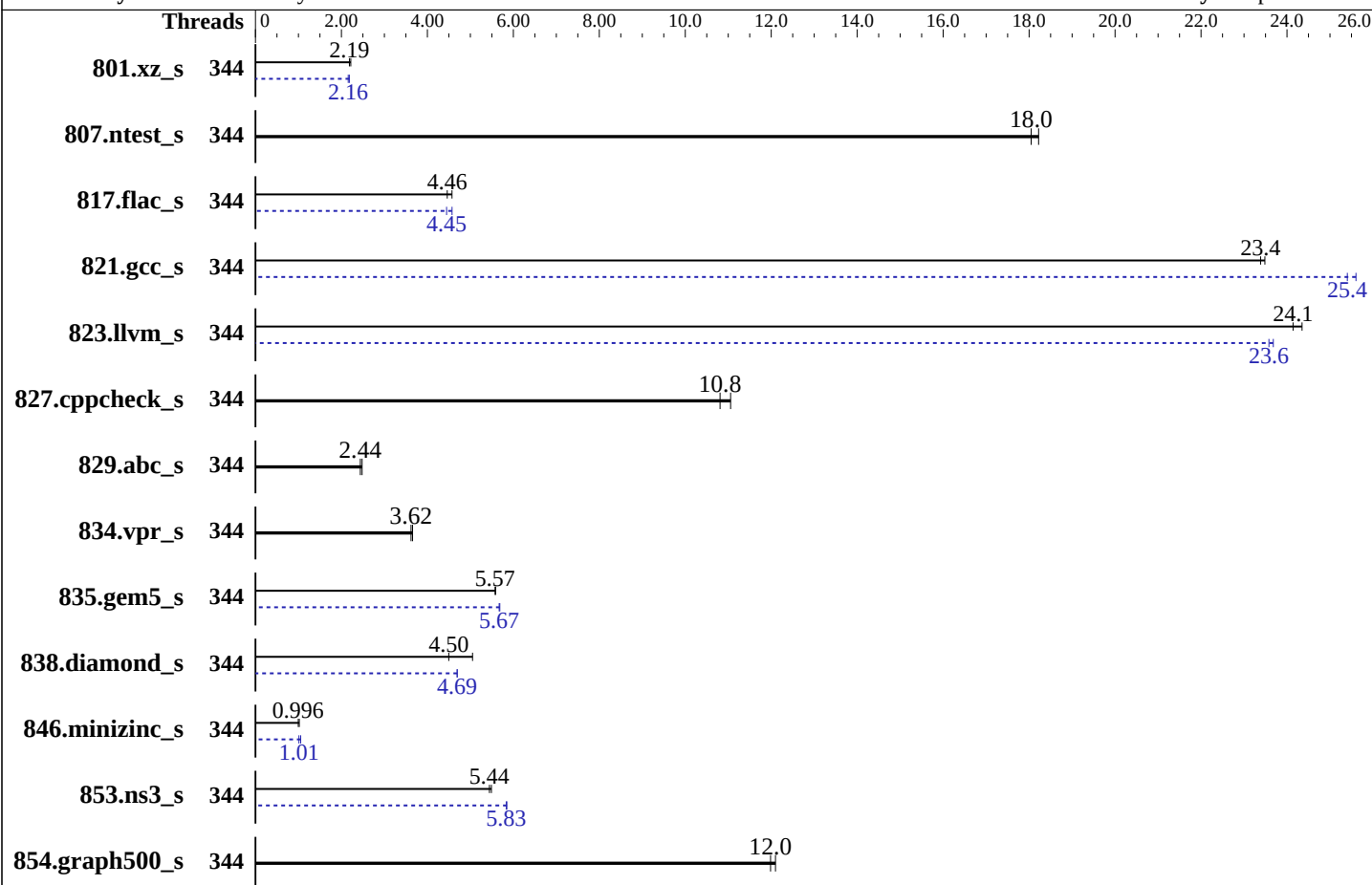
Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jun-2026

Hardware Availability: May-2025

Software Availability: Apr-2026



### Hardware

CPU Name: Intel Xeon 6787P  
Max MHz: 3800  
Nominal: 2000  
Enabled: 172 cores, 2 chips, 2 threads/core  
Orderable: 1,2 Chips  
Cache L1: 64 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 336 MB I+D on chip per chip  
Other: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-6400B-R)  
Storage: 1 x 1.9 TB NVME SSD  
Cooling: Air  
Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP6 6.4.0-150600.21-default  
Compiler: C/C++: Version 2026.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2026.0 of Intel Fortran Compiler for Linux  
Compiler Category: Vendor  
Firmware: Version 4.3.6b released Apr-2025  
File System: xfs  
System State: Run Level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: jemalloc memory allocator v5.3  
Power Management: BIOS set to prefer performance at the cost of additional power usage



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jun-2026

Hardware Availability: May-2025

Software Availability: Apr-2026

## Results Table

| Benchmark      | Base    |             |             |             |              |         |       | Peak    |             |             |             |             |         |       |
|----------------|---------|-------------|-------------|-------------|--------------|---------|-------|---------|-------------|-------------|-------------|-------------|---------|-------|
|                | Threads | Seconds     | Ratio       | Seconds     | Ratio        | Seconds | Ratio | Threads | Seconds     | Ratio       | Seconds     | Ratio       | Seconds | Ratio |
| 801.xz_s       | 344     | <b>270</b>  | <b>2.19</b> | 266         | 2.22         |         |       | 344     | 271         | 2.18        | <b>274</b>  | <b>2.16</b> |         |       |
| 807.ntest_s    | 344     | 62.6        | 18.2        | <b>63.2</b> | <b>18.0</b>  |         |       | 344     | 62.6        | 18.2        | <b>63.2</b> | <b>18.0</b> |         |       |
| 817.flac_s     | 344     | <b>389</b>  | <b>4.46</b> | 380         | 4.57         |         |       | 344     | <b>391</b>  | <b>4.45</b> | 380         | 4.57        |         |       |
| 821.gcc_s      | 344     | <b>88.5</b> | <b>23.4</b> | 88.1        | 23.5         |         |       | 344     | <b>81.5</b> | <b>25.4</b> | 80.8        | 25.6        |         |       |
| 823.llvm_s     | 344     | 57.9        | 24.4        | <b>58.4</b> | <b>24.1</b>  |         |       | 344     | 59.6        | 23.7        | <b>59.8</b> | <b>23.6</b> |         |       |
| 827.cppcheck_s | 344     | <b>104</b>  | <b>10.8</b> | 101         | 11.1         |         |       | 344     | <b>104</b>  | <b>10.8</b> | 101         | 11.1        |         |       |
| 829.abc_s      | 344     | <b>341</b>  | <b>2.44</b> | 335         | 2.48         |         |       | 344     | <b>341</b>  | <b>2.44</b> | 335         | 2.48        |         |       |
| 834.vpr_s      | 344     | <b>264</b>  | <b>3.62</b> | 261         | 3.66         |         |       | 344     | <b>264</b>  | <b>3.62</b> | 261         | 3.66        |         |       |
| 835.gem5_s     | 344     | <b>204</b>  | <b>5.57</b> | 204         | 5.59         |         |       | 344     | <b>201</b>  | <b>5.67</b> | 200         | 5.69        |         |       |
| 838.diamond_s  | 344     | <b>223</b>  | <b>4.50</b> | 198         | 5.05         |         |       | 344     | 213         | 4.69        | <b>213</b>  | <b>4.69</b> |         |       |
| 846.minizinc_s | 344     | 657         | 1.02        | <b>673</b>  | <b>0.996</b> |         |       | 344     | <b>662</b>  | <b>1.01</b> | 637         | 1.05        |         |       |
| 853.ns3_s      | 344     | <b>212</b>  | <b>5.44</b> | 210         | 5.49         |         |       | 344     | <b>198</b>  | <b>5.83</b> | 197         | 5.85        |         |       |
| 854.graph500_s | 344     | <b>51.0</b> | <b>12.0</b> | 50.5        | 12.1         |         |       | 344     | <b>51.0</b> | <b>12.0</b> | 50.5        | 12.1        |         |       |

SPECspeed®2026\_int\_base = **6.08**

SPECspeed®2026\_int\_peak = **6.17**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2026/lib"

MALLOC\_CONF = "retain:true"

OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using CentOS Stream 9.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop\_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

(Continued on next page)



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

**CPU2026 License:** 9019

**Test Sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test Date:** Jun-2026

**Hardware Availability:** May-2025

**Software Availability:** Apr-2026

## General Notes (Continued)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the CentOS Stream 9, and the system compiler gcc 11.5.0

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS settings:

Adjacent cache line prefetcher set to Enabled

Hardware prefetcher set to Enabled

UPI Power Management set to Enabled

Patrol scrub set to Enabled

XPT prefetch set to Auto

Enhanced CPU performance set to Auto

Sub NUMA clustering set to Enabled

Sysinfo program /home/cpu2026/bin/sysinfo

Rev: 779ab21020787073335a329f3a45e2cd

running on localhost Sun Jun 28 17:56:15 2026

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents

- 
1. `uname -srv`
  2. `w`
  3. Username
  4. `ulimit -a`
  5. `sysinfo process ancestry`
  6. `/proc/cpuinfo`
  7. `lscpu`
  8. `numactl --hardware`
  9. `/proc/meminfo`
  10. `who -r`
  11. Systemd service manager version: `systemd 254 (254.10+suse.84.ge8d77af424)`
  12. Services, from `systemctl list-unit-files`
  13. Linux kernel boot-time arguments, from `/proc/cmdline`
  14. `cpupower frequency-info`
  15. `sysctl`
  16. `/sys/kernel/mm/transparent_hugepage`
  17. `/sys/kernel/mm/transparent_hugepage/khugepaged`
  18. OS release

(Continued on next page)



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jun-2026

Hardware Availability: May-2025

Software Availability: Apr-2026

## Platform Notes (Continued)

19. Disk information

20. /sys/devices/virtual/dmi/id

21. dmidecode

22. BIOS

1. uname -srvrn

Linux 6.4.0-150600.21-default #1 SMP PREEMPT\_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09) x86\_64

2. w

17:56:15 up 3 min, 2 users, load average: 1.32, 2.26, 1.05

| USER | TTY  | FROM | LOGIN@ | IDLE   | JCPU  | PCPU  | WHAT  |
|------|------|------|--------|--------|-------|-------|-------|
| root | tty1 | -    | 17:53  | 30.00s | 2.76s | 0.77s | -bash |

3. Username

From environment variable \$USER: root

4. ulimit -a

|                      |                 |           |
|----------------------|-----------------|-----------|
| core file size       | (blocks, -c)    | unlimited |
| data seg size        | (kbytes, -d)    | unlimited |
| scheduling priority  | (-e)            | 0         |
| file size            | (blocks, -f)    | unlimited |
| pending signals      | (-i)            | 4124304   |
| max locked memory    | (kbytes, -l)    | 8192      |
| max memory size      | (kbytes, -m)    | unlimited |
| open files           | (-n)            | 1024      |
| pipe size            | (512 bytes, -p) | 8         |
| POSIX message queues | (bytes, -q)     | 819200    |
| real-time priority   | (-r)            | 0         |
| stack size           | (kbytes, -s)    | unlimited |
| cpu time             | (seconds, -t)   | unlimited |
| max user processes   | (-u)            | 4124304   |
| virtual memory       | (kbytes, -v)    | unlimited |
| file locks           | (-x)            | unlimited |

5. sysinfo process ancestry

/usr/lib/systemd/systemd --switched-root --system --deserialize=42

login -- root

-bash

-bash

runcpu --nobuild --reportable --action validate --define default-platform-flags -c

ic2026.0-graniterapids-cpu2026-1.0.1-speed-20260429.cfg --threads 344 --define cores=344 --tune base,peak

(Continued on next page)



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jun-2026

Hardware Availability: May-2025

Software Availability: Apr-2026

### Platform Notes (Continued)

```
-o all --define intspeedaffinity --define smt-on --define drop_caches intspeed
runccpu --nobuild --reportable --action validate --define default-platform-flags --configfile
ic2026.0-graniterapids-cpu2026-1.0.1-speed-20260429.cfg --threads 344 --define cores=344 --tune base,peak
--output_format all --define intspeedaffinity --define smt-on --define drop_caches --nopower --runmode
speed --tune base:peak --size refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2026.022/templogs/preenv.intspeed.022.0.log --lognum 022.0 --from_runccpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2026
```

#### 6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6787P
vendor_id       : GenuineIntel
cpu family      : 6
model           : 173
stepping        : 1
microcode       : 0x1000380
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores       : 86
siblings        : 172
2 physical ids (chips)
344 processors (hardware threads)
physical id 0: core ids 0-42,64-106
physical id 1: core ids 0-42,64-106
physical id 0: apicids 0-85,128-213
physical id 1: apicids 256-341,384-469
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

#### 7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      46 bits physical, 57 bits virtual
Byte Order:         Little Endian
CPU(s):             344
On-line CPU(s) list: 0-343
Vendor ID:          GenuineIntel
BIOS Vendor ID:     Intel(R) Corporation
Model name:          Intel(R) Xeon(R) 6787P
BIOS Model name:     Intel(R) Xeon(R) 6787P   CPU @ 2.0GHz
BIOS CPU family:     179
CPU family:          6
Model:              173
Thread(s) per core: 2
```

(Continued on next page)



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jun-2026

Hardware Availability: May-2025

Software Availability: Apr-2026

## Platform Notes (Continued)

Core(s) per socket: 86  
Socket(s): 2  
Stepping: 1  
CPU(s) scaling MHz: 21%  
CPU max MHz: 3800.0000  
CPU min MHz: 800.0000  
BogoMIPS: 4000.00

Flags:

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat  
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx  
pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good  
nopl xtopology nonstop\_tsc cpuid aperfmperf tsc\_known\_freq pni  
pclmulqdq dtes64 monitor ds\_cpl vmx smx est tm2 ssse3 sdbg fma cx16  
xtpr pdcm pcid dca sse4\_1 sse4\_2 x2apic movbe popcnt  
tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm  
3dnowprefetch cpuid\_fault epb cat\_l3 cat\_l2 cdp\_l3 intel\_ppin cdp\_l2  
ssbd mba ibrs ibpb stibp ibrs\_enhanced tpr\_shadow flexpriority ept  
vpid ept\_ad fsgsbase tsc\_adjust bmi1 hle avx2 smep bmi2 erms invpcid  
rtm cqm rdt\_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt  
clwb intel\_pt avx512cd sha\_ni avx512bw avx512vl xsaveopt xsavec  
xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total cqm\_mbm\_local  
split\_lock\_detect user\_shstk avx\_vnni avx512\_bf16 wbnoinvd dtherm ida  
arat pln pts hwp hwp\_act\_window hwp\_epp hwp\_pkg\_req vnmi avx512vbmi  
umip pku ospke waitpkg avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni  
avx512\_bitalg tme avx512\_vpopcntdq la57 rdpid bus\_lock\_detect  
cldemote movdiri movdir64b enqcmd fsrm md\_clear serialize tsxldtrk  
pconfig arch\_lbr ibt amx\_bf16 avx512\_fp16 amx\_tile amx\_int8 flush\_l1d  
arch\_capabilities

Virtualization:

VT-x

L1d cache: 8.1 MiB (172 instances)  
L1i cache: 10.8 MiB (172 instances)  
L2 cache: 344 MiB (172 instances)  
L3 cache: 672 MiB (2 instances)

NUMA node(s): 2  
NUMA node0 CPU(s): 0-85,172-257  
NUMA node1 CPU(s): 86-171,258-343

Vulnerability Gather data sampling: Not affected  
Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Mmio stale data: Not affected  
Vulnerability Reg file data sampling: Not affected  
Vulnerability Retbleed: Not affected  
Vulnerability Spec rstack overflow: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization

Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;

(Continued on next page)



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jun-2026

Hardware Availability: May-2025

Software Availability: Apr-2026

## Platform Notes (Continued)

Vulnerability Srbds:  
Vulnerability Tsx async abort:

PBSRB-eIBRS Not affected; BHI BHI\_DIS\_S  
Not affected  
Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS   | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|--------|----------|----------------|
| L1d  | 48K      | 8.1M     | 12   | Data        | 1     | 64     | 1        | 64             |
| L1i  | 64K      | 10.8M    | 16   | Instruction | 1     | 64     | 1        | 64             |
| L2   | 2M       | 344M     | 16   | Unified     | 2     | 2048   | 1        | 64             |
| L3   | 336M     | 672M     | 16   | Unified     | 3     | 344064 | 1        | 64             |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)  
node 0 cpus: 0-85,172-257  
node 0 size: 515130 MB  
node 0 free: 513765 MB  
node 1 cpus: 86-171,258-343  
node 1 size: 515973 MB  
node 1 free: 513703 MB  
node distances:  
node 0 1  
0: 10 21  
1: 21 10

9. /proc/meminfo

MemTotal: 1055849700 kB

10. who -r

run-level 3 Jun 28 17:53

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

| Default Target | Status  |
|----------------|---------|
| multi-user     | running |

12. Services, from systemctl list-unit-files

| STATE           | UNIT FILES                                                                                                                                                                                                                                                                                                                        |
|-----------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| enabled         | YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi<br>issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections<br>nvmmf-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore<br>virtqemud wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny |
| enabled-runtime | systemd-remount-fs                                                                                                                                                                                                                                                                                                                |

(Continued on next page)



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jun-2026

Hardware Availability: May-2025

Software Availability: Apr-2026

### Platform Notes (Continued)

disabled      autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait  
chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info  
firewalld fsidd gpm grub2-once haveged hv\_fcopy\_daemon hv\_kvp\_daemon hv\_vss\_daemon ipmi  
ipmievdev iscsi-init iscsid issue-add-ssh-keys kexec-load ksm kvm\_stat libvirt-guests  
lunmask man-db-create multipathd nfs nfs-blkmap nfs-server nfsserver rpcbind  
rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd\_generate\_opts snmpd snmptrapd  
strongswan strongswan-starter svnserv systemd-boot-check-no-failures systemd-confext  
systemd-network-generator systemd-nspawn@ systemd-sysext systemd-time-wait-sync  
systemd-timesyncd tcsh udisks2 virtinterfaced virtlockd virtlogd virtnetworkd virtnodedevd  
virtnwfilterd virtsecret virtstoraged  
indirect      pcsd systemd-userdbd tftp wickedd

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default  
root=UUID=164e42fe-9eb7-4cc5-9a7d-0451c23f36b9  
splash=silent  
resume=/dev/disk/by-uuid/b4ff94da-d908-4fdb-a575-9f9e7887f2f9  
mitigations=auto  
quiet  
security=apparmor

-----  
14. cpupower frequency-info  
analyzing CPU 234:  
current policy: frequency should be within 800 MHz and 3.80 GHz.  
The governor "powersave" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes  
Active: yes

-----  
15. sysctl  
kernel.numa\_balancing                    1  
kernel.randomize\_va\_space               2  
vm.compaction\_proactiveness             20  
vm.dirty\_background\_bytes               0  
vm.dirty\_background\_ratio               10  
vm.dirty\_bytes                           0  
vm.dirty\_expire\_centisecs               3000  
vm.dirty\_ratio                           20  
vm.dirty\_writeback\_centisecs            500  
vm.dirtytime\_expire\_seconds            43200  
vm.extfrag\_threshold                    500  
vm.min\_unmapped\_ratio                   1  
vm.nr\_hugepages                          0

(Continued on next page)





# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jun-2026

Hardware Availability: May-2025

Software Availability: Apr-2026

### Platform Notes (Continued)

```
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size  2097152
shmem_enabled  always within_size advise [never] deny force
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2026
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p3  xfs   741G   52G   690G   7% /home
```

```
-----
20. /sys/devices/virtual/dmi/id
Vendor:      Cisco Systems Inc
Product:     UCSC-C240-M8SX
Serial:      WZP29209S1T
```

```
-----
21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
```

(Continued on next page)



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

CPU2026 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jun-2026

Hardware Availability: May-2025

Software Availability: Apr-2026

## Platform Notes (Continued)

Memory:

16x 0xCE00 M321R8GA0PB2-CCPPC 64 GB 2 rank 6400

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Cisco Systems, Inc.

BIOS Version: C240M8.4.3.6b.0.0430251037

BIOS Date: 04/30/2025

BIOS Revision: 5.35

## Compiler Version Notes

C | 854.graph500\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2026.0.0 Build 20260331  
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.

C++ | 807.ntest\_s(base, peak) 827.cppcheck\_s(base, peak) 853.ns3\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2026.0.0 Build 20260331  
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.

C++, C | 801.xz\_s(base, peak) 817.flac\_s(base, peak) 821.gcc\_s(base, peak)  
| 823.llvm\_s(base, peak) 829.abc\_s(base, peak) 834.vpr\_s(base, peak)  
| 835.gem5\_s(base, peak) 838.diamond\_s(base, peak)  
| 846.minizinc\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2026.0.0 Build 20260331  
Copyright (C) 1985-2026 Intel Corporation. All rights reserved.



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

**CPU2026 License:** 9019

**Test Sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test Date:** Jun-2026

**Hardware Availability:** May-2025

**Software Availability:** Apr-2026

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Benchmarks using both C and C++:

icpx icx

## Base Portability Flags

834.vpr\_s: -fno-fast-math

## Base Optimization Flags

C benchmarks:

-m64 -std=c18 -Wl,-z,muldefs -Wl,-plugin-opt=-inline-threshold=1500  
-xgraniterapids -mprefer-vector-width=512 -O3 -ffp-model=fast -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-fno-strict-aliasing -fiopenmp -DSPEC\_OPENMP  
-L/usr/local/jemalloc-5.3.0/lib -ljemalloc

C++ benchmarks:

-m64 -std=c++17 -Wl,-z,muldefs -Wl,-plugin-opt=-inline-threshold=1500  
-xgraniterapids -mprefer-vector-width=512 -O3 -ffp-model=fast -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -pthread  
-fiopenmp -DSPEC\_OPENMP -L/usr/local/jemalloc-5.3.0/lib -ljemalloc

Benchmarks using both C and C++:

-m64 -std=c++17 -std=c18 -Wl,-z,muldefs  
-Wl,-plugin-opt=-inline-threshold=1500 -xgraniterapids  
-mprefer-vector-width=512 -O3 -ffp-model=fast -flto -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4 -fno-strict-aliasing -fiopenmp  
-DSPEC\_OPENMP -pthread -L/usr/local/jemalloc-5.3.0/lib -ljemalloc

## Peak Compiler Invocation

C benchmarks:

icx

(Continued on next page)



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

**CPU2026 License:** 9019

**Test Sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test Date:** Jun-2026

**Hardware Availability:** May-2025

**Software Availability:** Apr-2026

## Peak Compiler Invocation (Continued)

C++ benchmarks:

icpx

Benchmarks using both C and C++:

icpx icx

## Peak Portability Flags

834.vpr\_s: -fno-fast-math

## Peak Optimization Flags

C benchmarks:

854.graph500\_s: basepeak = yes

C++ benchmarks:

807.ntest\_s: basepeak = yes

827.cppcheck\_s: basepeak = yes

853.ns3\_s: -m64 -std=c++17 -Wl,-z,muldefs  
-Wl,-plugin-opt=-inline-threshold=1500  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xHost(pass 1)  
-ffp-model=fast -xgraniterapids(pass 2) -flto  
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3  
-mfpmath=sse -funroll-loops -pthread -fiopenmp  
-DSPEC\_OPENMP -L/usr/local/jemalloc-5.3.0/lib -ljemalloc

Benchmarks using both C and C++:

801.xz\_s: -m64 -std=c++17 -std=c18 -Wl,-z,muldefs  
-Wl,-plugin-opt=-inline-threshold=1500  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xHost(pass 1)  
-ffp-model=fast -xgraniterapids(pass 2) -flto  
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3  
-mfpmath=sse -funroll-loops -fno-strict-aliasing -fiopenmp  
-DSPEC\_OPENMP -pthread -L/usr/local/jemalloc-5.3.0/lib  
-ljemalloc

(Continued on next page)



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C240 M8 (Intel Xeon 6787P 2.00 GHz processor)

SPECspeed®2026\_int\_base = 6.08

SPECspeed®2026\_int\_peak = 6.17

**CPU2026 License:** 9019

**Test Sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test Date:** Jun-2026

**Hardware Availability:** May-2025

**Software Availability:** Apr-2026

## Peak Optimization Flags (Continued)

817.flac\_s: Same as 801.xz\_s

821.gcc\_s: -m64 -std=c++17 -std=c18 -Wl, -z, muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profddata(pass 2) -xHost(pass 1)  
-ffp-model=fast -xgraniterapids(pass 2) -flto  
-mprefer-vector-width=512 -qopt-mem-layout-trans=4 -O3  
-mfpmath=sse -funroll-loops -fno-strict-aliasing -fiopenmp  
-DSPEC\_OPENMP -pthread -L/usr/local/jemalloc-5.3.0/lib  
-ljemalloc

823.llvm\_s: Same as 821.gcc\_s

829.abc\_s: basepeak = yes

834.vpr\_s: basepeak = yes

835.gem5\_s: Same as 801.xz\_s

838.diamond\_s: Same as 801.xz\_s

846.minizinc\_s: Same as 801.xz\_s

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2026/results/flags/Intel-ic2026-official-linux64-v1.1.html>

<http://www.spec.org/cpu2026/results/flags/Cisco-Platform-Settings-V2.0-GNR-revP.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2026/results/flags/Intel-ic2026-official-linux64-v1.1.xml>

<http://www.spec.org/cpu2026/results/flags/Cisco-Platform-Settings-V2.0-GNR-revP.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2026 v1.0.1 on 2026-06-28 17:56:14-0400.

Report generated on 2026-07-14 17:23:31 by CPU2026 PDF formatter (unknown).

Originally published on 2026-07-14.