SPEC CPU®2017 Integer Rate Result

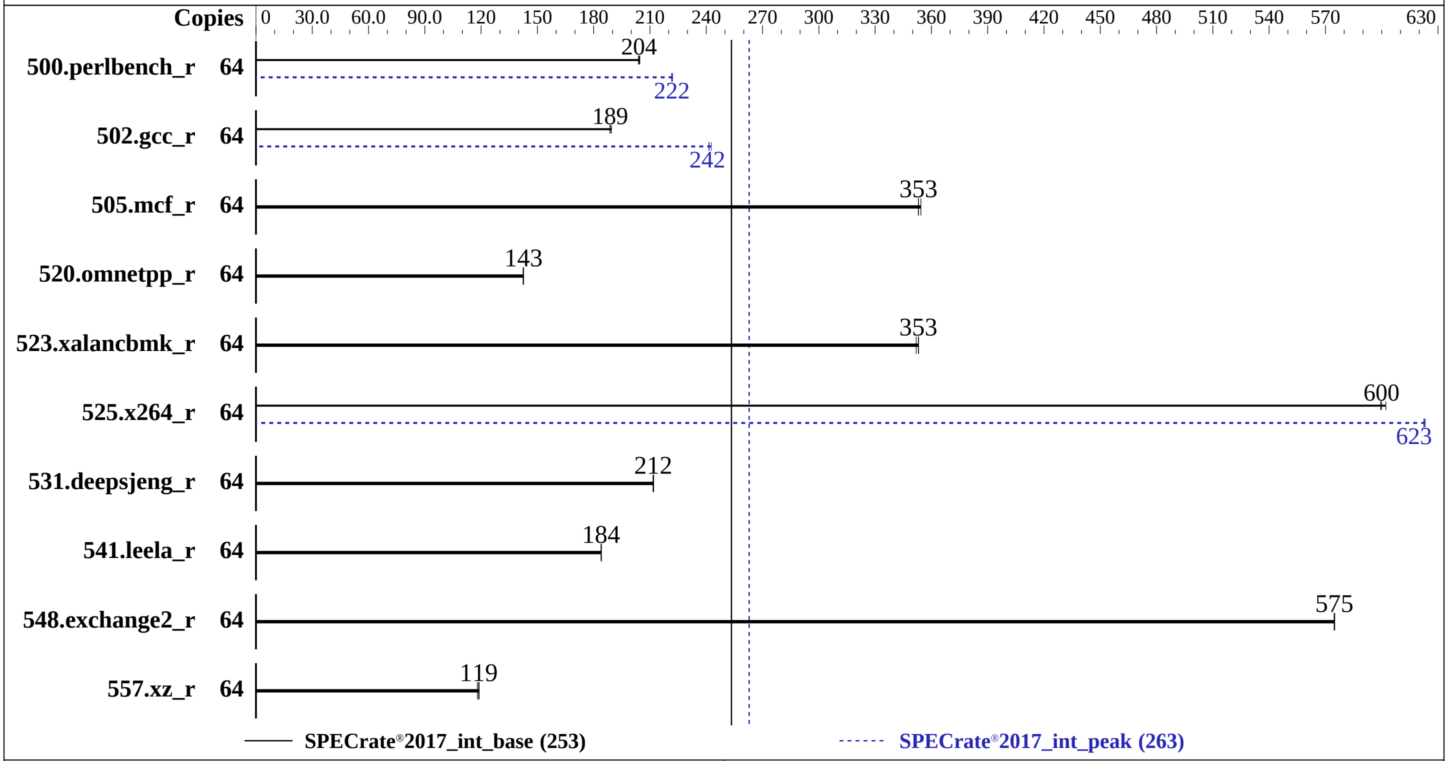
Cisco Systems

Cisco UCS XE130c M8 (Intel Xeon 6543P-B processor)

SPECrate®2017_int_base = 253

SPECrate®2017_int_peak = 263

CPU2017 License:	9019	**Test Date:**	Aug-2026
Test Sponsor:	Cisco Systems	**Hardware Availability:**	Feb-2025
Tested by:	Cisco Systems	**Software Availability:**	Jun-2025



Hardware

CPU Name:	Intel Xeon 6543P-B
Max MHz:	3300
Nominal:	2000
Enabled:	32 cores, 1 chip, 2 threads/core
Orderable:	1 chips
Cache L1:	64 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	128 MB I+D on chip per chip
Other:	None
Memory:	512 GB (8 x 64 GB 2Rx4 PC5-6400B-R, running at 5200)
Storage:	1 x 2.4 TB NVME SSD
Other:	CPU Cooling: Air

Software

OS:	SUSE Linux Enterprise Server 15 SP7 6.4.0-150700.51-default
Compiler:	C/C++: Version 2025.2 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2025.2 of Intel Fortran Compiler for Linux; C/C++: Version 2024.2 of Intel oneAPI DPC++/C++ Compiler for Linux;
Parallel:	No
Firmware:	Version 6.0.2c released May-2026
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	64	498	205	**499**	**204**	500	204	64	459	222	**460**	**222**	460	222
502.gcc_r	64	478	190	**480**	**189**	480	189	64	375	241	373	243	**375**	**242**
505.mcf_r	64	292	354	293	353	**293**	**353**	64	292	354	293	353	**293**	**353**
520.omnetpp_r	64	590	142	589	143	**589**	**143**	64	590	142	589	143	**589**	**143**
523.xalancbmk_r	64	192	352	**191**	**353**	191	353	64	192	352	**191**	**353**	191	353
525.x264_r	64	187	599	186	602	**187**	**600**	64	180	622	180	623	**180**	**623**
531.deepsjeng_r	64	347	212	346	212	**346**	**212**	64	347	212	346	212	**346**	**212**
541.leela_r	64	576	184	**576**	**184**	576	184	64	576	184	**576**	**184**	576	184
548.exchange2_r	64	292	575	**292**	**575**	292	575	64	292	575	**292**	**575**	292	575
557.xz_r	64	**582**	**119**	581	119	586	118	64	**582**	**119**	581	119	586	118

SPECrate®2017_int_base = 253

SPECrate®2017_int_peak = 263

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

General Notes

```
 Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
 memory using Red Hat Enterprise Linux 8.4
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
```

(Continued on next page)

General Notes (Continued)

```
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS settings:
Adjacent cache line prefetcher set to Disabled
DCU Streamer Prefetch set to Disabled
UPI Power Management set to Enabled
XPT prefetch set to Disabled
Patrol scrub set to Enabled

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Sun Aug  9 21:47:45 2026

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
 ------------------------------------------------------------

 ------------------------------------------------------------
 1. uname -a
   Linux localhost 6.4.0-150700.51-default #1 SMP PREEMPT_DYNAMIC Wed Apr 30 21:35:43 UTC 2025 (6930611)
   x86_64 x86_64 x86_64 GNU/Linux

 ------------------------------------------------------------
 2. w
    21:47:45 up 1 day,  7:35,  6 users,  load average: 0.24, 0.11, 0.08
   USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
   root     tty1     -                Sat14    9.00s  5.05s  3.36s -bash
   root     tty2     -                Sat23   22:33m  0.04s  0.04s -bash
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 2061588
  max locked memory       (kbytes, -l) 8192
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 2061588
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize=42
 login -- root
 -bash
 -bash
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=64 -c
   ic2025.2-lin-graniterapids-rate-20250605.cfg --reportable --iterations 3 --define smt-on --define cores=32
   --define physicalfirst --define invoke_with_interleave --define drop_caches --tune all -o all intrate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=64 --configfile
   ic2025.2-lin-graniterapids-rate-20250605.cfg --reportable --iterations 3 --define smt-on --define cores=32
   --define physicalfirst --define invoke_with_interleave --define drop_caches --tune all --output_format all
   --nopower --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : INTEL(R) XEON(R) 6543P-B
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 174
    stepping        : 1
    microcode       : 0x10002b3
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
    cpu cores       : 32
    siblings        : 64
    1 physical ids (chips)
    64 processors (hardware threads)
    physical id 0: core ids 0-31
    physical id 0: apicids 0-63
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
```

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Platform Notes (Continued)

```
7. lscpu

From lscpu from util-linux 2.40.4:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        46 bits physical, 57 bits virtual
  Byte Order:                           Little Endian
  CPU(s):                               64
  On-line CPU(s) list:                  0-63
  Vendor ID:                            GenuineIntel
  Model name:                           INTEL(R) XEON(R) 6543P-B
  CPU family:                           6
  Model:                                174
  Thread(s) per core:                   2
  Core(s) per socket:                   32
  Socket(s):                            1
  Stepping:                             1
  CPU(s) scaling MHz:                   43%
  CPU max MHz:                          3300.0000
  CPU min MHz:                          800.0000
  BogoMIPS:                             4000.00
  Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                        pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                                        pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                                        nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                                        pclmulqdq dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr
                                        pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer
                                        aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb
                                        cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp
                                        ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid
                                        cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
                                        intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
                                        xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                                        split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
                                        arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip
                                        pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
                                        avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                                        cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk
                                        pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d
                                        arch_capabilities
  L1d cache:                            1.5 MiB (32 instances)
  L1i cache:                            2 MiB (32 instances)
  L2 cache:                             64 MiB (32 instances)
  L3 cache:                             128 MiB (1 instance)
  NUMA node(s):                         1
  NUMA node0 CPU(s):                    0-63
  Vulnerability Gather data sampling:   Not affected
  Vulnerability Itlb multihit:          Not affected
  Vulnerability L1tf:                   Not affected
  Vulnerability Mds:                    Not affected
  Vulnerability Meltdown:               Not affected
  Vulnerability Mmio stale data:        Not affected
  Vulnerability Reg file data sampling: Not affected
  Vulnerability Retbleed:               Not affected
  Vulnerability Spec rstack overflow:   Not affected
  Vulnerability Spec store bypass:      Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:             Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:             Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                                        PBRSB-eIBRS Not affected; BHI BHI_DIS_S
  Vulnerability Srbds:                  Not affected
```

(Continued on next page)

Platform Notes (Continued)

```
    Vulnerability Tsx async abort:        Not affected

  From lscpu --cache:
       NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL   SETS PHY-LINE COHERENCY-SIZE
       L1d       48K     1.5M   12 Data            1     64        1             64
       L1i       64K       2M   16 Instruction     1     64        1             64
       L2         2M      64M   16 Unified         2   2048        1             64
       L3       128M     128M   16 Unified         3 131072        1             64

  ------------------------------------------------------------
  8. numactl --hardware
  NOTE: a numactl 'node' might or might not correspond to a physical chip.
    available: 1 nodes (0)
    node 0 cpus: 0-63
    node 0 size: 515424 MB
    node 0 free: 513806 MB
    node distances:
    node   0
      0:  10

  ------------------------------------------------------------
  9. /proc/meminfo
     MemTotal:       527794564 kB

  ------------------------------------------------------------
  10. who -r
    run-level 3 Aug 8 14:12

  ------------------------------------------------------------
  11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
    Default Target  Status
    multi-user      running

  ------------------------------------------------------------
  12. Services, from systemctl list-unit-files
    STATE            UNIT FILES
    enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance
                     iscsi issue-generator kbdsettings klog lvm2-monitor nscd nvmefc-boot-connections
                     nvmf-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore
                     virtqemud wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
    enabled-runtime  systemd-remount-fs
    disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                     chronyd console-getty cups cups-browsed debug-shell dnsmasq ebtables exchange-bmc-os-info
                     firewalld fsidd gpm grub2-once haveged hv_fcopy_daemon hv_fcopy_uio_daemon hv_kvp_daemon
                     hv_vss_daemon ipmi ipmievd iscsi-init iscsid issue-add-ssh-keys kexec-load ksm kvm_stat
                     libvirt-guests lunmask man-db-create multipathd nfs nfs-blkmap nfs-server nfsserver
                     rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts snmpd
                     snmptrapd strongswan strongswan-starter svnserve systemd-boot-check-no-failures
                     systemd-confext systemd-network-generator systemd-nspawn@ systemd-sysext
                     systemd-time-wait-sync systemd-timesyncd udisks2 virtinterfaced virtlockd virtlogd
                     virtnetworkd virtnodedevd virtnwfilterd virtsecretd virtstoraged vncserver@
    indirect         pcscd systemd-userdbd tftp wickedd

  ------------------------------------------------------------
  13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.51-default
    root=UUID=86ab0dee-90cf-4b4c-946a-7d30ab25e77b
    splash=silent
    resume=/dev/disk/by-uuid/5dc9ab87-6c70-4c1c-98ed-839677e40db9
    mitigations=auto
```

(Continued on next page)

Platform Notes (Continued)

```
    quiet
    security=apparmor

  ------------------------------------------------------------
  14. cpupower frequency-info
    analyzing CPU 39:
      current policy: frequency should be within 800 MHz and 3.30 GHz.
                      The governor "performance" may decide which speed to use
                      within this range.
      boost state support:
        Supported: yes
        Active: yes

  ------------------------------------------------------------
  15. sysctl
    kernel.numa_balancing              0
    kernel.randomize_va_space          2
    vm.compaction_proactiveness       20
    vm.dirty_background_bytes          0
    vm.dirty_background_ratio         10
    vm.dirty_bytes                     0
    vm.dirty_expire_centisecs       3000
    vm.dirty_ratio                    20
    vm.dirty_writeback_centisecs     500
    vm.dirtytime_expire_seconds    43200
    vm.extfrag_threshold             500
    vm.min_unmapped_ratio              1
    vm.nr_hugepages                    0
    vm.nr_hugepages_mempolicy          0
    vm.nr_overcommit_hugepages         0
    vm.swappiness                      1
    vm.watermark_boost_factor      15000
    vm.watermark_scale_factor         10
    vm.zone_reclaim_mode               0

  ------------------------------------------------------------
  16. /sys/kernel/mm/transparent_hugepage
    defrag          always defer defer+madvise [madvise] never
    enabled         [always] madvise never
    hpage_pmd_size  2097152
    shmem_enabled   always within_size advise [never] deny force

  ------------------------------------------------------------
  17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs   60000
    defrag                      1
    max_ptes_none             511
    max_ptes_shared           256
    max_ptes_swap              64
    pages_to_scan            4096
    scan_sleep_millisecs    10000

  ------------------------------------------------------------
  18. OS release
    From /etc/*-release /etc/*-version
    os-release SUSE Linux Enterprise Server 15 SP7

  ------------------------------------------------------------
  19. Disk information
  SPEC is set to: /home/cpu2017
```

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Platform Notes (Continued)

```
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/nvme0n1p3 xfs   2.4T  110G  2.3T   5% /home

 ------------------------------------------------------------
 20. /sys/devices/virtual/dmi/id
     Vendor:         Cisco Systems Inc
     Product:        UCSXE-130C-M8-32
     Product Family: Cisco Edge Server
     Serial:         WZP301390T5

 ------------------------------------------------------------
 21. dmidecode
   Additional information from dmidecode 3.6 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     8x 0xCE00 M321R8GA0PB2-CCPKC 64 GB 2 rank 6400, configured at 5200


 ------------------------------------------------------------
 22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Cisco Systems, Inc.
    BIOS Version:      XE1X0M8.6.0.2c.0.0526261754
    BIOS Date:         05/26/2026
    BIOS Revision:     5.35
```

Compiler Version Notes

```
==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.2.1 Build 20240711
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.2.1 Build 20240711
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------
```

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Compiler Version Notes (Continued)

```
==============================================================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
        | 541.leela_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran | 548.exchange2_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2025.2.0 Build 20250605
Copyright (C) 1985-2025 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifx`

Base Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
`-w -std=c11 -m64 -Wl,-z,muldefs -xgraniterapids -O3 -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`

(Continued on next page)

Base Optimization Flags (Continued)

C benchmarks (continued):
-L/home/specdev/intel-compilers/compiler/2025.2/lib -lqkmalloc

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xgraniterapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fdelayed-template-parsing
-L/home/specdev/intel-compilers/compiler/2025.2/lib -lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xgraniterapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/intel-compilers/compiler/2025.2/lib -lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icx

502.gcc_r: icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow -fno-strict-aliasing
-L/home/specdev/intel-compilers/compiler/2025.2/lib
-lqkmalloc

502.gcc_r: -m32 -L/home/specdev/intel-compilers/compiler/2024.2/lib32
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xgraniterapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/home/specdev/intel-compilers/compiler/2025.2/lib
-lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic2025-official-linux64.html
http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-V3.0-GNR-revA.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2025-official-linux64.xml
http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-V3.0-GNR-revA.xml

Tested with SPEC CPU®2017 v1.1.9 on 2026-08-09 21:47:45-0400.
Report generated on 2026-09-23 13:10:39 by CPU2017 PS formatter v6716.
Originally published on 2026-09-22.