SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

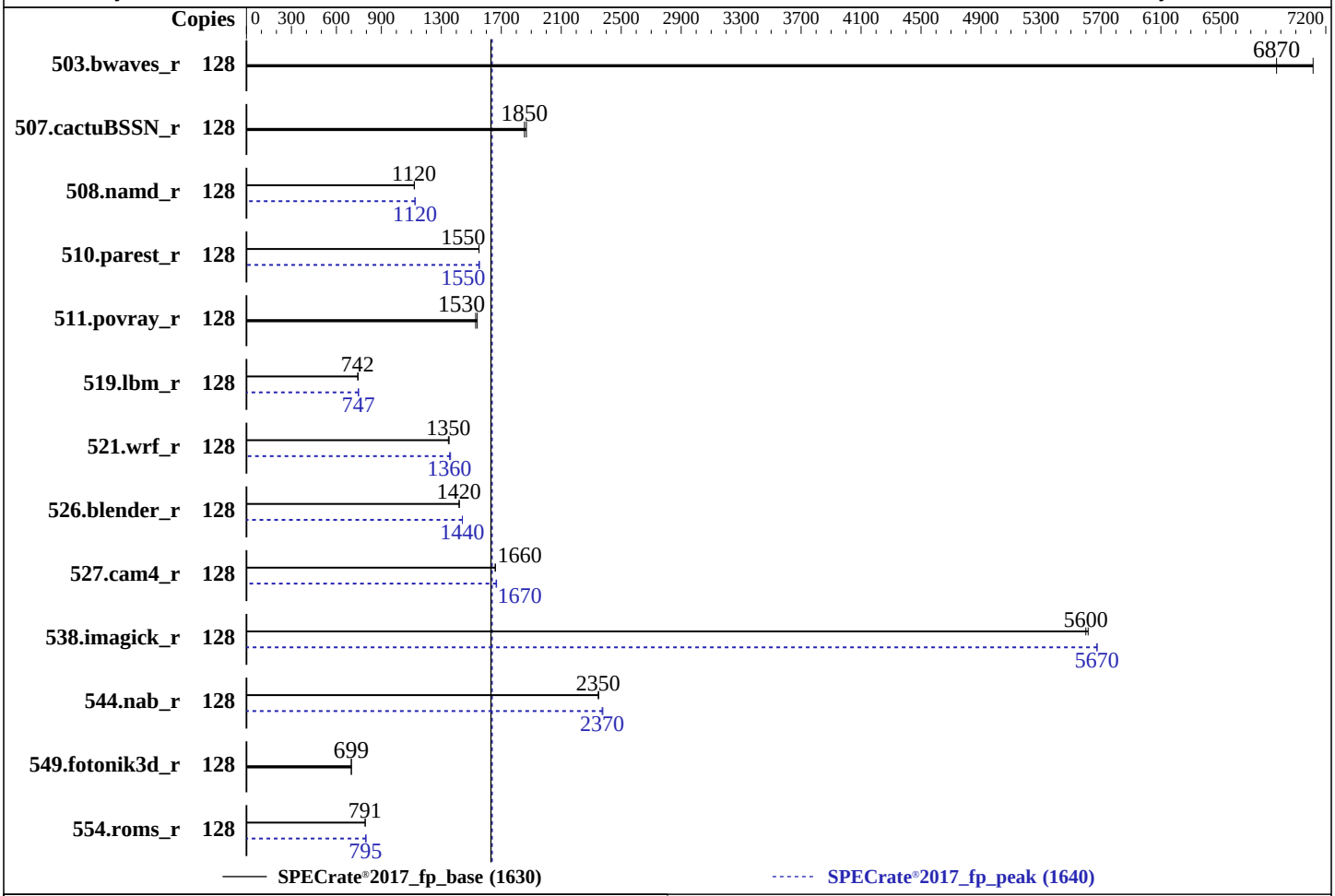
PowerEdge R7725 (AMD EPYC 9535 64-Core Processor)

SPECrate®2017_fp_base = 1630

SPECrate®2017_fp_peak = 1640

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2025
Hardware Availability: Nov-2025
Software Availability: Oct-2025



Hardware

CPU Name:	AMD EPYC 9535
Max MHz:	4300
Nominal:	2400
Enabled:	128 cores, 2 chips
Orderable:	1,2 chips
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	256 MB I+D on chip per chip, 16 MB shared / 4 cores
Other:	None
Memory:	1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)
Storage:	80 GB on tmpfs
Other:	CPU Cooling: Air

Software

OS:	Ubuntu 24.04 LTS 6.8.0-87-generic
Compiler:	C/C++/Fortran: Version 5.0.0 of AOCC
Parallel:	No
Firmware:	Version 1.3.3 released Jun-2025
File System:	tmpfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	None
Power Management:	BIOS set to prefer performance at the cost of additional power usage.

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	180	7120	**187**	**6870**			128	180	7120	**187**	**6870**		
507.cactuBSSN_r	128	**87.4**	**1850**	86.8	1870			128	**87.4**	**1850**	86.8	1870		
508.namd_r	128	**109**	**1120**	109	1120			128	108	1120	**108**	**1120**		
510.parest_r	128	216	1550	**216**	**1550**			128	**216**	**1550**	215	1550		
511.povray_r	128	**195**	**1530**	194	1540			128	**195**	**1530**	194	1540		
519.lbm_r	128	181	745	**182**	**742**			128	180	747	**181**	**747**		
521.wrf_r	128	212	1350	**213**	**1350**			128	**212**	**1360**	211	1360		
526.blender_r	128	137	1420	**137**	**1420**			128	135	1440	**135**	**1440**		
527.cam4_r	128	**135**	**1660**	135	1660			128	**134**	**1670**	134	1670		
538.imagick_r	128	56.7	5610	**56.9**	**5600**			128	56.1	5680	**56.1**	**5670**		
544.nab_r	128	91.7	2350	**91.8**	**2350**			128	90.6	2380	**90.8**	**2370**		
549.fotonik3d_r	128	**713**	**699**	713	700			128	**713**	**699**	713	700		
554.roms_r	128	**257**	**791**	257	793			128	255	798	**256**	**795**		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
```

(Continued on next page)

Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4/amd_rate_aocc500_znver5_A_lib/lib:/mnt/ramdisk/cpu2017
     -1.1.9-aocc500-znerv5_A1.4/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

 Benchmark run from a 80 GB ramdisk created with the cmd: "mount -t tmpfs -o size=80G tmpfs /mnt/ramdisk"
```

Platform Notes

```
 BIOS Settings:
                 Logical Processor : Disabled
          Virtualization Technology : Disabled
              NUMA Nodes Per Socket : 4

                      System Profile : Custom
                             C-States : Disabled
                Memory Patrol Scrub : Disabled
  PCI ASPM L1 Link Power Management : Disabled
    Periodic Directory Rinse Tuning : Blended
                Determinism Control : Manual
                 Determinism Slider : Power Determinism
                     Optimizer Mode : Enabled
                Adaptive Allocation : Enabled
                 Dram Refresh Delay : Performance
                DIMM Self Healing -
      on Uncorrectable Memory Error : Disabled

 Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on SLR7736-R7725 Sat Nov 15 17:01:06 2025

 SUT (System Under Test) info as seen by some common utilities.

 ----------------------------------------------------------
 Table of contents
 ----------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
```

(Continued on next page)

Platform Notes (Continued)

```
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.8)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux SLR7736-R7725 6.8.0-87-generic #88-Ubuntu SMP PREEMPT_DYNAMIC Sat Oct 11 09:28:41 UTC 2025 x86_64
  x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   17:01:06 up 3 min,  1 user,  load average: 0.26, 0.06, 0.02
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                16:58   33.00s  1.67s  0.43s /bin/bash ./amd_rate_aocc500_znver5_A1.sh

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  time(seconds)        unlimited
  file(blocks)         unlimited
  data(kbytes)         unlimited
  stack(kbytes)        unlimited
  coredump(blocks)     0
  memory(kbytes)       unlimited
  locked memory(kbytes) 2097152
  process              6189322
  nofiles              1024
  vmemory(kbytes)      unlimited
  locks                unlimited
  rtprio               0

------------------------------------------------------------
5. sysinfo process ancestry
 /sbin/init
 /bin/login -p --
 -bash
 /bin/bash /home/DellFiles/bin/DELL_rate.sh
 /bin/bash /home/DellFiles/bin/dell-run-main.sh rate
 /bin/bash /home/DellFiles/bin/dell-run-main.sh rate
 /bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-VERS=6.4_T17 --output_format
   html,pdf,txt
 python3 ./run_amd_rate_aocc500_znver5_A1.py
 /bin/bash ./amd_rate_aocc500_znver5_A1.sh
 runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
```

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Platform Notes (Continued)

```
   DL-VERS=6.4_T17 --output_format html,pdf,txt fprate
 runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
   DL-VERS=6.4_T17 --output_format html,pdf,txt --nopower --runmode rate --tune base:peak --size
   test:train:refrate fprate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : AMD EPYC 9535 64-Core Processor
    vendor_id       : AuthenticAMD
    cpu family      : 26
    model           : 2
    stepping        : 1
    microcode       : 0xb002151
    bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
    TLB size        : 192 4K pages
    cpu cores       : 64
    siblings        : 64
    2 physical ids (chips)
    128 processors (hardware threads)
    physical id 0: core ids
    0-3,16-19,32-35,48-51,64-67,80-83,96-99,112-115,128-131,144-147,160-163,176-179,192-195,208-211,224-227,2
    40-243
    physical id 1: core ids
    0-3,16-19,32-35,48-51,64-67,80-83,96-99,112-115,128-131,144-147,160-163,176-179,192-195,208-211,224-227,2
    40-243
    physical id 0: apicids
    0-3,16-19,32-35,48-51,64-67,80-83,96-99,112-115,128-131,144-147,160-163,176-179,192-195,208-211,224-227,2
    40-243
    physical id 1: apicids
    256-259,272-275,288-291,304-307,320-323,336-339,352-355,368-371,384-387,400-403,416-419,432-435,448-451,4
    64-467,480-483,496-499
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.39.3:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        52 bits physical, 57 bits virtual
  Byte Order:                           Little Endian
  CPU(s):                               128
  On-line CPU(s) list:                  0-127
  Vendor ID:                            AuthenticAMD
  BIOS Vendor ID:                       AMD
  Model name:                           AMD EPYC 9535 64-Core Processor
  BIOS Model name:                      AMD EPYC 9535 64-Core Processor                 CPU @ 2.4GHz
  BIOS CPU family:                      107
  CPU family:                           26
  Model:                                2
  Thread(s) per core:                   1
  Core(s) per socket:                   64
  Socket(s):                            2
  Stepping:                             1
  Frequency boost:                      enabled
  CPU(s) scaling MHz:                   64%
```

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Platform Notes (Continued)

```
   CPU max MHz:                          2400.0000
   CPU min MHz:                          1500.0000
   BogoMIPS:                             4793.59
   Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                         pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                                         rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                                         extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                                         sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                                         cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                                         osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                                         perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                                         ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
                                         smep bmi2 invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                                         avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                                         xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                         cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                                         xsaveerptr rdpru wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv
                                         svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
                                         pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl
                                         vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
                                         avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
                                         movdiri movdir64b overflow_recov succor smca avx512_vp2intersect
                                         flush_l1d debug_swap
   L1d cache:                            6 MiB (128 instances)
   L1i cache:                            4 MiB (128 instances)
   L2 cache:                             128 MiB (128 instances)
   L3 cache:                             512 MiB (32 instances)
   NUMA node(s):                         8
   NUMA node0 CPU(s):                    0-15
   NUMA node1 CPU(s):                    16-31
   NUMA node2 CPU(s):                    32-47
   NUMA node3 CPU(s):                    48-63
   NUMA node4 CPU(s):                    64-79
   NUMA node5 CPU(s):                    80-95
   NUMA node6 CPU(s):                    96-111
   NUMA node7 CPU(s):                    112-127
   Vulnerability Gather data sampling:   Not affected
   Vulnerability Itlb multihit:          Not affected
   Vulnerability L1tf:                   Not affected
   Vulnerability Mds:                    Not affected
   Vulnerability Meltdown:               Not affected
   Vulnerability Mmio stale data:        Not affected
   Vulnerability Reg file data sampling: Not affected
   Vulnerability Retbleed:               Not affected
   Vulnerability Spec rstack overflow:   Not affected
   Vulnerability Spec store bypass:      Mitigation; Speculative Store Bypass disabled via prctl
   Vulnerability Spectre v1:             Mitigation; usercopy/swapgs barriers and __user pointer sanitization
   Vulnerability Spectre v2:             Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                                         disabled; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
   Vulnerability Srbds:                  Not affected
   Vulnerability Tsx async abort:        Not affected
   Vulnerability Vmscape:                Not affected

 From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
      L1d       48K       6M   12 Data            1    64        1             64
      L1i       32K       4M    8 Instruction     1    64        1             64
      L2         1M     128M   16 Unified         2  1024        1             64
      L3        16M     512M   16 Unified         3 16384        1             64
```

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Platform Notes (Continued)

```
------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 8 nodes (0-7)
  node 0 cpus: 0-15
  node 0 size: 192817 MB
  node 0 free: 187552 MB
  node 1 cpus: 16-31
  node 1 size: 193526 MB
  node 1 free: 193193 MB
  node 2 cpus: 32-47
  node 2 size: 193526 MB
  node 2 free: 193256 MB
  node 3 cpus: 48-63
  node 3 size: 193467 MB
  node 3 free: 193165 MB
  node 4 cpus: 64-79
  node 4 size: 193526 MB
  node 4 free: 193230 MB
  node 5 cpus: 80-95
  node 5 size: 193526 MB
  node 5 free: 193205 MB
  node 6 cpus: 96-111
  node 6 size: 193526 MB
  node 6 free: 192770 MB
  node 7 cpus: 112-127
  node 7 size: 193491 MB
  node 7 free: 193137 MB
  node distances:
  node   0   1   2   3   4   5   6   7
    0:  10  12  12  12  32  32  32  32
    1:  12  10  12  12  32  32  32  32
    2:  12  12  10  12  32  32  32  32
    3:  12  12  12  10  32  32  32  32
    4:  32  32  32  32  10  12  12  12
    5:  32  32  32  32  12  10  12  12
    6:  32  32  32  32  12  12  10  12
    7:  32  32  32  32  12  12  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1584545536 kB

------------------------------------------------------------
10. who -r
  run-level 3 Nov 15 16:58

------------------------------------------------------------
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.8)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          apparmor apport blk-availability cloud-config cloud-final cloud-init cloud-init-local
                   console-setup cron e2scrub_reap finalrd getty@ gpu-manager grub-common
                   grub-initrd-fallback keyboard-setup lvm2-monitor multipathd networkd-dispatcher open-iscsi
                   pollinate secureboot-db setvtrgb snapd systemd-networkd systemd-networkd-wait-online
                   systemd-pstore systemd-resolved systemd-timesyncd thermald unattended-upgrades
```

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Platform Notes (Continued)

```
  enabled-runtime  netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
  disabled         console-getty debug-shell iscsid serial-getty@ ssh systemd-boot-check-no-failures
                   systemd-confext systemd-network-generator systemd-networkd-wait-online@
                   systemd-pcrlock-file-system systemd-pcrlock-firmware-code systemd-pcrlock-firmware-config
                   systemd-pcrlock-machine-id systemd-pcrlock-make-policy
                   systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
                   systemd-time-wait-sync upower
  indirect         systemd-sysupdate systemd-sysupdate-reboot
  masked           cryptdisks cryptdisks-early hwclock multipath-tools-boot sudo x11-common

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.8.0-87-generic
  root=UUID=a7565640-059d-4a86-afc8-58cd1cadb778
  ro

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 16:
    current policy: frequency should be within 1.50 GHz and 2.40 GHz.
                    The governor "schedutil" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes
      Boost States: 0
      Total States: 3
      Pstate-P0:  2400MHz

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           0
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                      8
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                       1
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                1

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          [always] defer defer+madvise madvise never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
```

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Platform Notes (Continued)

```
   alloc_sleep_millisecs   60000
   defrag                      1
   max_ptes_none             511
   max_ptes_shared           256
   max_ptes_swap              64
   pages_to_scan            4096
   scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release Ubuntu 24.04 LTS

------------------------------------------------------------
19. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4
  Filesystem     Type   Size  Used Avail Use% Mounted on
  tmpfs          tmpfs   80G  3.3G   77G   5% /mnt/ramdisk

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         Dell Inc.
    Product:        PowerEdge R7725
    Product Family: PowerEdge
    Serial:         SLR7736

------------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    24x 802C0000802C MTC40F2046S1RC64BD2 64 GB 2 rank 6400


------------------------------------------------------------
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       Dell Inc.
   BIOS Version:      1.3.3
   BIOS Date:         06/26/2025
   BIOS Revision:     1.3
```

Compiler Version Notes

```
==============================================================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
```

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Compiler Version Notes (Continued)

```
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------

==============================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------

==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------

==============================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------

==============================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50

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Base Optimization Flags (Continued)

C benchmarks (continued):
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

C++ benchmarks:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl

Benchmarks using both Fortran and C:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

Benchmarks using both C and C++:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100

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Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):
`-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang -ldl`

Benchmarks using Fortran, C, and C++:
`-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6 -Wl,-mllvm -Wl,-reduce-array-computations=3 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner -O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto -fstruct-layout=7 -mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -lflang -ldl`

Base Other Flags

C benchmarks:
`-Wno-unused-command-line-argument`

C++ benchmarks:
`-Wno-unused-command-line-argument`

Fortran benchmarks:
`-Wno-unused-command-line-argument`

Benchmarks using both Fortran and C:
`-Wno-unused-command-line-argument`

Benchmarks using both C and C++:
`-Wno-unused-command-line-argument`

Benchmarks using Fortran, C, and C++:
`-Wno-unused-command-line-argument`

Peak Compiler Invocation

C benchmarks:
`clang`

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Peak Compiler Invocation (Continued)

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

538.imagick_r: Same as 519.lbm_r

544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

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Peak Optimization Flags (Continued)

C++ benchmarks:

508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

510.parest_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm
-lamdalloc -ldl -lflang

Benchmarks using both Fortran and C:

521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang

527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3

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Peak Optimization Flags (Continued)

527.cam4_r (continued):
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang

Benchmarks using both C and C++:

511.povray_r: basepeak = yes

526.blender_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -ldl

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

Peak Other Flags

C benchmarks:
-Wno-unused-command-line-argument

C++ benchmarks:
-Wno-unused-command-line-argument

Fortran benchmarks:
-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:
-Wno-unused-command-line-argument

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Peak Other Flags (Continued)

Benchmarks using both C and C++:
`-Wno-unused-command-line-argument`

Benchmarks using Fortran, C, and C++:
`-Wno-unused-command-line-argument`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml`

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