SPEC CPU®2017 Integer Rate Result

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Dell Inc.

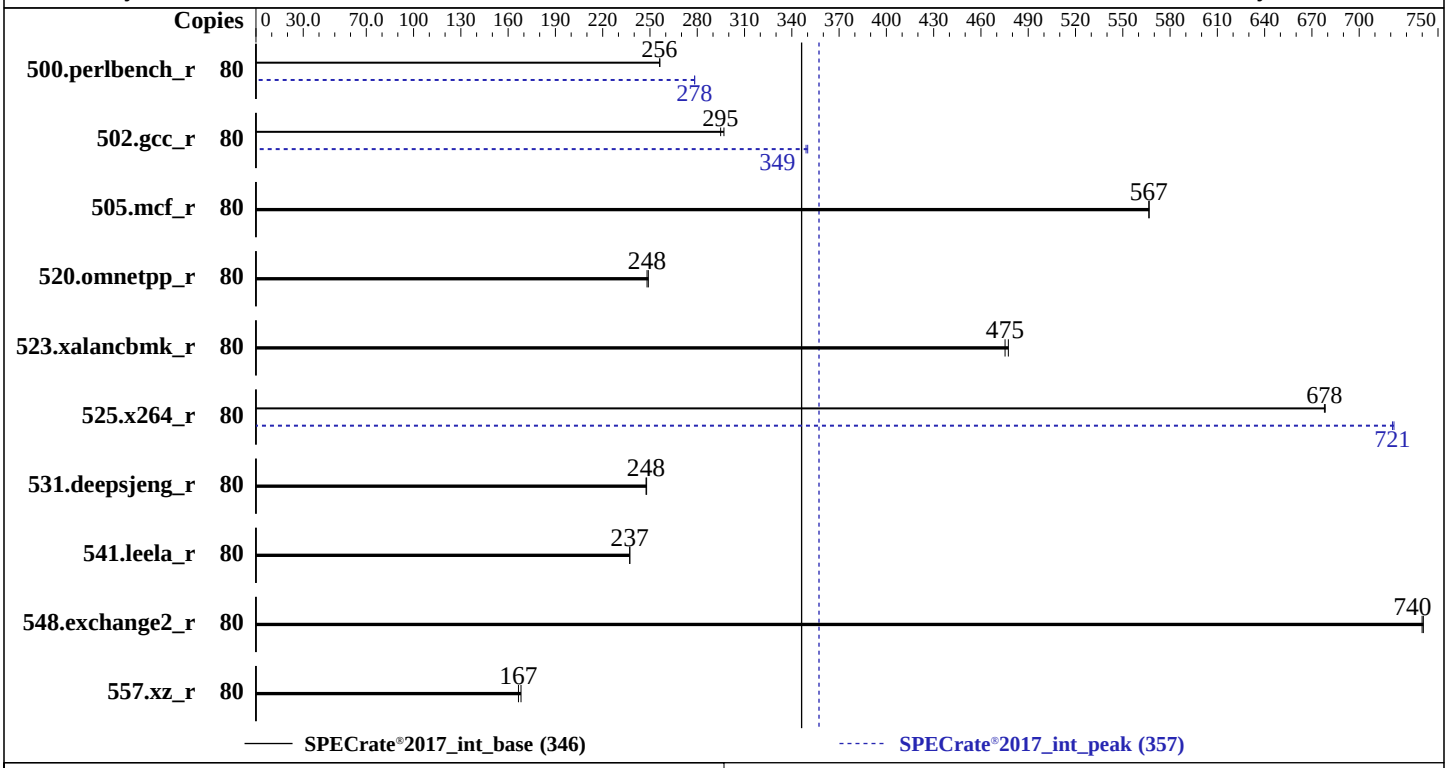
PowerEdge T560 (Intel Xeon Silver 4416+)

SPECrate®2017_int_base = 346

SPECrate®2017_int_peak = 357

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Oct-2025
Hardware Availability: Mar-2025
Software Availability: Mar-2024



Hardware

CPU Name:	Intel Xeon Silver 4416+
Max MHz:	3900
Nominal:	2000
Enabled:	40 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	37.5 MB I+D on chip per chip
Other:	None
Memory:	512 GB (16 x 32 GB 2Rx8 PC5-5600B-R, running at 4000)
Storage:	70 GB on tmpfs
Other:	CPU Cooling: Air

Software

OS:	Red Hat Enterprise Linux 9.2 (Plow) 5.14.0-284.11.1.el9_2.x86_64
Compiler:	C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel:	No
Firmware:	Version 2.7.5 released Jul-2025
File System:	tmpfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage.

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	80	**497**	**256**	497	256			80	458	278	**458**	**278**		
502.gcc_r	80	**384**	**295**	381	297			80	**325**	**349**	324	350		
505.mcf_r	80	228	567	**228**	**567**			80	228	567	**228**	**567**		
520.omnetpp_r	80	**423**	**248**	422	249			80	**423**	**248**	422	249		
523.xalancbmk_r	80	177	477	**178**	**475**			80	177	477	**178**	**475**		
525.x264_r	80	**207**	**678**	206	678			80	194	722	**194**	**721**		
531.deepsjeng_r	80	370	248	**370**	**248**			80	370	248	**370**	**248**		
541.leela_r	80	**559**	**237**	559	237			80	**559**	**237**	559	237		
548.exchange2_r	80	283	741	**283**	**740**			80	283	741	**283**	**740**		
557.xz_r	80	514	168	**519**	**167**			80	514	168	**519**	**167**		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/lib/ia32:/mnt/ram
     disk/cpu2017-1.1.9-ic2024.1/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

(Continued on next page)

General Notes (Continued)

```
Benchmark run from a 70 GB ramdisk created with the cmd: "mount -t tmpfs -o size=70G tmpfs /mnt/ramdisk"
```

Platform Notes

```
BIOS Settings:
         Virtualization Technology : Disabled
           DCU Streamer Prefetcher : Disabled
                  Sub NUMA Cluster : 2-Way Clustering
       UMA Based Clustering Status : Disable
              Dead Line LLC Alloc : Disabled
                       x2APIC Mode : Disabled

                     System Profile : Custom
              CPU Power Management : Maximum Performance
                               C1E : Disabled
                           C-States : Autonomous
              Memory Patrol Scrub : Disabled
           Energy Efficient Policy : Performance
            CPU Interconnect Bus -
             Link Power Management : Disabled
 PCI ASPM L1 Link Power Management : Disabled
              DIMM Self Healing -
     on Uncorrectable Memory Error : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2024.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on TH03503-T560 Tue Oct 28 11:55:45 2025

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-13.el9_2)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
------------------------------------------------------------
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
1. uname -a
  Linux TH03503-T560 5.14.0-284.11.1.el9_2.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 12 10:45:03 EDT 2023 x86_64
  x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   11:55:45 up 2 min,  1 user,  load average: 0.40, 0.16, 0.06
  USER     TTY        LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1      11:54   60.00s  0.89s  0.00s /bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh
  rate --define DL-VERS=6.3b --output_format html,pdf,txt

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  real-time non-blocking time  (microseconds, -R) unlimited
  core file size              (blocks, -c) 0
  data seg size               (kbytes, -d) unlimited
  scheduling priority                 (-e) 0
  file size                   (blocks, -f) unlimited
  pending signals                     (-i) 2059644
  max locked memory           (kbytes, -l) 64
  max memory size             (kbytes, -m) unlimited
  open files                          (-n) 1024
  pipe size                (512 bytes, -p) 8
  POSIX message queues         (bytes, -q) 819200
  real-time priority                  (-r) 0
  stack size                  (kbytes, -s) unlimited
  cpu time                   (seconds, -t) unlimited
  max user processes                  (-u) 2059644
  virtual memory              (kbytes, -v) unlimited
  file locks                          (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
 login -- root
 -bash
 /bin/bash /home/DellFiles/bin/DELL_rate.sh
 /bin/bash /home/DellFiles/bin/dell-run-main.sh rate
 /bin/bash /home/DellFiles/bin/dell-run-main.sh rate
 /bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.3b --output_format
   html,pdf,txt
 /bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-VERS=6.3b --output_format
   html,pdf,txt
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=80 -c
   ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=40 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base,peak -o all --iterations 2 --define
   DL-VERS=6.3b --output_format html,pdf,txt intrate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=80 --configfile
   ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=40 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --iterations 2
   --define DL-VERS=6.3b --output_format html,pdf,txt --nopower --runmode rate --tune base:peak --size
   refrate intrate --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log
   --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2024.1
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) Silver 4416+
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 143
    stepping        : 8
    microcode       : 0x2b000643
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
    cpu cores       : 20
    siblings        : 40
    2 physical ids (chips)
    80 processors (hardware threads)
    physical id 0: core ids 0-19
    physical id 1: core ids 0-19
    physical id 0: apicids 0-39
    physical id 1: apicids 128-167
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   46 bits physical, 57 bits virtual
  Byte Order:                      Little Endian
  CPU(s):                          80
  On-line CPU(s) list:             0-79
  Vendor ID:                       GenuineIntel
  BIOS Vendor ID:                  Intel
  Model name:                      Intel(R) Xeon(R) Silver 4416+
  BIOS Model name:                 Intel(R) Xeon(R) Silver 4416+
  CPU family:                      6
  Model:                           143
  Thread(s) per core:              2
  Core(s) per socket:              20
  Socket(s):                       2
  Stepping:                        8
  CPU max MHz:                     3900.0000
  CPU min MHz:                     800.0000
  BogoMIPS:                        4000.00
  Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                   clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                   lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                   nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                   ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                                   x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                                   abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                                   cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1
                                   avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                                   avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                                   xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                   cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                                   arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                                   vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                                   bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
                                   tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8
```

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Platform Notes (Continued)

```
                                 flush_l1d arch_capabilities
  L1d cache:                     1.9 MiB (40 instances)
  L1i cache:                     1.3 MiB (40 instances)
  L2 cache:                      80 MiB (40 instances)
  L3 cache:                      75 MiB (2 instances)
  NUMA node(s):                  4
  NUMA node0 CPU(s):             0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76
  NUMA node1 CPU(s):             2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78
  NUMA node2 CPU(s):             1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77
  NUMA node3 CPU(s):             3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71,75,79
  Vulnerability Itlb multihit:   Not affected
  Vulnerability L1tf:            Not affected
  Vulnerability Mds:             Not affected
  Vulnerability Meltdown:        Not affected
  Vulnerability Mmio stale data: Not affected
  Vulnerability Retbleed:        Not affected
  Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:      Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:      Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
                                 sequence
  Vulnerability Srbds:           Not affected
  Vulnerability Tsx async abort: Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     1.9M   12 Data            1    64        1             64
     L1i       32K     1.3M    8 Instruction     1    64        1             64
     L2         2M      80M   16 Unified         2  2048        1             64
     L3      37.5M      75M   15 Unified         3 40960        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76
  node 0 size: 127920 MB
  node 0 free: 117120 MB
  node 1 cpus: 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78
  node 1 size: 129019 MB
  node 1 free: 128523 MB
  node 2 cpus: 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77
  node 2 size: 129019 MB
  node 2 free: 128465 MB
  node 3 cpus: 3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71,75,79
  node 3 size: 129014 MB
  node 3 free: 128484 MB
  node distances:
  node     0    1    2    3
     0:   10   12   21   21
     1:   12   10   21   21
     2:   21   21   10   12
     3:   21   21   12   10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       527333448 kB

------------------------------------------------------------
10. who -r
  run-level 3 Oct 28 11:53
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
11. Systemd service manager version: systemd 252 (252-13.el9_2)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
                   accounts-daemon atd auditd avahi-daemon bluetooth chronyd crond cups dbus-broker firewalld
                   gdm getty@ insights-client-boot irqbalance iscsi iscsi-onboot kdump libstoragemgmt
                   low-memory-monitor lvm2-monitor mcelog mdmonitor microcode multipathd nis-domainname
                   nvmefc-boot-connections ostree-remount power-profiles-daemon qemu-guest-agent rhsmcertd
                   rsyslog rtkit-daemon selinux-autorelabel-mark smartd sshd sssd switcheroo-control
                   systemd-boot-update systemd-network-generator tuned udisks2 upower vgauthd vmtoolsd
  enabled-runtime  systemd-remount-fs
  disabled         arp-ethers blk-availability brltty canberra-system-bootup canberra-system-shutdown
                   canberra-system-shutdown-reboot chrony-wait cni-dhcp console-getty cpupower cups-browsed
                   dbus-daemon debug-shell dnf-system-upgrade dnsmasq iprdump iprinit iprupdate iscsid
                   iscsiuio kpatch kvm_stat ledmon man-db-restart-cache-update nftables nvmf-autoconnect
                   ostree-readonly-sysroot-migration podman podman-auto-update podman-clean-transient
                   podman-kube@ podman-restart psacct ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts
                   rpmdb-rebuild selinux-check-proper-disable serial-getty@ speech-dispatcherd sshd-keygen@
                   systemd-boot-check-no-failures systemd-pstore systemd-sysext wpa_supplicant
  indirect         spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
                   systemd-sysupdate systemd-sysupdate-reboot

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-284.11.1.el9_2.x86_64
  root=/dev/mapper/rhel-root
  ro
  crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
  resume=/dev/mapper/rhel-swap
  rd.lvm.lv=rhel/root
  rd.lvm.lv=rhel/swap
  rhgb
  quiet

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 0:
    current policy: frequency should be within 3.90 GHz and 3.90 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. tuned-adm active
  Current active profile: throughput-performance

------------------------------------------------------------
16. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
```

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Platform Notes (Continued)

```
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     40
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      10
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
18. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
19. OS release
  From /etc/*-release /etc/*-version
  os-release     Red Hat Enterprise Linux 9.2 (Plow)
  redhat-release Red Hat Enterprise Linux release 9.2 (Plow)
  system-release Red Hat Enterprise Linux release 9.2 (Plow)

------------------------------------------------------------
20. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2024.1
  Filesystem     Type   Size  Used Avail Use% Mounted on
  tmpfs          tmpfs   70G  5.0G   66G   8% /mnt/ramdisk

------------------------------------------------------------
21. /sys/devices/virtual/dmi/id
    Vendor:         Dell Inc.
    Product:        PowerEdge T560
    Product Family: PowerEdge
    Serial:         TH03503

------------------------------------------------------------
22. dmidecode
  Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
```

(Continued on next page)

Platform Notes (Continued)

```
     16x 002C0632002C MTC20F2085S1RC56BG1 32 GB 2 rank 5600, configured at 4000


 ------------------------------------------------------------
 23. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Dell Inc.
    BIOS Version:      2.7.5
    BIOS Date:         07/31/2025
    BIOS Revision:     2.7
```

Compiler Version Notes

```
==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 502.gcc_r(peak)
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
        | 541.leela_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran | 548.exchange2_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

(Continued on next page)

Peak Optimization Flags (Continued)

505.mcf_r: `basepeak = yes`

525.x264_r: `-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fno-alias -L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc`

557.xz_r: `basepeak = yes`

C++ benchmarks:

520.omnetpp_r: `basepeak = yes`

523.xalancbmk_r: `basepeak = yes`

531.deepsjeng_r: `basepeak = yes`

541.leela_r: `basepeak = yes`

Fortran benchmarks:

548.exchange2_r: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.17.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.17.xml`

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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