SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

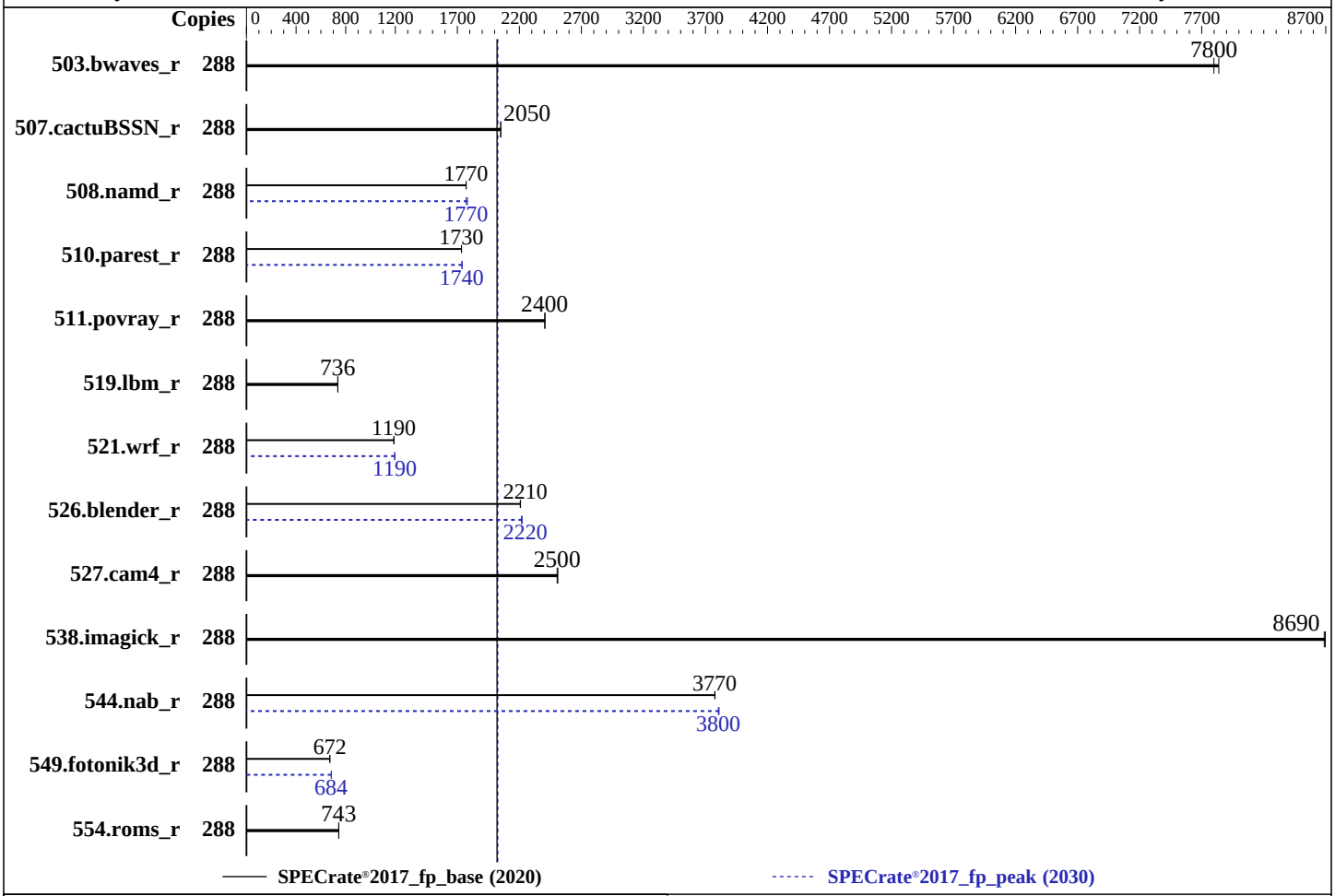
PowerEdge XE9685L (AMD EPYC 9825 144-Core Processor)

SPECrate®2017_fp_base = 2020

SPECrate®2017_fp_peak = 2030

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jun-2025
Hardware Availability: May-2025
Software Availability: Oct-2024



Hardware

CPU Name:	AMD EPYC 9825
Max MHz:	3700
Nominal:	2200
Enabled:	288 cores, 2 chips
Orderable:	1,2 chips
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	384 MB I+D on chip per chip, 32 MB shared / 12 cores
Other:	None
Memory:	1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)
Storage:	150 GB on tmpfs
Other:	CPU Cooling: DLC

Software

OS:	Ubuntu 22.04.5 LTS 6.8.0-65-generic
Compiler:	C/C++/Fortran: Version 5.0.0 of AOCC
Parallel:	No
Firmware:	Version 1.1.5 released Jul-2025
File System:	tmpfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	None
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage.

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9825 144-Core Processor)

SPECrate®2017_fp_base = 2020

SPECrate®2017_fp_peak = 2030

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jun-2025
Hardware Availability: May-2025
Software Availability: Oct-2024

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	288	**370**	**7800**	368	7840			288	**370**	**7800**	368	7840		
507.cactuBSSN_r	288	178	2050	**178**	**2050**			288	178	2050	**178**	**2050**		
508.namd_r	288	154	1770	**155**	**1770**			288	**154**	**1770**	154	1780		
510.parest_r	288	**435**	**1730**	434	1730			288	**434**	**1740**	433	1740		
511.povray_r	288	279	2410	**280**	**2400**			288	279	2410	**280**	**2400**		
519.lbm_r	288	412	737	**413**	**736**			288	412	737	**413**	**736**		
521.wrf_r	288	542	1190	**543**	**1190**			288	**540**	**1190**	540	1200		
526.blender_r	288	**199**	**2210**	199	2210			288	197	2220	**198**	**2220**		
527.cam4_r	288	**201**	**2500**	201	2510			288	**201**	**2500**	201	2510		
538.imagick_r	288	**82.4**	**8690**	82.4	8700			288	**82.4**	**8690**	82.4	8700		
544.nab_r	288	128	3780	**128**	**3770**			288	127	3810	**127**	**3800**		
549.fotonik3d_r	288	1670	672	**1670**	**672**			288	1640	684	**1642**	**684**		
554.roms_r	288	**616**	**743**	615	745			288	**616**	**743**	615	745		

SPECrate®2017_fp_base = 2020

SPECrate®2017_fp_peak = 2030

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
```

(Continued on next page)

Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4/amd_rate_aocc500_znver5_A_lib/lib:/mnt/ramdisk/cpu2017
     -1.1.9-aocc500-znerv5_A1.4/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

 Benchmark run from a 150 GB ramdisk created with the cmd: "mount -t tmpfs -o size=150G tmpfs /mnt/ramdisk"
```

Platform Notes

```
 BIOS Settings:
                  Logical Processor : Disabled
          Virtualization Technology : Disabled
              NUMA Nodes Per Socket : 4

                      System Profile : Custom
                CPU Power Management : Maximum Performance
                          C-States : Disabled
                 Memory Patrol Scrub : Disabled
  PCI ASPM L1 Link Power Management : Disabled
    Periodic Directory Rinse Tuning : Blended
                 Determinism Control : Manual
                  Determinism Slider : Power Determinism
                 Adaptive Allocation : Enabled
                  Dram Refresh Delay : Performance
                 DIMM Self Healing -
      on Uncorrectable Memory Error : Disabled

 Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on 1234567-XE9685L Wed Jun  4 13:05:00 2025

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
```

(Continued on next page)

Platform Notes (Continued)

```
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.16)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux 1234567-XE9685L 6.8.0-65-generic #68~22.04.1-Ubuntu SMP PREEMPT_DYNAMIC Tue Jul 15 18:06:34 UTC 2
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   13:05:00 up  2:47,  1 user,  load average: 152.13, 250.61, 271.06
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                10:17    2:45m  1.81s  0.62s /bin/bash ./amd_rate_aocc500_znver5_A1.sh

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  time(seconds)        unlimited
  file(blocks)         unlimited
  data(kbytes)         unlimited
  stack(kbytes)        unlimited
  coredump(blocks)     0
  memory(kbytes)       unlimited
  locked memory(kbytes) 2097152
  process              6188746
  nofiles              1024
  vmemory(kbytes)      unlimited
  locks                unlimited
  rtprio               0

------------------------------------------------------------
5. sysinfo process ancestry
 /sbin/init splash
 /bin/login -p --
 -bash
 /bin/bash /home/DellFiles/bin/DELL_rate.sh
 /bin/bash /home/DellFiles/bin/dell-run-main.sh rate
 /bin/bash /home/DellFiles/bin/dell-run-main.sh rate
 /bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-VERS=6.3a --output_format
   html,pdf,txt
 python3 ./run_amd_rate_aocc500_znver5_A1.py
 /bin/bash ./amd_rate_aocc500_znver5_A1.sh
 runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define DL-VERS=6.3a
```

(Continued on next page)

Platform Notes (Continued)

```
   --output_format html,pdf,txt fprate
 runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
   DL-VERS=6.3a --output_format html,pdf,txt --nopower --runmode rate --tune base:peak --size
   test:train:refrate fprate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : AMD EPYC 9825 144-Core Processor
    vendor_id       : AuthenticAMD
    cpu family      : 26
    model           : 17
    stepping        : 0
    microcode       : 0xb10104e
    bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
    TLB size        : 192 4K pages
    cpu cores       : 144
    siblings        : 144
    2 physical ids (chips)
    288 processors (hardware threads)
    physical id 0: core ids 0-11,16-27,32-43,48-59,64-75,80-91,96-107,112-123,128-139,144-155,160-171,176-187
    physical id 1: core ids 0-11,16-27,32-43,48-59,64-75,80-91,96-107,112-123,128-139,144-155,160-171,176-187
    physical id 0: apicids 0-11,16-27,32-43,48-59,64-75,80-91,96-107,112-123,128-139,144-155,160-171,176-187
    physical id 1: apicids
    256-267,272-283,288-299,304-315,320-331,336-347,352-363,368-379,384-395,400-411,416-427,432-443
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.2:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        52 bits physical, 57 bits virtual
  Byte Order:                           Little Endian
  CPU(s):                               288
  On-line CPU(s) list:                  0-287
  Vendor ID:                            AuthenticAMD
  Model name:                           AMD EPYC 9825 144-Core Processor
  CPU family:                           26
  Model:                                17
  Thread(s) per core:                   1
  Core(s) per socket:                   144
  Socket(s):                            2
  Stepping:                             0
  Frequency boost:                      enabled
  CPU max MHz:                          2200.0000
  CPU min MHz:                          1500.0000
  BogoMIPS:                             4394.14
  Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                        pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                                        rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                                        extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                                        sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                                        cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                                        osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                                        perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
```

(Continued on next page)

Platform Notes (Continued)

```
                                 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
                                 smep bmi2 invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                                 avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                                 xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                 cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                                 xsaveerptr rdpru wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv
                                 svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
                                 pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl
                                 vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
                                 avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
                                 movdiri movdir64b overflow_recov succor smca avx512_vp2intersect
                                 flush_l1d debug_swap
  L1d cache:                     13.5 MiB (288 instances)
  L1i cache:                     9 MiB (288 instances)
  L2 cache:                      288 MiB (288 instances)
  L3 cache:                      768 MiB (24 instances)
  NUMA node(s):                  8
  NUMA node0 CPU(s):             0-35
  NUMA node1 CPU(s):             36-71
  NUMA node2 CPU(s):             72-107
  NUMA node3 CPU(s):             108-143
  NUMA node4 CPU(s):             144-179
  NUMA node5 CPU(s):             180-215
  NUMA node6 CPU(s):             216-251
  NUMA node7 CPU(s):             252-287
  Vulnerability Gather data sampling:   Not affected
  Vulnerability Itlb multihit:          Not affected
  Vulnerability L1tf:                   Not affected
  Vulnerability Mds:                    Not affected
  Vulnerability Meltdown:               Not affected
  Vulnerability Mmio stale data:        Not affected
  Vulnerability Reg file data sampling: Not affected
  Vulnerability Retbleed:               Not affected
  Vulnerability Spec rstack overflow:   Not affected
  Vulnerability Spec store bypass:      Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:             Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:             Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                                 disabled; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
  Vulnerability Srbds:                  Not affected
  Vulnerability Tsx async abort:        Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       48K    13.5M   12 Data            1    64        1             64
     L1i       32K       9M    8 Instruction     1    64        1             64
     L2         1M     288M   16 Unified         2  1024        1             64
     L3        32M     768M   16 Unified         3 32768        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 8 nodes (0-7)
  node 0 cpus: 0-35
  node 0 size: 192745 MB
  node 0 free: 190325 MB
  node 1 cpus: 36-71
  node 1 size: 193517 MB
  node 1 free: 191648 MB
  node 2 cpus: 72-107
  node 2 size: 193517 MB
```

(Continued on next page)

Platform Notes (Continued)

```
  node 2 free: 188378 MB
  node 3 cpus: 108-143
  node 3 size: 193501 MB
  node 3 free: 191761 MB
  node 4 cpus: 144-179
  node 4 size: 193517 MB
  node 4 free: 191689 MB
  node 5 cpus: 180-215
  node 5 size: 193517 MB
  node 5 free: 191756 MB
  node 6 cpus: 216-251
  node 6 size: 193517 MB
  node 6 free: 191707 MB
  node 7 cpus: 252-287
  node 7 size: 193431 MB
  node 7 free: 191618 MB
  node distances:
  node   0   1   2   3   4   5   6   7
    0:  10  12  12  12  32  32  32  32
    1:  12  10  12  12  32  32  32  32
    2:  12  12  10  12  32  32  32  32
    3:  12  12  12  10  32  32  32  32
    4:  32  32  32  32  10  12  12  12
    5:  32  32  32  32  12  10  12  12
    6:  32  32  32  32  12  12  10  12
    7:  32  32  32  32  12  12  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1584402000 kB

------------------------------------------------------------
10. who -r
  run-level 3 Jun 4 10:17

------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.16)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
                   accounts-daemon anacron apparmor avahi-daemon bluetooth console-setup cron cups
                   cups-browsed dmesg e2scrub_reap getty@ gpu-manager grub-common grub-initrd-fallback
                   irqbalance kerneloops keyboard-setup lm-sensors networkd-dispatcher openvpn
                   power-profiles-daemon rsyslog secureboot-db setvtrgb ssh switcheroo-control systemd-oomd
                   systemd-pstore systemd-resolved systemd-timesyncd thermald tuned ua-reboot-cmds
                   ubuntu-advantage udisks2 ufw wpa_supplicant
  enabled-runtime  netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
  disabled         acpid brltty console-getty debug-shell firewalld nftables openvpn-client@ openvpn-server@
                   openvpn@ rsync rtkit-daemon serial-getty@ speech-dispatcherd
                   systemd-boot-check-no-failures systemd-network-generator systemd-networkd
                   systemd-networkd-wait-online systemd-sysext systemd-time-wait-sync upower
                   wpa_supplicant-nl80211@ wpa_supplicant-wired@ wpa_supplicant@
  generated        apport mst speech-dispatcher
  indirect         saned@ spice-vdagentd uuidd
  masked           alsa-utils cryptdisks cryptdisks-early hwclock pulseaudio-enable-autospawn rc rcS saned
                   snapd sudo x11-common
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.8.0-65-generic
  root=UUID=3d56a3ce-309d-4204-9289-26dc61264336
  ro
  quiet
  splash
  vt.handoff=7

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 183:
    current policy: frequency should be within 1.50 GHz and 2.20 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes
      Boost States: 0
      Total States: 3
      Pstate-P0:  2200MHz

------------------------------------------------------------
15. tuned-adm active
  Current active profile: latency-performance

------------------------------------------------------------
16. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           0
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio           3
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                      8
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                       1
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                1

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage
  defrag          [always] defer defer+madvise madvise never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
18. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
```

(Continued on next page)

Platform Notes (Continued)

```
   max_ptes_none             511
   max_ptes_shared           256
   max_ptes_swap              64
   pages_to_scan            4096
   scan_sleep_millisecs    10000

 ------------------------------------------------------------
 19. OS release
   From /etc/*-release /etc/*-version
   os-release Ubuntu 22.04.5 LTS

 ------------------------------------------------------------
 20. Disk information
 SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4
   Filesystem     Type   Size  Used Avail Use% Mounted on
   tmpfs          tmpfs  150G  3.3G  147G   3% /mnt/ramdisk

 ------------------------------------------------------------
 21. /sys/devices/virtual/dmi/id
     Vendor:          Dell Inc.
     Product:         PowerEdge XE9685L
     Product Family: PowerEdge
     Serial:          1234567

 ------------------------------------------------------------
 22. dmidecode
   Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     24x 80AD000080AD HMCG94AHBRA485N 64 GB 2 rank 6400


 ------------------------------------------------------------
 23. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Dell Inc.
    BIOS Version:      1.1.5
    BIOS Date:         07/28/2025
    BIOS Revision:     1.1
```

Compiler Version Notes

```
==============================================================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
```

(Continued on next page)

Compiler Version Notes (Continued)

```
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50

(Continued on next page)

Base Optimization Flags (Continued)

C benchmarks (continued):
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

C++ benchmarks:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl

Benchmarks using both Fortran and C:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

Benchmarks using both C and C++:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100

(Continued on next page)

Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):
`-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang -ldl`

Benchmarks using Fortran, C, and C++:
`-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6 -Wl,-mllvm -Wl,-reduce-array-computations=3 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner -O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto -fstruct-layout=7 -mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -lflang -ldl`

Base Other Flags

C benchmarks:
`-Wno-unused-command-line-argument`

C++ benchmarks:
`-Wno-unused-command-line-argument`

Fortran benchmarks:
`-Wno-unused-command-line-argument`

Benchmarks using both Fortran and C:
`-Wno-unused-command-line-argument`

Benchmarks using both C and C++:
`-Wno-unused-command-line-argument`

Benchmarks using Fortran, C, and C++:
`-Wno-unused-command-line-argument`

Peak Compiler Invocation

C benchmarks:
`clang`

(Continued on next page)

Peak Compiler Invocation (Continued)

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

C++ benchmarks:

508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto

(Continued on next page)

Peak Optimization Flags (Continued)

508.namd_r (continued):
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

510.parest_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang

554.roms_r: basepeak = yes

Benchmarks using both Fortran and C:

521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: basepeak = yes

526.blender_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

(Continued on next page)

Peak Optimization Flags (Continued)

526.blender_r (continued):
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -ldl

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

Peak Other Flags

C benchmarks:
-Wno-unused-command-line-argument

C++ benchmarks:
-Wno-unused-command-line-argument

Fortran benchmarks:
-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:
-Wno-unused-command-line-argument

Benchmarks using both C and C++:
-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:
-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-06-04 13:05:00-0400.
Report generated on 2025-12-03 14:23:55 by CPU2017 PS formatter v6716.
Originally published on 2025-12-02.