



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

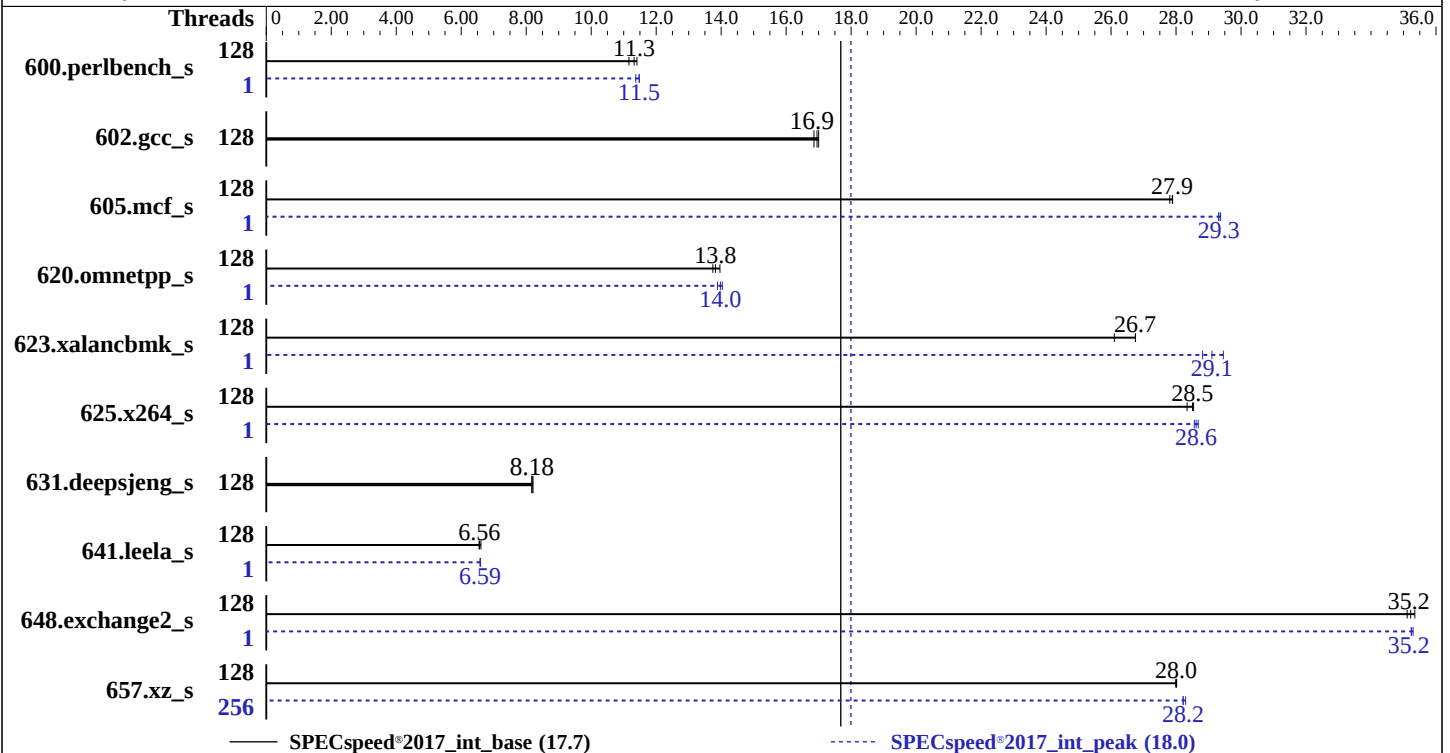
Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024



Hardware

CPU Name: AMD EPYC 9755
Max MHz: 4100
Nominal: 2700
Enabled: 128 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 512 MB I+D on chip per chip, 32 MB shared / 8 cores
Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 5200)
Storage: 1 x 1.8 TB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
kernel 6.4.0-150600.21-default
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: Yes
Firmware: HPE BIOS Version v1.20 07/11/2025 released Jul-2025
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12
(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	128	159	11.2	<u>157</u>	<u>11.3</u>	156	11.4	1	155	11.5	<u>155</u>	<u>11.5</u>	156	11.4
602.gcc_s	128	<u>235</u>	<u>16.9</u>	234	17.0	236	16.9	128	<u>235</u>	<u>16.9</u>	234	17.0	236	16.9
605.mcf_s	128	<u>169</u>	<u>27.9</u>	169	27.9	170	27.8	1	161	29.4	161	29.3	<u>161</u>	<u>29.3</u>
620.omnetpp_s	128	117	14.0	119	13.7	<u>118</u>	<u>13.8</u>	1	117	13.9	116	14.0	<u>117</u>	<u>14.0</u>
623.xalancbmk_s	128	53.0	26.8	<u>53.0</u>	<u>26.7</u>	54.3	26.1	1	<u>48.7</u>	<u>29.1</u>	49.2	28.8	48.1	29.5
625.x264_s	128	61.8	28.5	62.2	28.3	<u>61.9</u>	<u>28.5</u>	1	61.5	28.7	61.8	28.6	<u>61.6</u>	<u>28.6</u>
631.deepsjeng_s	128	175	8.17	175	8.21	<u>175</u>	<u>8.18</u>	128	175	8.17	175	8.21	<u>175</u>	<u>8.18</u>
641.leela_s	128	260	6.55	<u>260</u>	<u>6.56</u>	258	6.61	1	<u>259</u>	<u>6.59</u>	259	6.58	259	6.59
648.exchange2_s	128	83.7	35.1	<u>83.5</u>	<u>35.2</u>	83.2	35.3	1	83.4	35.2	83.3	35.3	<u>83.4</u>	<u>35.2</u>
657.xz_s	128	221	28.0	221	28.0	<u>221</u>	<u>28.0</u>	256	218	28.3	219	28.2	<u>219</u>	<u>28.2</u>

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP_CPU_AFFINITY = "0-255"

LD_LIBRARY_PATH =

"/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib32:"

LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"

MALLOC_CONF = "retain:true"

OMP_DYNAMIC = "false"

OMP_SCHEDULE = "static"

OMP_STACKSIZE = "128M"

OMP_THREAD_LIMIT = "256"

Environment variables set by runcpu during the 600.perlbench_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 605.mcf_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 620.omnetpp_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 623.xalancbmk_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 625.x264_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 641.leela_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 648.exchange2_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 657.xz_s peak run:

GOMP_CPU_AFFINITY = "0-255"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configurations : Parameters are selected in the order shown below

Workload Profile set to General Peak Frequency Compute

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

Memory Patrol Scrubbing set to Disabled

ACPI CST C2 Latency set to 18 microseconds

Last-Level Cache (LLC) as NUMA Node set to Enabled

NUMA memory domains per socket set to Four memory domains per socket

Thermal Configuration set to Enhanced CPU Cooling

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Platform Notes (Continued)

Workload Profile set to Custom
Power Regulator set to OS Control Mode

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Sat Aug 30 21:54:11 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e091p)
x86_64 x86_64 x86_64 GNU/Linux

2. w
21:54:11 up 1:36, 3 users, load average: 0.18, 0.04, 0.01
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root pts/1 10.79.30.182:S.0 21:53 11.00s 0.89s 0.10s /bin/bash ./amd_speed_aocc500_znver5_A1.sh

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 3094218
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Platform Notes (Continued)

```
open files                (-n) 1024
pipe size                  (512 bytes, -p) 8
POSIX message queues      (bytes, -q) 819200
real-time priority        (-r) 0
stack size                 (kbytes, -s) unlimited
cpu time                   (seconds, -t) unlimited
max user processes        (-u) 3094218
virtual memory             (kbytes, -v) unlimited
file locks                 (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
screen -S cpu
SCREEN -S cpu
/bin/bash
python3 ./run_intspeed.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 intspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.019/temlogs/preenv.intspeed.019.0.log --lognum 019.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : AMD EPYC 9755 128-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 2
stepping       : 1
microcode      : 0xb002151
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 192 4K pages
cpu cores      : 128
siblings       : 256
1 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids 0-127
physical id 0: apicids 0-255
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      52 bits physical, 57 bits virtual
Byte Order:         Little Endian
CPU(s):             256
On-line CPU(s) list: 0-255
Vendor ID:          AuthenticAMD
BIOS Vendor ID:     Advanced Micro Devices, Inc.
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12
(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Platform Notes (Continued)

Model name:	AMD EPYC 9755 128-Core Processor	CPU @ 2.7GHz
BIOS Model name:	AMD EPYC 9755 128-Core Processor	
BIOS CPU family:	107	
CPU family:	26	
Model:	2	
Thread(s) per core:	2	
Core(s) per socket:	128	
Socket(s):	1	
Stepping:	1	
Frequency boost:	enabled	
CPU(s) scaling MHz:	101%	
CPU max MHz:	2700.0000	
CPU min MHz:	1500.0000	
BogoMIPS:	5392.00	
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cpc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect flush_l1d debug_swap	
Virtualization:	AMD-V	
L1d cache:	6 MiB (128 instances)	
L1i cache:	4 MiB (128 instances)	
L2 cache:	128 MiB (128 instances)	
L3 cache:	512 MiB (16 instances)	
NUMA node(s):	16	
NUMA node0 CPU(s):	0-7,128-135	
NUMA node1 CPU(s):	8-15,136-143	
NUMA node2 CPU(s):	16-23,144-151	
NUMA node3 CPU(s):	24-31,152-159	
NUMA node4 CPU(s):	32-39,160-167	
NUMA node5 CPU(s):	40-47,168-175	
NUMA node6 CPU(s):	48-55,176-183	
NUMA node7 CPU(s):	56-63,184-191	
NUMA node8 CPU(s):	64-71,192-199	
NUMA node9 CPU(s):	72-79,200-207	
NUMA node10 CPU(s):	80-87,208-215	
NUMA node11 CPU(s):	88-95,216-223	
NUMA node12 CPU(s):	96-103,224-231	
NUMA node13 CPU(s):	104-111,232-239	
NUMA node14 CPU(s):	112-119,240-247	
NUMA node15 CPU(s):	120-127,248-255	
Vulnerability Gather data sampling:	Not affected	
Vulnerability Itlb multihit:	Not affected	
Vulnerability L1tf:	Not affected	
Vulnerability Mds:	Not affected	

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Platform Notes (Continued)

Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	32K	4M	8	Instruction	1	64	1	64
L2	1M	128M	16	Unified	2	1024	1	64
L3	32M	512M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 16 nodes (0-15)
node 0 cpus: 0-7,128-135
node 0 size: 48063 MB
node 0 free: 47396 MB
node 1 cpus: 8-15,136-143
node 1 size: 48379 MB
node 1 free: 48156 MB
node 2 cpus: 16-23,144-151
node 2 size: 48379 MB
node 2 free: 48178 MB
node 3 cpus: 24-31,152-159
node 3 size: 48379 MB
node 3 free: 48133 MB
node 4 cpus: 32-39,160-167
node 4 size: 48379 MB
node 4 free: 48126 MB
node 5 cpus: 40-47,168-175
node 5 size: 48379 MB
node 5 free: 48199 MB
node 6 cpus: 48-55,176-183
node 6 size: 48379 MB
node 6 free: 48172 MB
node 7 cpus: 56-63,184-191
node 7 size: 48341 MB
node 7 free: 48180 MB
node 8 cpus: 64-71,192-199
node 8 size: 48379 MB
node 8 free: 48183 MB
node 9 cpus: 72-79,200-207
node 9 size: 48379 MB
node 9 free: 48095 MB
node 10 cpus: 80-87,208-215
node 10 size: 48379 MB
node 10 free: 48107 MB
node 11 cpus: 88-95,216-223
node 11 size: 48244 MB
node 11 free: 48092 MB
node 12 cpus: 96-103,224-231

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12
(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Platform Notes (Continued)

node 12 size: 48379 MB
node 12 free: 48212 MB
node 13 cpus: 104-111,232-239
node 13 size: 48379 MB
node 13 free: 48240 MB
node 14 cpus: 112-119,240-247
node 14 size: 48379 MB
node 14 free: 48055 MB
node 15 cpus: 120-127,248-255
node 15 size: 48379 MB
node 15 free: 48061 MB

node distances:

node	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0:	10	11	11	11	11	11	11	11	12	12	12	12	12	12	12	12
1:	11	10	11	11	11	11	11	11	12	12	12	12	12	12	12	12
2:	11	11	10	11	11	11	11	11	12	12	12	12	12	12	12	12
3:	11	11	11	10	11	11	11	11	12	12	12	12	12	12	12	12
4:	11	11	11	11	10	11	11	11	12	12	12	12	12	12	12	12
5:	11	11	11	11	11	10	11	11	12	12	12	12	12	12	12	12
6:	11	11	11	11	11	11	10	11	12	12	12	12	12	12	12	12
7:	11	11	11	11	11	11	11	10	12	12	12	12	12	12	12	12
8:	12	12	12	12	12	12	12	12	10	11	11	11	11	11	11	11
9:	12	12	12	12	12	12	12	12	11	10	11	11	11	11	11	11
10:	12	12	12	12	12	12	12	12	11	11	10	11	11	11	11	11
11:	12	12	12	12	12	12	12	12	11	11	11	10	11	11	11	11
12:	12	12	12	12	12	12	12	12	11	11	11	11	10	11	11	11
13:	12	12	12	12	12	12	12	12	11	11	11	11	11	10	11	11
14:	12	12	12	12	12	12	12	12	11	11	11	11	11	11	10	11
15:	12	12	12	12	12	12	12	12	11	11	11	11	11	11	11	10

9. /proc/meminfo
MemTotal: 792151108 kB

10. who -r
run-level 5 Aug 30 20:18

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target Status
graphical running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron display-manager getty@ irqbalance iscsi issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvme-fc-autoconnect oracle postfix purge-kernels rollback rsyslog sapconf smartd sshd systemctl-logger systemd-pstore virtgemud virtxend wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant xencommons
enabled-runtime	systemd-fsck-root systemd-remount-fs
disabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon amavis apache2 apache2@ autofs autovast-initscripts bgpd blk-availability bluetooth-mesh boot-sysctl booth-arbitrator booth@ ca-certificates certmonger chrony-wait chronyd clamav-milter clamd console-getty corosync corosync-notifyd crm_mon ctdb cups cups-browsed ddclient debug-shell dhcpd dhcpd6 dhcrelay dhcrelay6 dirsrv@ dlm dnsmasq drbd drbd-lvchange@ drbd-wait-promotable@ ebttables exchange-bmc-os-info fetchmail firewallld freshclam fsidd gpm grub2-once haveged hawk hwloc-dump-hwdata ipmi ipmievdp ipvsadm iscsi-init iscsid isisd issue-add-ssh-keys kexec-load ksm kvm_stat ldirectord

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Platform Notes (Continued)

libvirt-guests logd lunmask lvmlckd lvmlcks man-db-create mariadb mariadb@ multipathd
named nfs nfs-blkmap nfs-server nfsserver nmb openvpn@ ospf6d ospfd ostree-remount
pacemaker racoon-setkey radvd rarpd@ ripd ripngd rpcbind rpmconfigcheck rsyncd
rtkit-daemon sanlock sbd sbd_remote serial-getty@ smartd_generate_opts smb snmpd snmptrapd
spamd spampd speech-dispatcherd squid srp_daemon srp_daemon_port@ strongswan
strongswan-starter svnserve sysstat systemd-boot-check-no-failures systemd-confext
systemd-network-generator systemd-nspawn@ systemd-sysextd systemd-time-wait-sync
systemd-timesyncd tcsd udisks2 update-system-flatpaks upower virtinterfaced virtlockd
virtlogd virtnetworkd virtnodedevd virtnwfilterd virtsecret virtstoraged vncserver@
vsftpd wdm winbind wpa_supplicant@ xen-dom0-modules xen-init-dom0
xen-qemu-dom0-disk-backend xen-watchdog xenconsole xenconsole xenstored zebra
pcscd saned@ systemd-userdbd tftp uidd wickedd

indirect

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=97def8eb-7bcd-4ed3-b3a2-8816e352f9b0
splash=silent
mitigations=auto
quiet
security=apparmor
amd_pstate=active

14. cpupower frequency-info

analyzing CPU 240:
current policy: frequency should be within 1.50 GHz and 2.70 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes

15. sysctl

kernel.numa_balancing	0
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	314572800
vm.dirty_background_ratio	0
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

16. /sys/kernel/mm/transparent_hugepage

defrag [always] defer defer+madvise madvise never
enabled [always] madvise never
hpage_pmd_size 2097152

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Platform Notes (Continued)

shmem_enabled always within_size advise [never] deny force

17. /sys/kernel/mm/transparent_hugepage/khugepaged

```
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000
```

18. OS release

```
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

19. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme0n1p3	ext4	1.8T	19G	1.7T	2%	/

20. /sys/devices/virtual/dmi/id

```
Vendor: HPE
Product: HPE ProLiant Compute DL325 Gen12
Product Family: ProLiant
Serial: SANJACSCM
```

21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

```
8x Hynix HMC694AHBRA480N 64 GB 2 rank 6400, configured at 5200
4x Micron MTC40F2046S1RC64BD2 QSFF 64 GB 2 rank 6400, configured at 5200
```

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor: HPE
BIOS Version: 1.20
BIOS Date: 07/11/2025
BIOS Revision: 1.20
Firmware Revision: 1.16
```

Compiler Version Notes

```
C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
  | 657.xz_s(base, peak)
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin

```
=====  
C++      | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)  
          | 641.leela_s(base, peak)  
=====
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin

```
=====  
Fortran  | 648.exchange2_s(base, peak)  
=====
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64

602.gcc_s: -DSPEC_LP64

605.mcf_s: -DSPEC_LP64

620.omnetpp_s: -DSPEC_LP64

623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64

625.x264_s: -DSPEC_LP64

631.deepsjeng_s: -DSPEC_LP64

641.leela_s: -DSPEC_LP64

648.exchange2_s: -DSPEC_LP64

657.xz_s: -DSPEC_LP64



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-extra-inliner -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP
-flto -fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp -lamdlibm
-lflang -lamdalloc
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-mllvm -loop-unswitch-threshold=2000000
-mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-iv-split -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

602.gcc_s: basepeak = yes

```
605.mcf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

625.x264_s: Same as 600.perlbench_s

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12
(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Peak Optimization Flags (Continued)

657.xz_s: Same as 600.perlbench_s

C++ benchmarks:

620.omnetpp_s: -m64 -std=c++14

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang

623.xalancbmk_s: -m64 -std=c++14

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -fopenmp=libomp -lomp
-lamdlibm -lamdalloc-ext -lflang

631.deepsjeng_s: basepeak = yes

641.leela_s: -m64 -std=c++14

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-iv-split -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.70 GHz, AMD EPYC 9755)

SPECspeed®2017_int_base = 17.7

SPECspeed®2017_int_peak = 18.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Aug-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.7.html>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.7.xml>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-08-30 22:54:11-0400.

Report generated on 2025-11-05 12:07:53 by CPU2017 PDF formatter v6716.

Originally published on 2025-11-04.