



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860 V4  
(2.40 GHz, Intel Xeon 6768P)

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

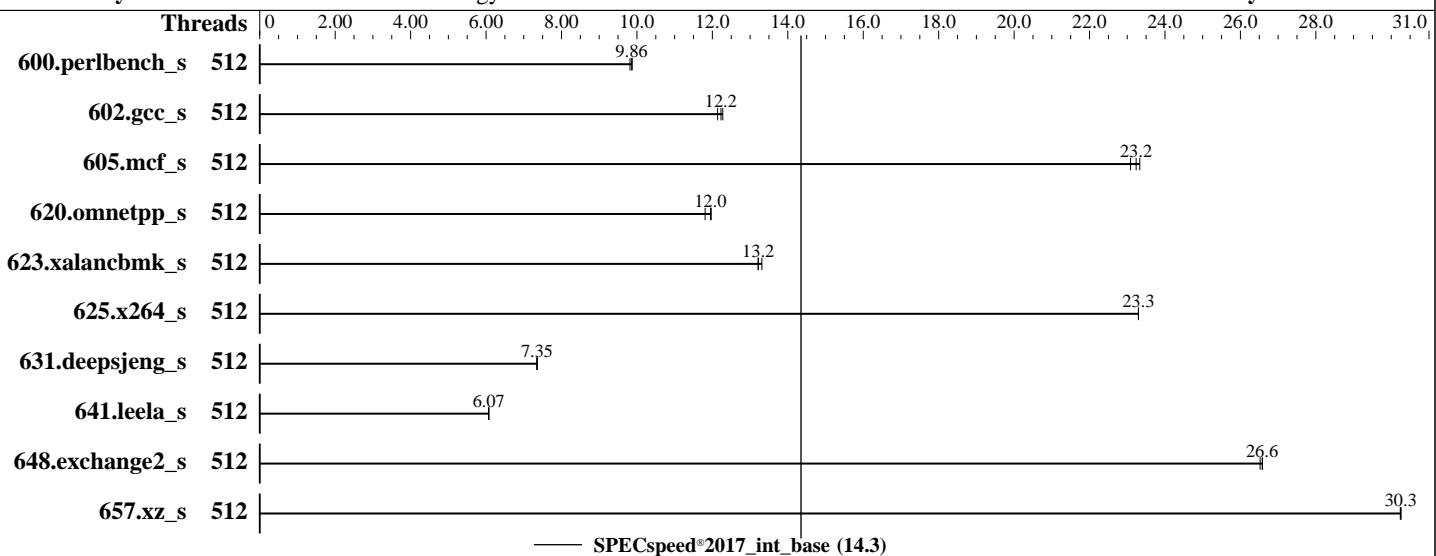
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025



### Hardware

CPU Name: Intel Xeon 6768P  
Max MHz: 3900  
Nominal: 2400  
Enabled: 256 cores, 4 chips, 2 threads/core  
Orderable: 2,4 chips  
Cache L1: 64 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 336 MB I+D on chip per chip  
Other: None  
Memory: 2 TB (32 x 64 GB 2Rx4 PC5-6400B-R)  
Storage: 1 x 1.92 TB NVMe SSD  
Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP7  
Compiler: Kernel 6.4.0-150700.51-default  
C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
Parallel: Yes  
Firmware: Lenovo BIOS Version RVE103X 1.10 released Jul-2025  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860 V4  
(2.40 GHz, Intel Xeon 6768P)

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2025

Tested by: Lenovo Global Technology

Software Availability: Jun-2025

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	512	<b>180</b>	<b>9.86</b>	181	9.82	180	9.87							
602.gcc_s	512	324	12.3	328	12.1	<b>326</b>	<b>12.2</b>							
605.mcf_s	512	204	23.1	202	23.3	<b>203</b>	<b>23.2</b>							
620.omnetpp_s	512	138	11.8	<b>136</b>	<b>12.0</b>	136	12.0							
623.xalancbmk_s	512	106	13.3	<b>107</b>	<b>13.2</b>	107	13.2							
625.x264_s	512	75.7	23.3	75.7	23.3	<b>75.7</b>	<b>23.3</b>							
631.deepsjeng_s	512	<b>195</b>	<b>7.35</b>	195	7.36	195	7.34							
641.leela_s	512	281	6.07	281	6.07	<b>281</b>	<b>6.07</b>							
648.exchange2_s	512	111	26.6	111	26.5	<b>111</b>	<b>26.6</b>							
657.xz_s	512	<b>204</b>	<b>30.3</b>	204	30.2	204	30.3							

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-ic2024.1/lib/intel64:/home/cpu2017-1.1.9-ic2024.1/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM

memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860 V4  
(2.40 GHz, Intel Xeon 6768P)

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2025

Tested by: Lenovo Global Technology

Software Availability: Jun-2025

## Platform Notes

BIOS configuration:

Workload Profile set to General Computing - Peak Frequency

SNC set to Enabled

Page Policy set to Adaptive

```
Sysinfo program /home/cpu2017-1.1.9-ic2024.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Fri Aug 29 05:11:11 2025
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents

```
1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
```

-----

```
1. uname -a
Linux localhost 6.4.0-150700.51-default #1 SMP PREEMPT_DYNAMIC Wed Apr 30 21:35:43 UTC 2025 (6930611)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
05:11:11 up 4 min, 1 user, load average: 1.36, 3.94, 2.01
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals          (-i) 8254234
max locked memory       (kbytes, -l) 8192
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860 V4  
(2.40 GHz, Intel Xeon 6768P)

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2025

Tested by: Lenovo Global Technology

Software Availability: Jun-2025

## Platform Notes (Continued)

```
max memory size      (kbytes, -m) unlimited
open files           (-n) 1024
pipe size            (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority   (-r) 0
stack size            (kbytes, -s) unlimited
cpu time              (seconds, -t) unlimited
max user processes    (-u) 8254234
virtual memory        (kbytes, -v) unlimited
file locks             (-x) unlimited
```

---

### 5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash ./02.remote_local_SPECCpu_1.01.sh
sh Run542-compliant-ic2024.1-lin-sapphirerapids-speedint-base-smt-on-20240308.sh
runcpu --nobuild --action validate --define default-platform-flags -c
  ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=256 --tune base -o all --define
  intspeedaffinity --define smt-on --define drop_caches intspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
  ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=256 --tune base --output_format all --define
  intspeedaffinity --define smt-on --define drop_caches --nopower --runmode speed --tune base --size
  refspeed intspeed --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.532/templogs/preenv.intspeed.532.0.log --lognum 532.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2024.1
```

---

### 6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6768P
vendor_id       : GenuineIntel
cpu family     : 6
model          : 173
stepping        : 1
microcode       : 0x10003d0
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores       : 64
siblings         : 128
4 physical ids (chips)
512 processors (hardware threads)
physical id 0: core ids 0-31,64-95
physical id 1: core ids 0-31,64-95
physical id 2: core ids 0-31,64-95
physical id 3: core ids 0-31,64-95
physical id 0: apicids 0-63,128-191
physical id 1: apicids 256-319,384-447
physical id 2: apicids 512-575,640-703
physical id 3: apicids 768-831,896-959
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

---

### 7. lscpu

```
From lscpu from util-linux 2.40.4:
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860 V4  
(2.40 GHz, Intel Xeon 6768P)

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2025

Tested by: Lenovo Global Technology

Software Availability: Jun-2025

## Platform Notes (Continued)

Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	512
On-line CPU(s) list:	0-511
Vendor ID:	GenuineIntel
Model name:	Intel(R) Xeon(R) 6768P
CPU family:	6
Model:	173
Thread(s) per core:	2
Core(s) per socket:	64
Socket(s):	4
Stepping:	1
BogoMIPS:	4800.00
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nospl xtstopology nonstop_tsc cpuid aperfmpf tsc_known_freq pnpi pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 intel_ppin cdp_12 ssbd mba ibrs ibpb stibrs_ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqmi rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmi_llc cqmi_occup_llc cqmi_mbmi_total cqmi_mbmi_local split_lock_detect user_shstx avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitlg tme avx512_vpocntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_llc arch_capabilities
Virtualization:	VT-x
L1d cache:	12 MiB (256 instances)
L1i cache:	16 MiB (256 instances)
L2 cache:	512 MiB (256 instances)
L3 cache:	1.3 GiB (4 instances)
NUMA node(s):	8
NUMA node0 CPU(s):	0-31,256-287
NUMA node1 CPU(s):	32-63,288-319
NUMA node2 CPU(s):	64-95,320-351
NUMA node3 CPU(s):	96-127,352-383
NUMA node4 CPU(s):	128-159,384-415
NUMA node5 CPU(s):	160-191,416-447
NUMA node6 CPU(s):	192-223,448-479
NUMA node7 CPU(s):	224-255,480-511
Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability Llftf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation: Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation: Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSB-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds:	Not affected

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860 V4  
(2.40 GHz, Intel Xeon 6768P)

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2025

Tested by: Lenovo Global Technology

Software Availability: Jun-2025

## Platform Notes (Continued)

Vulnerability Tsx async abort: Not affected

```
From lscpu --cache:
  NAME ONE-SIZE ALL-SIZE WAYS TYPE      LEVEL    SETS PHY-LINE COHERENCY-SIZE
  L1d     48K      12M   12 Data          1       64      1        64
  L1i     64K      16M   16 Instruction  1       64      1        64
  L2      2M       512M  16 Unified       2      2048      1        64
  L3     336M     1.3G  16 Unified       3     344064      1        64
```

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)
node 0 cpus: 0-31,256-287
node 0 size: 257565 MB
node 0 free: 256737 MB
node 1 cpus: 32-63,288-319
node 1 size: 258030 MB
node 1 free: 257036 MB
node 2 cpus: 64-95,320-351
node 2 size: 258030 MB
node 2 free: 257167 MB
node 3 cpus: 96-127,352-383
node 3 size: 258030 MB
node 3 free: 257093 MB
node 4 cpus: 128-159,384-415
node 4 size: 257992 MB
node 4 free: 257282 MB
node 5 cpus: 160-191,416-447
node 5 size: 258030 MB
node 5 free: 257265 MB
node 6 cpus: 192-223,448-479
node 6 size: 258030 MB
node 6 free: 257325 MB
node 7 cpus: 224-255,480-511
node 7 size: 257873 MB
node 7 free: 257227 MB
node distances:
node  0  1  2  3  4  5  6  7
 0: 10 21 21 21 21 21 21
 1: 12 10 21 21 21 21 21
 2: 21 21 10 12 21 21 21
 3: 21 21 12 10 21 21 21
 4: 21 21 21 21 10 12 21
 5: 21 21 21 21 12 10 21
 6: 21 21 21 21 21 10 12
 7: 21 21 21 21 21 12 10
```

-----  
9. /proc/meminfo

```
MemTotal: 2113112048 kB
```

-----  
10. who -r
run-level 3 Aug 29 05:08

-----  
11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
Default Target Status
multi-user running

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860 V4  
(2.40 GHz, Intel Xeon 6768P)

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2025

Tested by: Lenovo Global Technology

Software Availability: Jun-2025

## Platform Notes (Continued)

```
-----  
12. Services, from systemctl list-unit-files  
STATE          UNIT FILES  
enabled        YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator  
                kbdsettings klog lvm2-monitor nscd nvmefc-boot-connections nvmf-autoconnect postfix  
                purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4  
                wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled       autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait  
                chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info  
                firewalld fsidd gpm grub2-once haveged ipmi ipmievfd issue-add-ssh-keys kexec-load lunmask  
                man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@  
                smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-confext  
                systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2  
indirect        systemd-userdbd wickedd
```

```
-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.51-default  
root=UUID=50860560-ae42-405b-bae3-a2b14754d476  
splash=silent  
mitigations=auto  
quiet  
security=apparmor
```

```
-----  
14. cpupower frequency-info  
analyzing CPU 309:  
  Unable to determine current policy  
  boost state support:  
    Supported: yes  
    Active: yes
```

```
-----  
15. sysctl  
kernel.numa_balancing          1  
kernel.randomize_va_space       2  
vm.compaction_prolactiveness   20  
vm.dirty_background_bytes      0  
vm.dirty_background_ratio      10  
vm.dirty_bytes                 0  
vm.dirty_expire_centisecs     3000  
vm.dirty_ratio                 20  
vm.dirty_writeback_centisecs   500  
vm.dirtytime_expire_seconds    43200  
vm.extfrag_threshold           500  
vm.min_unmapped_ratio          1  
vm.nr_hugepages                0  
vm.nr_hugepages_mempolicy      0  
vm.nr_overcommit_hugepages     0  
vm.swappiness                  60  
vm.watermark_boost_factor      15000  
vm.watermark_scale_factor      10  
vm.zone_reclaim_mode           0
```

```
-----  
16. /sys/kernel/mm/transparent_hugepage  
defrag      always defer defer+madvise [madvise] never  
enabled     [always] madvise never
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860 V4  
(2.40 GHz, Intel Xeon 6768P)

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2025

Tested by: Lenovo Global Technology

Software Availability: Jun-2025

## Platform Notes (Continued)

```
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage/khugepaged  
    alloc_sleep_millisecs 60000  
    defrag 1  
    max_ptes_none 511  
    max_ptes_shared 256  
    max_ptes_swap 64  
    pages_to_scan 4096  
    scan_sleep_millisecs 10000
```

```
-----  
18. OS release  
From /etc/*-release /etc/*-version  
os-release SUSE Linux Enterprise Server 15 SP7
```

```
-----  
19. Disk information  
SPEC is set to: /home/cpu2017-1.1.9-ic2024.1  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/nvme0n1p3 xfs 1.8T 69G 1.7T 4% /
```

```
-----  
20. /sys/devices/virtual/dmi/id  
Vendor: Lenovo  
Product: ThinkSystem SR860 V4  
Product Family: ThinkSystem  
Serial: 9876543210
```

```
-----  
21. dmidecode  
Additional information from dmidecode 3.6 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the  
"DMTF SMBIOS" standard.
```

```
Memory:  
4x SK Hynix HMCG94AHBRA275N 64 GB 2 rank 6400  
7x SK Hynix HMCG94AHBRA277N 64 GB 2 rank 6400  
3x SK Hynix HMCG94AHBRA281N 64 GB 2 rank 6400  
2x SK Hynix HMCG94AHBRA283N 64 GB 2 rank 6400  
8x SK Hynix HMCG94AHBRA480N 64 GB 2 rank 6400  
3x SK Hynix HMCG94AHBRA481N 64 GB 2 rank 6400  
3x SK Hynix HMCG94AHBRA486N 64 GB 2 rank 6400  
2x SK Hynix HMCG94AHBRA487N 64 GB 2 rank 6400
```

```
-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Lenovo  
BIOS Version: RVE103X-1.10  
BIOS Date: 07/17/2025  
BIOS Revision: 1.10  
Firmware Revision: 1.40
```



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860 V4  
(2.40 GHz, Intel Xeon 6768P)

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2025

Tested by: Lenovo Global Technology

Software Availability: Jun-2025

## Compiler Version Notes

=====  
C | 600.perlbench\_s(base) 602.gcc\_s(base) 605.mcf\_s(base) 625.x264\_s(base) 657.xz\_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====  
C++ | 620.omnetpp\_s(base) 623.xalancbmk\_s(base) 631.deepsjeng\_s(base) 641.leela\_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====  
Fortran | 648.exchange2\_s(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860 V4  
(2.40 GHz, Intel Xeon 6768P)

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Birchstream-F.html>  
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Birchstream-F.xml>  
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2025-08-28 17:11:10-0400.

Report generated on 2025-09-23 16:55:29 by CPU2017 PDF formatter v6716.

Originally published on 2025-09-23.