



SPEC CPU®2017 Floating Point Speed Result

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xFusion

FusionServer 2288H V7 (Intel Xeon Gold 6544Y)

SPECSspeed®2017_fp_base = 302

SPECSspeed®2017_fp_peak = 302

CPU2017 License: 6488

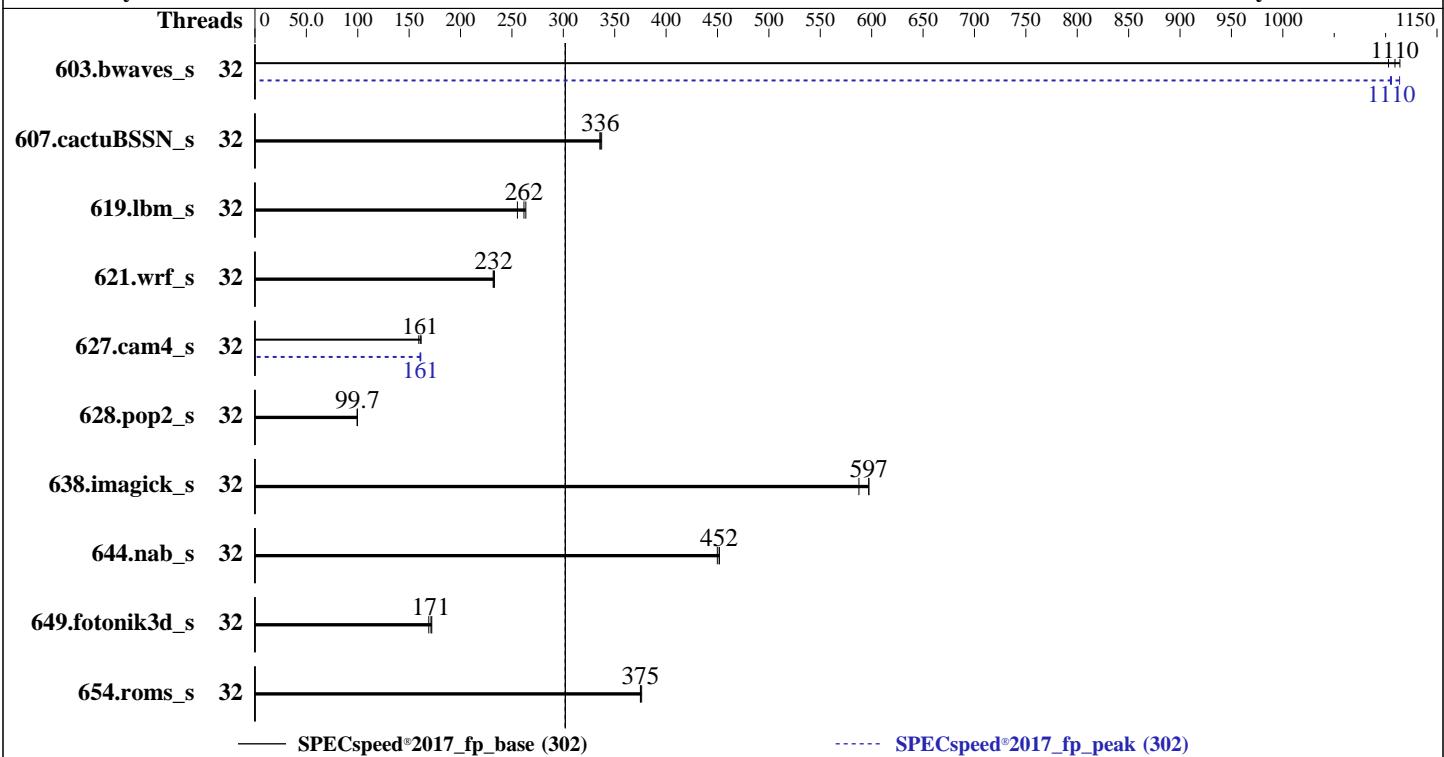
Test Date: Jul-2025

Test Sponsor: xFusion

Hardware Availability: Dec-2023

Tested by: xFusion

Software Availability: Mar-2024



Hardware		Software	
CPU Name:	Intel Xeon Gold 6544Y	OS:	Red Hat Enterprise Linux 9.2 (Plow)
Max MHz:	4100	Compiler:	5.14.0-284.11.1.el9_2.x86_64
Nominal:	3600	Parallel:	C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Enabled:	32 cores, 2 chips	Firmware:	Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Orderable:	1,2 chips	File System:	Yes
Cache L1:	32 KB I + 48 KB D on chip per core	System State:	Version 01.01.06.16 Released May-2025
L2:	2 MB I+D on chip per core	Base Pointers:	xfs
L3:	45 MB I+D on chip per chip	Peak Pointers:	Run level 3 (multi-user)
Other:	None	Other:	64-bit
Memory:	1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 5200)	Power Management:	64-bit
Storage:	1 x 1.92 TB SATA SSD		jemalloc memory allocator V5.0.1
Other:	CPU Cooling: Air		BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	32	53.0	1110	53.2	1110	53.5	1100	32	53.0	1110	53.4	1110	53.4	1100
607.cactuBSSN_s	32	49.6	336	49.4	337	49.7	336	32	49.6	336	49.4	337	49.7	336
619.lbm_s	32	19.9	264	20.0	262	20.5	255	32	19.9	264	20.0	262	20.5	255
621.wrf_s	32	56.8	233	56.9	232	57.1	232	32	56.8	233	56.9	232	57.1	232
627.cam4_s	32	55.6	159	55.1	161	54.8	162	32	55.0	161	55.2	161	55.0	161
628.pop2_s	32	119	99.7	119	99.4	119	99.8	32	119	99.7	119	99.4	119	99.8
638.imagick_s	32	24.2	597	24.2	597	24.6	588	32	24.2	597	24.2	597	24.6	588
644.nab_s	32	38.7	452	38.8	450	38.7	452	32	38.7	452	38.8	450	38.7	452
649.fotonik3d_s	32	53.9	169	53.2	171	53.0	172	32	53.9	169	53.2	171	53.0	172
654.roms_s	32	41.9	376	42.0	375	42.0	375	32	41.9	376	42.0	375	42.0	375

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/spec2017/lib/intel64:/home/spec2017/je5.0.1-64"

MALLOC_CONF = "retain:true"

OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:

Performance Profile Set to Load Balance

Enable LP [Global] Set to Single LP

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Platform Notes (Continued)

ADDDC Sparing Set to Disabled

```
Sysinfo program /home/spec2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Fri Jul 11 03:42:38 2025
```

SUT (System Under Test) info as seen by some common utilities.

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10. who -r
11. Systemd service manager version: systemd 252 (252-13.el9_2)
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19. /sys/kernel/mm/transparent_hugepage/khugepaged
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23. dmidecode
24. BIOS

1. uname -a
Linux localhost.localdomain 5.14.0-284.11.1.el9_2.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 12 10:45:03 EDT
2023 x86_64 x86_64 x86_64 GNU/Linux

2. w
03:42:38 up 4:07, 2 users, load average: 5.83, 4.74, 2.87
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root tty1 23:38 3:05m 0.90s 0.00s -bash
root pts/0 23:36 3:05m 0.06s 0.06s -bash

3. Username
From environment variable \$USER: root

4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited

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Platform Notes (Continued)

pending signals	(-i) 4124634
max locked memory	(kbytes, -l) 64
max memory size	(kbytes, -m) unlimited
open files	(-n) 1024
pipe size	(512 bytes, -p) 8
POSIX message queues	(bytes, -q) 819200
real-time priority	(-r) 0
stack size	(kbytes, -s) unlimited
cpu time	(seconds, -t) unlimited
max user processes	(-u) 4124634
virtual memory	(kbytes, -v) unlimited
file locks	(-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 31
login -- root
-bash
-bash
runcpu --define default-platform-flags -c ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=32
--tune base,peak -o all --define drop_caches fpspeed
runcpu --define default-platform-flags --configfile ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define
cores=32 --tune base,peak --output_format all --define drop_caches --nopower --runmode speed --tune
base:peak --size refspeed fpspeed --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.017/templogs/preenv.fpspeed.017.0.log --lognum 017.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/spec2017

6. /proc/cpuinfo
model name : INTEL(R) XEON(R) GOLD 6544Y
vendor_id : GenuineIntel
cpu family : 6
model : 207
stepping : 2
microcode : 0x21000283
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrp_pbrsb
cpu cores : 16
siblings : 16
2 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-15
physical id 1: core ids 0-15
physical id 0: apicids 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30
physical id 1: apicids 128,130,132,134,136,138,140,142,144,146,148,150,152,154,156,158
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation

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Platform Notes (Continued)

```

Model name: INTEL(R) XEON(R) GOLD 6544Y
BIOS Model name: INTEL(R) XEON(R) GOLD 6544Y
CPU family: 6
Model: 207
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 2
Stepping: 2
Frequency boost: enabled
CPU max MHz: 3601.0000
CPU min MHz: 800.0000
BogoMIPS: 7200.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtTopology
       nonstop_tsc cpuid aperfmpf tsc_known_freq pni pclmulqdq dtes64 monitor
       ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrp pdcm pcid dca sse4_1
       sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
      lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13
       invpcid_single cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
       vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2
       erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap avx512fma
       clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsaved
       xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local avx_vnni
       avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi avx512vbmi umip pku ospke
       waitpkg avx512_vbmi2 gfn vaes vpclmulqdq avx512_vnni avx512_bitalg tme
       avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
       enqcmd fsrm md_clear serialize tsxlptrk pconfig arch_lbr ibt amx_bf16
       avx512_fp16 amx_tile amx_int8 flush_lld arch_capabilities
Virtualization: VT-x
L1d cache: 1.5 MiB (32 instances)
L1i cache: 1 MiB (32 instances)
L2 cache: 64 MiB (32 instances)
L3 cache: 90 MiB (2 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation: Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation: usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation: Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.5M	12	Data	1	64	1	64
L1i	32K	1M	8	Instruction	1	64	1	64
L2	2M	64M	16	Unified	2	2048	1	64
L3	45M	90M	15	Unified	3	49152	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

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Platform Notes (Continued)

```
available: 2 nodes (0-1)
node 0 cpus: 0-15
node 0 size: 515150 MB
node 0 free: 513282 MB
node 1 cpus: 16-31
node 1 size: 516048 MB
node 1 free: 509231 MB
node distances:
node    0    1
 0:   10   21
 1:   21   10

-----
9. /proc/meminfo
MemTotal:      1055947236 kB

-----
10. who -r
run-level 3 Jul 10 23:35

-----
11. Systemd service manager version: systemd 252 (252-13.el9_2)
Default Target      Status
multi-user          degraded

-----
12. Failed units, from systemctl list-units --state=failed
UNIT            LOAD ACTIVE SUB           DESCRIPTION
* sep5.service loaded failed failed systemd script to load sep5 driver at boot time

-----
13. Services, from systemctl list-unit-files
STATE           UNIT FILES
enabled         NetworkManager NetworkManager-dispatcher NetworkManager-wait-online audited chronyd crond
                dbus-broker firewalld getty@ insights-client-boot irqbalance kdump low-memory-monitor
                lvm2-monitor mdmonitor microcode nis-domainname nvmefc-boot-connections rhsmcertd rsyslog
                rtkit-daemon selinux-autorelabel-mark sep5 smartd sshd sssd sysstat systemd-boot-update
                systemd-network-generator tuned udisks2 upower
enabled-runtime systemd-remount-fs
disabled        blk-availability canberra-system-bootup canberra-system-shutdown
                canberra-system-shutdown-reboot chrony-wait console-getty cpupower debug-shell
                dnf-system-upgrade kvm_stat man-db-restart-cache-update nftables nvmf-autoconnect pesign
                rdisc rhcd rhsm rhsm-facts rpmdb-rebuild selinux-check-proper-disable serial-getty@
                sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysext
indirect        sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
                systemd-sysupdate-reboot

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-284.11.1.el9_2.x86_64
root=/dev/mapper/rhel-root
ro
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap

-----
15. cpupower frequency-info
analyzing CPU 0:
```

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Platform Notes (Continued)

```
current policy: frequency should be within 800 MHz and 3.60 GHz.  
The governor "performance" may decide which speed to use  
within this range.
```

```
boost state support:
```

```
Supported: yes  
Active: yes
```

```
16. tuned-adm active
```

```
Current active profile: throughput-performance
```

```
17. sysctl
```

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	40
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	10
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

```
18. /sys/kernel/mm/transparent_hugepage
```

```
defrag      always defer defer+madvise [madvise] never  
enabled     [always] madvise never  
hpage_pmd_size 2097152  
shmem_enabled always within_size advise [never] deny force
```

```
19. /sys/kernel/mm/transparent_hugepage/khugepaged
```

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

```
20. OS release
```

```
From /etc/*-release /etc/*-version  
os-release    Red Hat Enterprise Linux 9.2 (Plow)  
redhat-release Red Hat Enterprise Linux release 9.2 (Plow)  
system-release Red Hat Enterprise Linux release 9.2 (Plow)
```

```
21. Disk information
```

```
SPEC is set to: /home/spec2017
```

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Platform Notes (Continued)

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   819G   54G  766G   7% /home

-----
22. /sys/devices/virtual/dmi/id
Vendor:          XFUSION
Product:         2288H V7
Product Family: Eagle Stream
Serial:          2106182101X3N8000006

-----
23. dmidecode
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
16x Samsung M321R8GA0EB0-CWMCH 64 GB 2 rank 5600, configured at 5200

-----
24. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:    XFUSION
BIOS Version:   01.01.06.16
BIOS Date:      05/08/2025
BIOS Revision:  6.16
```

Compiler Version Notes

```
=====
C           | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
=====
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
C++, C, Fortran | 607.cactusBSSN_s(base, peak)
=====
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
Fortran      | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
=====
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
Fortran, C    | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
=====
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
```

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Compiler Version Notes (Continued)

Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308

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Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactubSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-fsto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

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Base Optimization Flags (Continued)

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int  
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte  
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags



SPEC CPU®2017 Floating Point Speed Result

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xFusion

FusionServer 2288H V7 (Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 302

SPECspeed®2017_fp_peak = 302

CPU2017 License: 6488

Test Sponsor: xFusion

Tested by: xFusion

Test Date: Jul-2025

Hardware Availability: Dec-2023

Software Availability: Mar-2024

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

```
603.bwaves_s: -w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids
-Ofast -ffast-math -fsto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

```
627.cam4_s: -w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -fsto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactubSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-EMR-V1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-EMR-V1.1.xml>



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