



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488

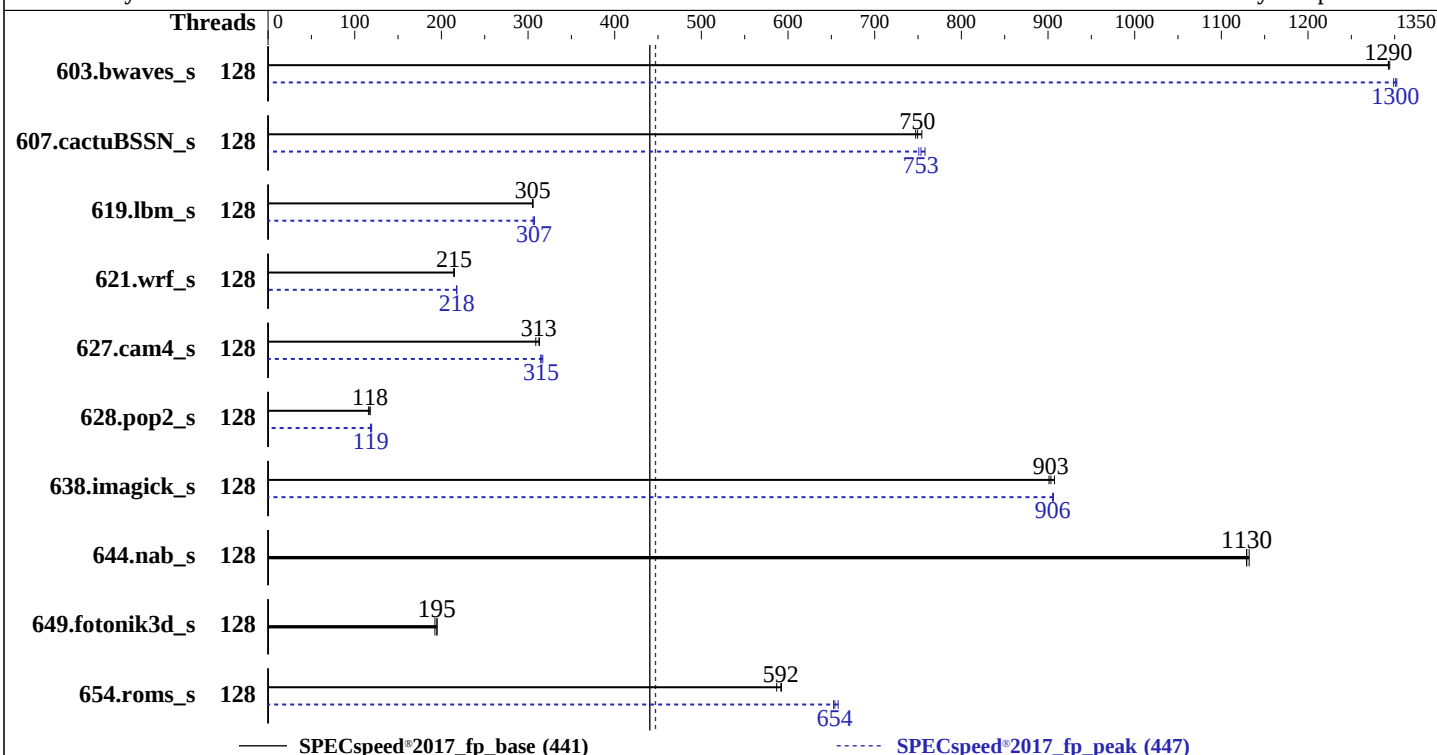
Test Sponsor: xFusion

Tested by: xFusion

Test Date: Jun-2025

Hardware Availability: Apr-2025

Software Availability: Apr-2025



Hardware

CPU Name: AMD EPYC 9755
Max MHz: 4100
Nominal: 2700
Enabled: 128 cores, 1 chip
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 512 MB I+D on chip per chip, 32 MB shared / 8 cores
Other: None
Memory: 384 GB (12 x 32 GB 2Rx8 PC5-6400B-R)
Storage: 1 x 1.6 TB PCIe NVMe SSD
Other: CPU Cooling: Air

Software

OS: Ubuntu 24.04 LTS
6.8.0-59-generic
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: Yes
Firmware: Version 00.13.02.12 released May-2025
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Apr-2025

Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	128	45.6	1290	45.6	1290	<u>45.6</u>	<u>1290</u>	128	45.4	1300	45.3	1300	<u>45.3</u>	<u>1300</u>		
607.cactuBSSN_s	128	22.3	747	<u>22.2</u>	<u>750</u>	22.1	754	128	22.2	751	22.0	758	<u>22.1</u>	<u>753</u>		
619.lbm_s	128	<u>17.1</u>	<u>305</u>	17.2	305	17.1	306	128	<u>17.1</u>	<u>307</u>	17.1	306	17.0	307		
621.wrf_s	128	61.5	215	<u>61.6</u>	<u>215</u>	61.7	214	128	60.7	218	60.8	217	<u>60.8</u>	<u>218</u>		
627.cam4_s	128	28.7	309	<u>28.4</u>	<u>313</u>	28.3	313	128	28.0	317	<u>28.1</u>	<u>315</u>	28.2	315		
628.pop2_s	128	101	118	103	116	<u>101</u>	<u>118</u>	128	<u>100</u>	<u>119</u>	100	118	99.3	120		
638.imagick_s	128	16.0	901	<u>16.0</u>	<u>903</u>	15.9	907	128	15.9	905	<u>15.9</u>	<u>906</u>	15.9	906		
644.nab_s	128	15.5	1130	15.4	1130	<u>15.5</u>	<u>1130</u>	128	15.5	1130	15.4	1130	<u>15.5</u>	<u>1130</u>		
649.fotonik3d_s	128	47.3	193	46.8	195	<u>46.8</u>	<u>195</u>	128	47.3	193	46.8	195	<u>46.8</u>	<u>195</u>		
654.roms_s	128	26.6	592	<u>26.6</u>	<u>592</u>	26.8	587	128	23.9	658	24.1	652	<u>24.1</u>	<u>654</u>		

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit
The kernel stops sending timer ticks to CPUs by using "nohz_full=1-255"
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Apr-2025

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-127"
LD_LIBRARY_PATH =
"/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "128"
```

Environment variables set by runcpu during the 603.bwaves_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 607.cactuBSSN_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 619.lbm_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 621.wrf_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 627.cam4_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 628.pop2_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 638.imagick_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 654.roms_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:

```
Determinism Control = Manual
Determinism Enable = Power
TDP Control = Manual
TDP = 500
PPT Control = Manual
PPT = 500
NUMA Nodes Per Socket = NPS2
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Apr-2025

Platform Notes (Continued)

SMT = Disabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on 2158hv8 Thu May 29 15:55:03 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.1)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux 2158hv8 6.8.0-59-generic #61-Ubuntu SMP PREEMPT_DYNAMIC Fri Apr 11 23:16:11 UTC 2025 x86_64 x86_64
x86_64 GNU/Linux

2. w
15:55:03 up 14 min, 2 users, load average: 0.04, 0.03, 0.04
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - 15:45 9:51 0.06s ? -bash

3. Username
From environment variable \$USER: root

4. ulimit -a
time(seconds) unlimited
file(blocks) unlimited
data(kbytes) unlimited
stack(kbytes) unlimited
coredump(blocks) 0
memory(kbytes) unlimited
locked memory(kbytes) 2097152
process 1545581

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Apr-2025

Platform Notes (Continued)

nfiles	1024
vmemory(kbytes)	unlimited
locks	unlimited
rtprio	0

5. sysinfo process ancestry

```
/sbin/init
python3 ./run_amd_speed_aocc500_znver5_A1.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.009/templogs/preenv.fpspeed.009.0.log --lognum 009.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : AMD EPYC 9755 128-Core Processor
vendor_id       : AuthenticAMD
cpu family      : 26
model           : 2
stepping        : 1
microcode       : 0xb002147
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 128
siblings        : 128
1 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,
240-247
physical id 0: apicids
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,
240-247
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	128
On-line CPU(s) list:	0-127
Vendor ID:	AuthenticAMD
BIOS Vendor ID:	Advanced Micro Devices, Inc.
Model name:	AMD EPYC 9755 128-Core Processor
BIOS Model name:	AMD EPYC 9755 128-Core Processor Unknown CPU @ 2.7GHz
BIOS CPU family:	107
CPU family:	26
Model:	2
Thread(s) per core:	1
Core(s) per socket:	128
Socket(s):	1

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Apr-2025

Platform Notes (Continued)

Stepping: 1
Frequency boost: enabled
CPU(s) scaling MHz: 66%
CPU max MHz: 4121.1909
CPU min MHz: 1500.0000
BogoMIPS: 5391.81
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnni avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect flush_l1d debug_swap
Virtualization: AMD-V
L1d cache: 6 MiB (128 instances)
L1i cache: 4 MiB (128 instances)
L2 cache: 128 MiB (128 instances)
L3 cache: 512 MiB (16 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-63
NUMA node1 CPU(s): 64-127
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP disabled; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	32K	4M	8	Instruction	1	64	1	64
L2	1M	128M	16	Unified	2	1024	1	64
L3	32M	512M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Apr-2025

Platform Notes (Continued)

```
available: 2 nodes (0-1)
node 0 cpus: 0-63
node 0 size: 193019 MB
node 0 free: 192217 MB
node 1 cpus: 64-127
node 1 size: 193448 MB
node 1 free: 192565 MB
node distances:
node 0 1
0: 10 12
1: 12 10
```

```
-----
9. /proc/meminfo
MemTotal: 395743408 kB
```

```
-----
10. who -r
run-level 5 May 29 15:41
```

```
-----
11. Systemd service manager version: systemd 255 (255.4-ubuntu8.1)
Default Target Status
graphical running
```

```
-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online apparmor
apport blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup
cron dmesg e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback
keyboard-setup lm-sensors lvm2-monitor multipathd networkd-dispatcher networking
open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb sysstat systemd-networkd
systemd-networkd-wait-online systemd-pstore systemd-resolved systemd-timesyncd thermald
ua-reboot-cmds ubuntu-advantage udisks2 ufw vgauth wpa_supplicant
enabled-runtime netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled console-getty debug-shell ifupdown-wait-online iscsid nftables rsync serial-getty@ ssh
systemd-boot-check-no-failures systemd-confext systemd-network-generator
systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
systemd-time-wait-sync upower wpa_supplicant-nl80211@ wpa_supplicant-wired@
wpa_supplicant@
indirect systemd-sysupdate systemd-sysupdate-reboot uuid
masked cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common
```

```
-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=vmlinuz-6.8.0-59-generic
root=/dev/mapper/ubuntu--vg-ubuntu--lv
ro
nohz_full=1-191
```

```
-----
14. cpupower frequency-info
analyzing CPU 75:
current policy: frequency should be within 1.50 GHz and 2.70 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488

Test Sponsor: xFusion

Tested by: xFusion

Test Date: Jun-2025

Hardware Availability: Apr-2025

Software Availability: Apr-2025

Platform Notes (Continued)

Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 2700MHz

15. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

16. /sys/kernel/mm/transparent_hugepage

defrag	[always]	defer	defer+madvise	madvise	never
enabled	[always]	madvise	never		
hpage_pmd_size	2097152				
shmem_enabled	always	within_size	advise	[never]	deny force

17. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

18. OS release

From /etc/*-release /etc/*-version
os-release Ubuntu 24.04 LTS

19. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/ubuntu--vg-ubuntu--lv	ext4	196G	18G	169G	10%	/

20. /sys/devices/virtual/dmi/id

Vendor: XFUSION
Product: 2158H V8

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Apr-2025

Platform Notes (Continued)

Product Family: Turin

21. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

3x Samsung M321R4GA3EB2-CCPKC 32 GB 2 rank 6400
5x Samsung M321R4GA3EB2-CCPPC 32 GB 2 rank 6400
4x Samsung M321R4GA3EB2-CCPWC 32 GB 2 rank 6400

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: XFUSION
BIOS Version: 00.13.02.12
BIOS Date: 05/14/2025
BIOS Revision: 2.12

Compiler Version Notes

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Apr-2025

Compiler Version Notes (Continued)

=====

Fortran, C		621.wrf_s(base, peak)	627.cam4_s(base, peak)	628.pop2_s(base, peak)
------------	--	-----------------------	------------------------	------------------------

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:
clang

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Apr-2025

Base Optimization Flags (Continued)

C benchmarks (continued):

-Wl, -mllvm -Wl, -reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

Fortran benchmarks:

-m64 -Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6
-Wl, -mllvm -Wl, -reduce-array-computations=3
-Wl, -mllvm -Wl, -enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang

Benchmarks using both Fortran and C:

-m64 -Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6
-Wl, -mllvm -Wl, -reduce-array-computations=3
-Wl, -mllvm -Wl, -enable-X86-prefetching -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang

Benchmarks using Fortran, C, and C++:

-m64 -std=c++14 -Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6
-Wl, -mllvm -Wl, -reduce-array-computations=3
-Wl, -mllvm -Wl, -x86-use-vzeroupper=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt
-mllvm -loop-unswitch-threshold=2000000 -mllvm -unroll-threshold=100
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Apr-2025

Base Other Flags (Continued)

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jun-2025
Hardware Availability: Apr-2025
Software Availability: Apr-2025

Peak Optimization Flags (Continued)

619.lbm_s (continued):

-lamdlibm -lamdalloc -lflang

638.imagick_s: Same as 619.lbm_s

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -fscalar-transform -fvector-transform
-mllvm -reduce-array-computations=3 -Mrecursive
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

649.fotonik3d_s: basepeak = yes

654.roms_s: Same as 603.bwaves_s

Benchmarks using both Fortran and C:

621.wrf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang

627.cam4_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -Mrecursive
-mrecip=none -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488

Test Sponsor: xFusion

Tested by: xFusion

Test Date: Jun-2025

Hardware Availability: Apr-2025

Software Availability: Apr-2025

Peak Optimization Flags (Continued)

```
628.pop2_s: -m64 -Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6
-Wl, -mllvm -Wl, -reduce-array-computations=3
-Wl, -mllvm -Wl, -enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fscalar-transform
-fvector-transform -Mrecursive -fopenmp=libomp -lomp
-lamplibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6
-Wl, -mllvm -Wl, -reduce-array-computations=3
-Wl, -mllvm -Wl, -x86-use-vzeroupper=false -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -DSPEC_OPENMP
-fremap-arrays -fstrip-mining -fstruct-layout=9
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mllvm -unroll-threshold=100
-Mrecursive -fopenmp=libomp -lomp -lamplibm -lamdalloc -lflang
```

Peak Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.5.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.5.xml>



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9755)

SPECspeed®2017_fp_base = 441

SPECspeed®2017_fp_peak = 447

CPU2017 License: 6488

Test Sponsor: xFusion

Tested by: xFusion

Test Date: Jun-2025

Hardware Availability: Apr-2025

Software Availability: Apr-2025

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-05-29 11:55:03-0400.
Report generated on 2025-07-01 19:09:07 by CPU2017 PDF formatter v6716.
Originally published on 2025-07-01.