



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8 (AMD EPYC 9275F  
4.10 GHz Processor)

SPECspeed®2017\_fp\_base = 404

SPECspeed®2017\_fp\_peak = 407

CPU2017 License: 9019

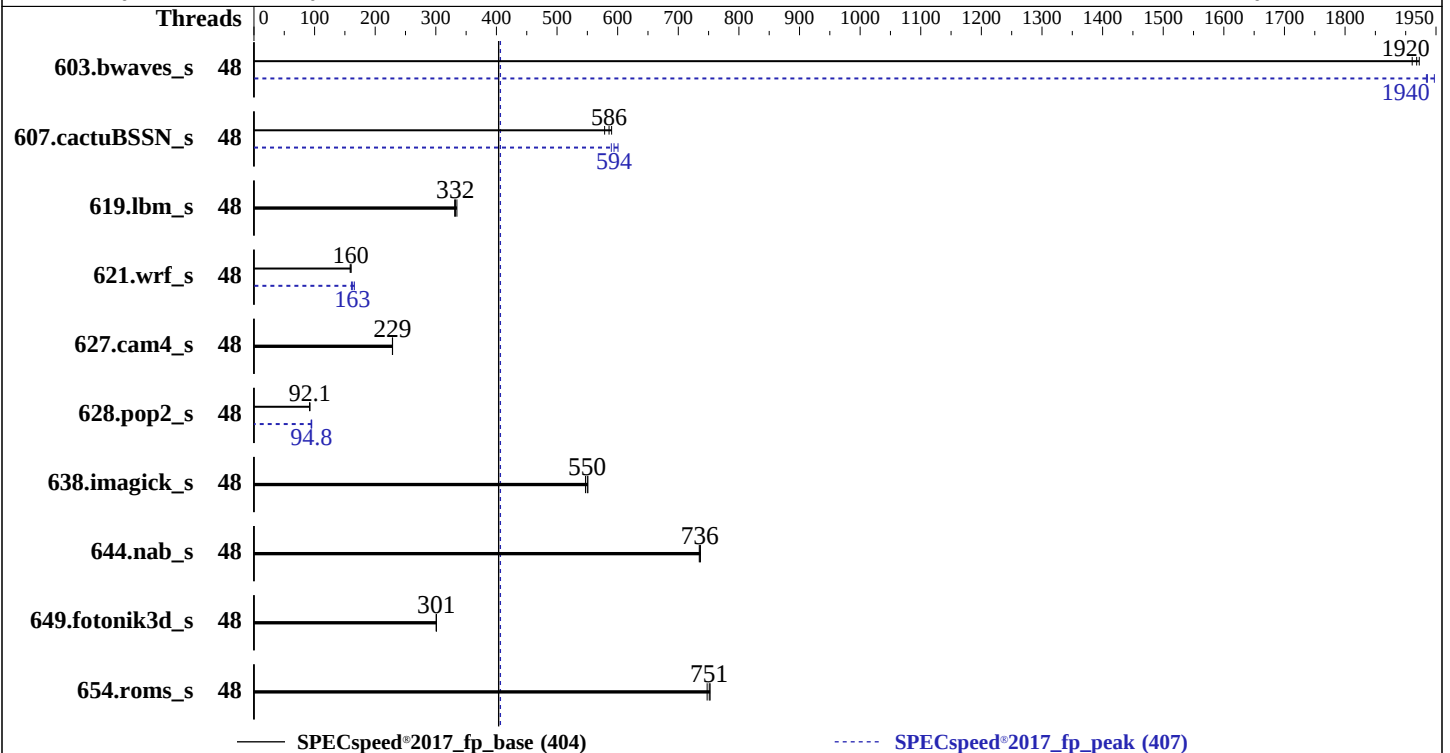
Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jan-2025

Hardware Availability: Oct-2024

Software Availability: Oct-2024



### Hardware

CPU Name: AMD EPYC 9275F  
Max MHz: 4800  
Nominal: 4100  
Enabled: 48 cores, 2 chips  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 256 MB I+D on chip per chip, 32 MB shared / 3 cores  
Other: None  
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R, running at 6000)  
Storage: 1 x 960 GB SATA SSD  
Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP6  
kernel version 6.4.0-150600.21-default  
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
Parallel: Yes  
Firmware: Version 4.3.5c released Dec-2024  
File System: btrfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: None  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

| Benchmark       | Base    |                    |                   |                    |                   |                    |                    | Peak    |                    |                   |                    |                   |                    |                    |
|-----------------|---------|--------------------|-------------------|--------------------|-------------------|--------------------|--------------------|---------|--------------------|-------------------|--------------------|-------------------|--------------------|--------------------|
|                 | Threads | Seconds            | Ratio             | Seconds            | Ratio             | Seconds            | Ratio              | Threads | Seconds            | Ratio             | Seconds            | Ratio             | Seconds            | Ratio              |
| 603.bwaves_s    | 48      | 30.7               | 1920              | 30.9               | 1910              | <b><u>30.8</u></b> | <b><u>1920</u></b> | 48      | 30.3               | 1950              | 30.5               | 1930              | <b><u>30.5</u></b> | <b><u>1940</u></b> |
| 607.cactuBSSN_s | 48      | 28.3               | 590               | 28.8               | 579               | <b><u>28.5</u></b> | <b><u>586</u></b>  | 48      | <b><u>28.1</u></b> | <b><u>594</u></b> | 27.8               | 601               | 28.3               | 590                |
| 619.lbm_s       | 48      | 15.6               | 335               | <b><u>15.8</u></b> | <b><u>332</u></b> | 15.8               | 331                | 48      | 15.6               | 335               | <b><u>15.8</u></b> | <b><u>332</u></b> | 15.8               | 331                |
| 621.wrf_s       | 48      | 83.3               | 159               | <b><u>82.8</u></b> | <b><u>160</u></b> | 82.6               | 160                | 48      | 82.3               | 161               | 79.8               | 166               | <b><u>81.2</u></b> | <b><u>163</u></b>  |
| 627.cam4_s      | 48      | <b><u>38.8</u></b> | <b><u>229</u></b> | 38.8               | 228               | 38.8               | 229                | 48      | <b><u>38.8</u></b> | <b><u>229</u></b> | 38.8               | 228               | 38.8               | 229                |
| 628.pop2_s      | 48      | 129                | 92.2              | 130                | 91.6              | <b><u>129</u></b>  | <b><u>92.1</u></b> | 48      | 126                | 94.4              | 125                | 95.3              | <b><u>125</u></b>  | <b><u>94.8</u></b> |
| 638.imagick_s   | 48      | 26.2               | 551               | 26.4               | 547               | <b><u>26.2</u></b> | <b><u>550</u></b>  | 48      | 26.2               | 551               | 26.4               | 547               | <b><u>26.2</u></b> | <b><u>550</u></b>  |
| 644.nab_s       | 48      | <b><u>23.8</u></b> | <b><u>736</u></b> | 23.7               | 737               | 23.8               | 735                | 48      | <b><u>23.8</u></b> | <b><u>736</u></b> | 23.7               | 737               | 23.8               | 735                |
| 649.fotonik3d_s | 48      | 30.3               | 301               | 30.3               | 301               | <b><u>30.3</u></b> | <b><u>301</u></b>  | 48      | 30.3               | 301               | 30.3               | 301               | <b><u>30.3</u></b> | <b><u>301</u></b>  |
| 654.roms_s      | 48      | <b><u>21.0</u></b> | <b><u>751</u></b> | 21.1               | 748               | 20.9               | 753                | 48      | <b><u>21.0</u></b> | <b><u>751</u></b> | 21.1               | 748               | 20.9               | 753                |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-47"

LD\_LIBRARY\_PATH =

"/home/cpu2017/amd\_speed\_aocc500\_znver5\_A\_lib/lib:/home/cpu2017/amd\_speed\_aocc500\_znver5\_A\_lib/lib32:"

LIBOMP\_NUM\_HIDDEN\_HELPER\_THREADS = "0"

MALLOC\_CONF = "retain:true"

OMP\_DYNAMIC = "false"

OMP\_SCHEDULE = "static"

OMP\_STACKSIZE = "128M"

OMP\_THREAD\_LIMIT = "48"

Environment variables set by runcpu during the 603.bwaves\_s peak run:

GOMP\_CPU\_AFFINITY = "0-47"

Environment variables set by runcpu during the 607.cactuBSSN\_s peak run:

GOMP\_CPU\_AFFINITY = "0-47"

Environment variables set by runcpu during the 621.wrf\_s peak run:

GOMP\_CPU\_AFFINITY = "0-47"

Environment variables set by runcpu during the 628.pop2\_s peak run:

GOMP\_CPU\_AFFINITY = "0-47"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS settings:

SMT Mode set to Disabled

NUMA nodes per socket set to NPS1

Determinism Slider set to Power

DF C-States set to Disabled

Enhanced CPU performance set to Auto

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Fri May 23 12:16:12 2025

SUT (System Under Test) info as seen by some common utilities.

-----  
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- 1. uname -a  
2. w  
3. Username

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## Platform Notes (Continued)

```
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
```

```
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
12:16:12 up 2:43, 3 users, load average: 0.00, 1.09, 1.88
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU   WHAT
root      tty1      -             09:40    20.00s 0.77s  0.07s /bin/bash ./amd_speed_aocc500_znver5_A1.sh
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 6190885
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 6190885
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
```

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## Platform Notes (Continued)

```
python3 ./run_amd_speed_aocc500_znver5_A1.py -b fpspeed
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/temlogs/preenv.fpspeed.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

### 6. /proc/cpuinfo

```
model name      : AMD EPYC 9275F 24-Core Processor
vendor_id       : AuthenticAMD
cpu family      : 26
model           : 2
stepping        : 1
microcode       : 0xb00211a
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 24
siblings        : 24
2 physical ids (chips)
48 processors (hardware threads)
physical id 0:  core ids 0-2,16-18,32-34,48-50,64-66,80-82,96-98,112-114
physical id 1:  core ids 0-2,16-18,32-34,48-50,64-66,80-82,96-98,112-114
physical id 0:  apicids 0-2,16-18,32-34,48-50,64-66,80-82,96-98,112-114
physical id 1:  apicids 128-130,144-146,160-162,176-178,192-194,208-210,224-226,240-242
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.39.3:

|                      |                                                                                                                                                                                                                                                                                           |
|----------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Architecture:        | x86_64                                                                                                                                                                                                                                                                                    |
| CPU op-mode(s):      | 32-bit, 64-bit                                                                                                                                                                                                                                                                            |
| Address sizes:       | 52 bits physical, 57 bits virtual                                                                                                                                                                                                                                                         |
| Byte Order:          | Little Endian                                                                                                                                                                                                                                                                             |
| CPU(s):              | 48                                                                                                                                                                                                                                                                                        |
| On-line CPU(s) list: | 0-47                                                                                                                                                                                                                                                                                      |
| Vendor ID:           | AuthenticAMD                                                                                                                                                                                                                                                                              |
| BIOS Vendor ID:      | Advanced Micro Devices, Inc.                                                                                                                                                                                                                                                              |
| Model name:          | AMD EPYC 9275F 24-Core Processor                                                                                                                                                                                                                                                          |
| BIOS Model name:     | AMD EPYC 9275F 24-Core Processor                                                                                                                                                                                                                                                          |
| BIOS CPU family:     | 107                                                                                                                                                                                                                                                                                       |
| CPU family:          | 26                                                                                                                                                                                                                                                                                        |
| Model:               | 2                                                                                                                                                                                                                                                                                         |
| Thread(s) per core:  | 1                                                                                                                                                                                                                                                                                         |
| Core(s) per socket:  | 24                                                                                                                                                                                                                                                                                        |
| Socket(s):           | 2                                                                                                                                                                                                                                                                                         |
| Stepping:            | 1                                                                                                                                                                                                                                                                                         |
| Frequency boost:     | enabled                                                                                                                                                                                                                                                                                   |
| CPU(s) scaling MHz:  | 79%                                                                                                                                                                                                                                                                                       |
| CPU max MHz:         | 4816.6992                                                                                                                                                                                                                                                                                 |
| CPU min MHz:         | 1500.0000                                                                                                                                                                                                                                                                                 |
| BogoMIPS:            | 8188.10                                                                                                                                                                                                                                                                                   |
| Flags:               | fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat<br>pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb<br>rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid<br>extd_apicid aperfmpperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid |

Unknown CPU @ 4.1GHz

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## Platform Notes (Continued)

sse4\_1 sse4\_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf\_lm  
cmp\_legacy svm extapic cr8\_legacy abm sse4a misalignsse 3dnowprefetch  
osvw ibs skinit wdt tce topoext perfctr\_core perfctr\_nb bpeext  
perfctr\_llc mwaitx cpb cat\_l3 cdp\_l3 hw\_pstate ssbd mba perfmon\_v2  
ibrs ibpb stibp ibrs\_enhanced vmmcall fsgsbase tsc\_adjust bmi1 avx2  
smep bmi2 erms invpcid cqm rdt\_a avx512f avx512dq rdseed adx smap  
avx512ifma clflushopt clwb avx512cd sha\_ni avx512bw avx512vl xsaveopt  
xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total  
cqm\_mbm\_local user\_shstk avx\_vnni avx512\_bf16 clzero irperf  
xsaveerptr rdpru wbnoinvd amd\_ppin cппc arat npt lbrv svm\_lock  
nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter  
pfthreshold avic v\_vmsave\_vmload vgif x2avic v\_spec\_ctrl vnmi  
avx512vbmi umip pku ospke avx512\_vbmi2 gfni vaes vpclmulqdq  
avx512\_vnni avx512\_bitalg avx512\_vpopcntdq la57 rdpid bus\_lock\_detect  
movdiri movdir64b overflow\_recov succor smca fsrm avx512\_vp2intersect  
flush\_l1d debug\_swap

AMD-V

2.3 MiB (48 instances)

1.5 MiB (48 instances)

48 MiB (48 instances)

512 MiB (16 instances)

2

0-23

24-47

Not affected

Not affected

Not affected

Not affected

Not affected

Not affected

Not affected

Not affected

Not affected

Mitigation; Speculative Store Bypass disabled via prctl

Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization

Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP

disabled; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected

Not affected

Not affected

Virtualization:

L1d cache:

L1i cache:

L2 cache:

L3 cache:

NUMA node(s):

NUMA node0 CPU(s):

NUMA node1 CPU(s):

Vulnerability Gather data sampling:

Vulnerability Itlb multihit:

Vulnerability L1tf:

Vulnerability Mds:

Vulnerability Meltdown:

Vulnerability Mmio stale data:

Vulnerability Reg file data sampling:

Vulnerability Retbleed:

Vulnerability Spec rstack overflow:

Vulnerability Spec store bypass:

Vulnerability Spectre v1:

Vulnerability Spectre v2:

Vulnerability Srbds:

Vulnerability Tsx async abort:

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 48K      | 2.3M     | 12   | Data        | 1     | 64    | 1        | 64             |
| L1i  | 32K      | 1.5M     | 8    | Instruction | 1     | 64    | 1        | 64             |
| L2   | 1M       | 48M      | 16   | Unified     | 2     | 1024  | 1        | 64             |
| L3   | 32M      | 512M     | 16   | Unified     | 3     | 32768 | 1        | 64             |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-23

node 0 size: 773682 MB

node 0 free: 772594 MB

node 1 cpus: 24-47

node 1 size: 774065 MB

node 1 free: 772809 MB

node distances:

node 0 1

0: 10 32

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## Platform Notes (Continued)

1: 32 10

9. /proc/meminfo

MemTotal: 1584894224 kB

10. who -r

run-level 3 May 23 09:32

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target Status  
multi-user running

12. Services, from systemctl list-unit-files

STATE UNIT FILES

enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi

issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections

nvmmf-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore

virtqemu wickd wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny

enabled-runtime systemd-remount-fs

disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait  
chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info  
firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmiev d iscsi-init iscsid  
issue-add-ssh-keys kexec-load ksm kvm\_stat libvirt-guests lunmask man-db-create multipathd  
munge nfs nfs-blkmap nfs-server nfsserver rpcbind rpmconfigcheck rsyncd rtkit-daemon  
salt-minion serial-getty@ slurmd smartd\_generate\_opts snmpd snmptrapd strongswan  
strongswan-starter svnserv systemd-boot-check-no-failures systemd-confext  
systemd-network-generator systemd-nspawn@ systemd-sysex systemd-time-wait-sync  
systemd-timesyncd tcdd udisks2 virtinterfaced virtlockd virtlogd virtnetworkd virtnodevdev  
virtnwfilterd virtsecret virtstorage ypbind

indirect pcsd systemd-userdbd tftpd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT\_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default

root=UUID=a2d5841a-0efc-41c7-a352-ff6ddae8fa0b

splash=silent

mitigations=auto

quiet

security=apparmor

14. cpupower frequency-info

analyzing CPU 24:

current policy: frequency should be within 1.50 GHz and 4.10 GHz.

The governor "performance" may decide which speed to use  
within this range.

boost state support:

Supported: yes

Active: yes

15. sysctl

kernel.numa\_balancing 1

kernel.randomize\_va\_space 0

vm.compaction\_proactiveness 20

vm.dirty\_background\_bytes 0

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### Platform Notes (Continued)

```
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvice madvice never
enabled     [always] madvice never
hpage_pmd_size  2097152
shmem_enabled  always within_size advise [never] deny force
-----
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs    10000
-----
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
-----
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/nvme1n1p3  btrfs 477G   16G  458G   4% /home
-----
```

```
-----
20. /sys/devices/virtual/dmi/id
Vendor:      Cisco Systems Inc
Product:     UCSX-215C-M8
Product Family: Cisco UCS Rack Server
Serial:      FCH282172GT
-----
```

```
-----
21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
24x 0xCE00 M321R8GA0PB2-CCPKC 64 GB 2 rank 6400, configured at 6000
-----
```

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## Cisco Systems

Cisco UCS X215c M8 (AMD EPYC 9275F  
4.10 GHz Processor)

SPECspeed®2017\_fp\_base = 404

SPECspeed®2017\_fp\_peak = 407

CPU2017 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jan-2025

Hardware Availability: Oct-2024

Software Availability: Oct-2024

## Platform Notes (Continued)

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Cisco Systems, Inc.  
BIOS Version: X215M8.4.3.5c.0.1202241033  
BIOS Date: 12/02/2024  
BIOS Revision: 5.35

## Compiler Version Notes

C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak) 654.roms\_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak) 628.pop2\_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin



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CPU2017 License: 9019

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Tested by: Cisco Systems

Test Date: Jan-2025

Hardware Availability: Oct-2024

Software Availability: Oct-2024

## Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
627.cam4\_s: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
628.pop2\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC\_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp  
-lamdlibm -lamdalloc -lflang

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC\_OPENMP -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3

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## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc  
-lflang

Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC\_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -funroll-loops  
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp  
-lomp -lamdlibm -lamdalloc -lflang

Benchmarks using Fortran, C, and C++:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC\_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt  
-mllvm -loop-unswitch-threshold=2000000 -mllvm -unroll-threshold=100  
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none  
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

## Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument



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Software Availability: Oct-2024

## Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: basepeak = yes

644.nab\_s: basepeak = yes

Fortran benchmarks:

603.bwaves\_s: -m64 -wl, -mllvm -wl, -align-all-nofallthru-blocks=6

-wl, -mllvm -wl, -reduce-array-computations=3

-wl, -mllvm -wl, -enable-X86-prefetching -DSPEC\_OPENMP

-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math

-fopenmp -fscalar-transform -fvector-transform

-mllvm -reduce-array-computations=3 -Mrecursive

-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

649.fotonik3d\_s: basepeak = yes

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

(Continued on next page)



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## Peak Optimization Flags (Continued)

```
621.wrf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang
```

627.cam4\_s: basepeak = yes

```
628.pop2_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fscalar-transform
-fvector-transform -Mrecursive -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -DSPEC_OPENMP
-fremap-arrays -fstrip-mining -fstruct-layout=9
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mllvm -unroll-threshold=100
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

## Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

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## Peak Other Flags (Continued)

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.html>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.xml>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.xml>

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