



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS C225 M8 (AMD EPYC 9355P 32-Core processor)

SPECSpeed®2017_fp_base = 310

SPECSpeed®2017_fp_peak = 319

CPU2017 License: 9019

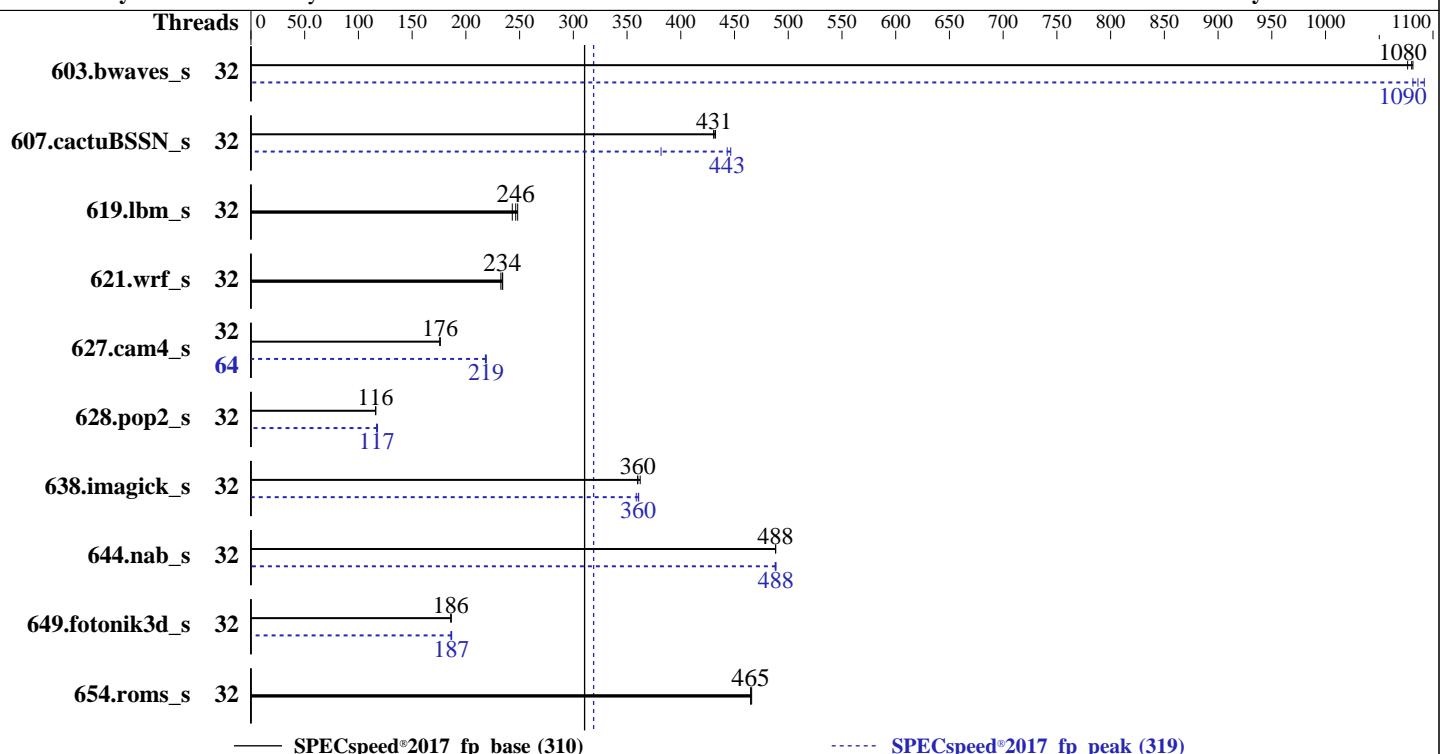
Test Date: Apr-2025

Test Sponsor: Cisco Systems

Hardware Availability: Oct-2024

Tested by: Cisco Systems

Software Availability: Oct-2024



— SPECSpeed®2017_fp_base (310)

----- SPECSpeed®2017_fp_peak (319)

Hardware

CPU Name: AMD EPYC 9355P
 Max MHz: 4400
 Nominal: 3550
 Enabled: 32 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 256 MB I+D on chip per chip, 32 MB shared / 4 cores
 Other: None
 Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 6000)
 Storage: 1 x 240 GB M.2 SSD
 Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
 Compiler: kernel version 6.4.0-150600.21-default
 Parallel: C/C++/Fortran: Version 5.0.0 of AOCC
 Firmware: Yes
 File System: Version 4.3.5d released Feb-2025
 System State: btrfs
 Base Pointers: Run level 3 (multi-user)
 Peak Pointers: 64-bit
 Other: 64-bit
 Power Management: None
 BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	32	54.6	1080	54.8	1080	54.6	1080	32	54.6	1080	54.0	1090	54.3	1090		
607.cactuBSSN_s	32	38.7	431	38.6	432	38.7	431	32	43.7	382	37.3	446	37.6	443		
619.lbm_s	32	21.5	243	21.1	248	21.3	246	32	21.5	243	21.1	248	21.3	246		
621.wrf_s	32	56.9	233	56.5	234	56.5	234	32	56.9	233	56.5	234	56.5	234		
627.cam4_s	32	50.3	176	50.4	176	50.4	176	64	40.5	219	40.5	219	40.6	218		
628.pop2_s	32	102	116	102	116	102	116	32	102	117	101	118	101	117		
638.imagick_s	32	39.8	362	40.1	360	40.1	360	32	40.2	359	40.0	361	40.0	360		
644.nab_s	32	35.8	488	35.8	488	35.8	488	32	35.8	489	35.8	488	35.8	488		
649.fotonik3d_s	32	48.9	186	49.0	186	49.0	186	32	48.8	187	48.8	187	49.0	186		
654.roms_s	32	33.9	465	33.8	466	33.9	465	32	33.9	465	33.8	466	33.9	465		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
 To free node-local memory and avoid remote memory usage,
 'sysctl -w vm.zone_reclaim_mode=1' run as root.
 To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
 To disable address space layout randomization (ASLR) to reduce run-to-run
 variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,
 'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
 To enable THP for all allocations for peak runs,
 'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
 'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-63"  
LD_LIBRARY_PATH =  
    "/home/cpu2017/amd_speed_aocc500_znver5_A/lib:/home/cpu2017/amd_speed_aocc500_znver5_A/lib/lib32:  
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"  
MALLOC_CONF = "retain:true"  
OMP_DYNAMIC = "false"  
OMP_SCHEDULE = "static"  
OMP_STACKSIZE = "128M"  
OMP_THREAD_LIMIT = "64"
```

Environment variables set by runcpu during the 603.bwaves_s peak run:

```
GOMP_CPU_AFFINITY = "0-31"
```

Environment variables set by runcpu during the 607.cactuBSSN_s peak run:

```
GOMP_CPU_AFFINITY = "0-31"
```

Environment variables set by runcpu during the 627.cam4_s peak run:

```
GOMP_CPU_AFFINITY = "0-63"
```

Environment variables set by runcpu during the 628.pop2_s peak run:

```
GOMP_CPU_AFFINITY = "0-31"
```

Environment variables set by runcpu during the 638.imagick_s peak run:

```
GOMP_CPU_AFFINITY = "0-31"
```

Environment variables set by runcpu during the 644.nab_s peak run:

```
GOMP_CPU_AFFINITY = "0-31"
```

Environment variables set by runcpu during the 649.fotonik3d_s peak run:

```
GOMP_CPU_AFFINITY = "0-31"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:

NUMA nodes per socket set to NPS1

Determinism Slider set to Power

DF C-States set to Disabled

```
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Tue Apr 15 05:13:24 2025
```

SUT (System Under Test) info as seen by some common utilities.

(Continued on next page)



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Platform Notes (Continued)

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2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

```
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36cle09)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
05:13:24 up 4 min, 1 user, load average: 0.00, 0.00, 0.00
USER      TTY      FROM             LOGIN@     IDLE     JCPU    PCPU WHAT
root      ttys1     -          05:12    20.00s  0.77s  0.04s /bin/bash ./amd_speed_aocc500_znver5_A1.sh
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals          (-i) 3092761
max locked memory        (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority       (-r) 0
stack size                (kbytes, -s) unlimited
cpu time                  (seconds, -t) unlimited
max user processes        (-u) 3092761
```

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Platform Notes (Continued)

```
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
python3 ./run_amd_speed_aocc500_znver5_A1.py -b fpspeed
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.fpspeed.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

-----
6. /proc/cpuinfo
model name      : AMD EPYC 9355P 32-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 26
model          : 2
stepping        : 1
microcode       : 0xb00211e
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 32
siblings        : 64
1 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-3,8-11,16-19,24-27,32-35,40-43,48-51,56-59
physical id 0: apicids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

-----
7. lscpu

From lscpu from util-linux 2.39.3:
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:              Little Endian
CPU(s):                 64
On-line CPU(s) list:    0-63
Vendor ID:               AuthenticAMD
BIOS Vendor ID:         Advanced Micro Devices, Inc.
Model name:              AMD EPYC 9355P 32-Core Processor
BIOS Model name:         AMD EPYC 9355P 32-Core Processor
BIOS CPU family:         107
CPU family:              26
Model:                  2
Thread(s) per core:     2
Core(s) per socket:      32
Socket(s):               1
Stepping:                1
Frequency boost:         enabled
CPU(s) scaling MHz:     78%
CPU max MHz:             4413.2319
                                         Unknown CPU @ 3.5GHz
```

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Platform Notes (Continued)

```

CPU min MHz: 1500.0000
BogoMIPS: 7088.86
Flags:
fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmil avx2
smep bmi2 erms invpcid cqmqrdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqmqllc cqmqoccup_llc cqmqmbm_total
cqmqmbm_local user_shst avx_vnni avx512_bf16 clzero iperf
xsaverptr rdpru wbnoinvd amd_ppin cpc_arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists_pausefilter
pfthreshold avic v_vmsave_vmlload vgif x2avic v_spec_ctrl vnmi
avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_lll debug_swap

L1d cache: 1.5 MiB (32 instances)
L1i cache: 1 MiB (32 instances)
L2 cache: 32 MiB (32 instances)
L3 cache: 256 MiB (8 instances)
NUMA node(s): 1
NUMA node0 CPU(s): 0-63
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability Llft: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.5M	12	Data	1	64	1	64
L1i	32K	1M	8	Instruction	1	64	1	64
L2	1M	32M	16	Unified	2	1024	1	64
L3	32M	256M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)

node 0 cpus: 0-63

node 0 size: 773217 MB

node 0 free: 772205 MB

node distances:

node 0

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Platform Notes (Continued)

0: 10

9. /proc/meminfo
MemTotal: 791774992 kB

10. who -r
run-level 3 Apr 15 05:09

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target Status
multi-user running

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi
issue-generator kbdsettings klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog
smartd sshd systemd-pstore virtqemud wickedd wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronynd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info
firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievrd iscsi-init iscsid
issue-add-ssh-keys kexec-load ksm kvm kvm_stat libvirt-guests lunmask man-db-create multipathd
munge nfs nfs-blkmap nfs-server nfsserver rpcbind rpmconfigcheck rsyncd salt-minion
serial-getty@ slurmd smartd_generate_opts snmpd snmptrapd strongswan strongswan-starter
svnserve systemd-boot-check-no-failures systemd-confcontext systemd-network-generator
systemd-nspawn@ systemd-sysexit systemd-time-wait-sync systemd-timesyncd tcsd tuned udisks2
virtinterfaced virtlockd virtlogd virtnetworkd virtnodeudev virtnwfilterd virtsecretd
virtstoraged yplibd
indirect pcscd systemd-userdbd tftp wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=62bf82ca-659e-4401-8eca-cfbfb4d3e5700
amd_pstate=disable

14. cpupower frequency-info
analyzing CPU 0:
current policy: frequency should be within 1.50 GHz and 3.55 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes

15. tuned-adm active
It seems that tuned daemon is not running, preset profile is not activated.
Preset profile: latency-performance

16. sysctl
kernel.numa_balancing 0
kernel.randomize_va_space 0

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Platform Notes (Continued)

```
vm.compaction_proactiveness          20
vm.dirty_background_bytes            0
vm.dirty_background_ratio           10
vm.dirty_bytes                      0
vm.dirty_expire_centisecs          3000
vm.dirty_ratio                      8
vm.dirty_writeback_centisecs       500
vm.dirtytime_expire_seconds        43200
vm.extfrag_threshold                500
vm.min_unmapped_ratio              1
vm.nr_hugepages                     0
vm.nr_hugepages_mempolicy          0
vm.nr_overcommit_hugepages         0
vm.swappiness                        1
vm.watermark_boost_factor          15000
vm.watermark_scale_factor          10
vm.zone_reclaim_mode               1

-----
17. /sys/kernel/mm/transparent_hugepage
    defrag      [always] defer defer+madvise madvise never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled always within_size advise [never] deny force

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs   60000
    defrag                  1
    max_ptes_none           511
    max_ptes_shared          256
    max_ptes_swap             64
    pages_to_scan            4096
    scan_sleep_millisecs    10000

-----
19. OS release
    From /etc/*-release /etc/*-version
    os-release SUSE Linux Enterprise Server 15 SP6

-----
20. Disk information
    SPEC is set to: /home/cpu2017
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sda2        btrfs  222G  8.7G  209G  4%  /home

-----
21. /sys/devices/virtual/dmi/id
    Vendor:          Cisco Systems Inc
    Product:         UCSC-C225-M8S
    Product Family: Cisco UCS Rack Server
    Serial:          WZP28199HWN

-----
22. dmidecode
    Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
    The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
    "DMTF SMBIOS" standard.
    Memory:
```

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Platform Notes (Continued)

12x 0xAD00 HMCG94AHBRA480N 64 GB 2 rank 6400, configured at 6000

23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: Cisco Systems, Inc.
BIOS Version: C225M8.4.3.5d.0.0206250854
BIOS Date: 02/06/2025
BIOS Revision: 5.35

Compiler Version Notes

=====

C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C, Fortran | 607.cactusBSSN_s(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
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AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
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=====

Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

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SPECspeed®2017_fp_peak = 319

CPU2017 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Apr-2025

Hardware Availability: Oct-2024

Software Availability: Oct-2024

Compiler Version Notes (Continued)

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapi -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapi -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIB -ffast-math -fopenmp -DSPEC_OPENMP -fsto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp
-lamdlibm -lamdaloc -lflang

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

(Continued on next page)



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Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3  
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc  
-lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -funroll-loops  
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp  
-lomp -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt  
-mllvm -loop-unswitch-threshold=200000 -mllvm -unroll-threshold=100  
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none  
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -Wno-unused-command-line-argument
```



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Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

644.nab_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Fortran benchmarks:

(Continued on next page)



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Peak Optimization Flags (Continued)

603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -fscalar-transform -fvector-transform
-mllvm -reduce-array-computations=3 -Mrecursive
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

649.fotonik3d_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -flto -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm
-lamdalloc -lflang

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -Mrecursive
-mrecip=none -fopenmp=libomp -lomp -lamdlibm -lamdallic
-lflang

628.pop2_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fscalar-transform
-fvector-transform -Mrecursive -fopenmp=libomp -lomp
-lamdlibm -lamdallic -lflang

Benchmarks using Fortran, C, and C++:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

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Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -DSPEC_OPENMP  
-fremap-arrays -fstrip-mining -fstruct-layout=9  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -mllvm -unroll-threshold=100  
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Peak Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.html>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.xml>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.xml>

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