



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8 (AMD EPYC 9535  
2.40 GHz Processor)

**SPECrate®2017\_int\_base = 1420**

**SPECrate®2017\_int\_peak = 1450**

CPU2017 License: 9019

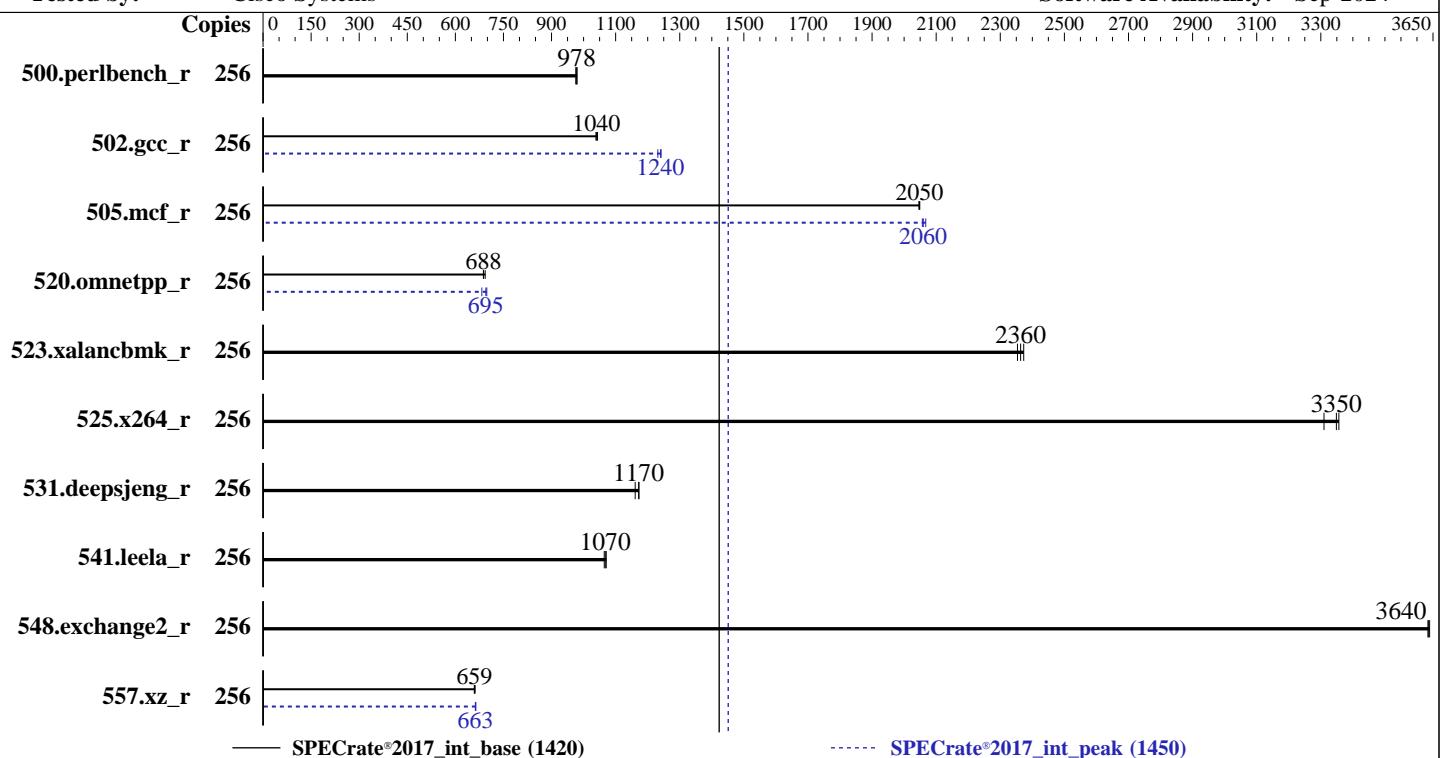
Test Sponsor: Cisco Systems

Tested by: Cisco Systems

**Test Date:** Feb-2025

**Hardware Availability:** Oct-2024

**Software Availability:** Sep-2024



| Hardware   |  | Software          |  |
|------------|--|-------------------|--|
| CPU Name:  | AMD EPYC 9535  | OS:               | SUSE Linux Enterprise Server 15 SP6  |
| Max MHz:   | 4300   |                   | kernel version   |
| Nominal:   | 2400   |                   | 6.4.0-150600.21-default  |
| Enabled:   | 128 cores, 2 chips, 2 threads/core                     | Compiler:         | C/C++/Fortran: Version 5.0.0 of AOCC   |
| Orderable: | 1,2 chips  | Parallel:         | No   |
| Cache L1:  | 32 KB I + 48 KB D on chip per core                     | Firmware:         | Version 4.3.5f released Feb-2025   |
| L2:        | 1 MB I+D on chip per core                              | File System:      | btrfs  |
| L3:        | 256 MB I+D on chip per chip, 16 MB shared / 4 cores    | System State:     | Run level 3 (multi-user)   |
| Other:     | None   | Base Pointers:    | 64-bit   |
| Memory:    | 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R, running at 6000) | Peak Pointers:    | 32/64-bit  |
| Storage:   | 1 x 960 GB SATA SSD                                    | Other:            | None   |
| Other:     | CPU Cooling: Air                                       | Power Management: | BIOS and OS set to prefer performance at the cost of additional power usage. |



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## Results Table

| Benchmark       | Base   |            |            |            |             |            |             |            | Peak       |             |            |             |            |             |         |       |
|-----------------|--------|------------|------------|------------|-------------|------------|-------------|------------|------------|-------------|------------|-------------|------------|-------------|---------|-------|
|                 | Copies | Seconds    | Ratio      | Seconds    | Ratio       | Seconds    | Ratio       | Copies     | Seconds    | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       | Seconds | Ratio |
| 500.perlbench_r | 256    | 416        | 980        | 418        | 976         | <b>417</b> | <b>978</b>  | 256        | 416        | 980         | 418        | 976         | <b>417</b> | <b>978</b>  |         |       |
| 502.gcc_r       | 256    | 347        | 1040       | <b>348</b> | <b>1040</b> | 349        | 1040        | <b>256</b> | <b>292</b> | <b>1240</b> | 294        | 1230        | 292        | 1240        |         |       |
| 505.mcf_r       | 256    | 202        | 2050       | <b>202</b> | <b>2050</b> | 202        | 2050        | <b>256</b> | <b>200</b> | <b>2070</b> | 201        | 2060        | <b>201</b> | <b>2060</b> |         |       |
| 520.omnetpp_r   | 256    | <b>488</b> | <b>688</b> | 485        | 693         | 489        | 687         | <b>256</b> | <b>483</b> | <b>695</b>  | 492        | 683         | 481        | 698         |         |       |
| 523.xalancbmk_r | 256    | 115        | 2350       | 114        | 2370        | <b>114</b> | <b>2360</b> | <b>256</b> | 115        | 2350        | 114        | 2370        | <b>114</b> | <b>2360</b> |         |       |
| 525.x264_r      | 256    | 135        | 3310       | <b>134</b> | <b>3350</b> | 134        | 3360        | <b>256</b> | 135        | 3310        | <b>134</b> | <b>3350</b> | 134        | 3360        |         |       |
| 531.deepsjeng_r | 256    | 253        | 1160       | 250        | 1170        | <b>250</b> | <b>1170</b> | <b>256</b> | 253        | 1160        | 250        | 1170        | <b>250</b> | <b>1170</b> |         |       |
| 541.leela_r     | 256    | 396        | 1070       | <b>397</b> | <b>1070</b> | 398        | 1060        | <b>256</b> | 396        | 1070        | <b>397</b> | <b>1070</b> | 398        | 1060        |         |       |
| 548.exchange2_r | 256    | 185        | 3630       | <b>185</b> | <b>3640</b> | 184        | 3640        | <b>256</b> | 185        | 3630        | <b>185</b> | <b>3640</b> | 184        | 3640        |         |       |
| 557.xz_r        | 256    | 418        | 662        | <b>419</b> | <b>659</b>  | 420        | 659         | <b>256</b> | <b>417</b> | <b>663</b>  | 416        | 664         | 417        | 662         |         |       |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.  
To enable THP for all allocations for peak runs,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS settings:

NUMA nodes per socket set to NPS4  
Determinism Slider set to Power  
DF C-States set to Disabled  
Enhanced CPU performance set to Auto

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Fri Apr 25 06:18:19 2025
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode

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## Platform Notes (Continued)

22. BIOS

```
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
06:18:19 up 3 min, 3 users, load average: 0.19, 0.19, 0.09
USER      TTY      FROM             LOGIN@     IDLE    JCPU    PCPU WHAT
root      tty1      -           06:15    27.00s  1.00s  0.13s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals          (-i) 6190347
max locked memory        (kbytes, -l) 2097152
max memory size          (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority       (-r) 0
stack size                (kbytes, -s) unlimited
cpu time                  (seconds, -t) unlimited
max user processes        (-u) 6190347
virtual memory            (kbytes, -v) unlimited
file locks               (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
python3 ./run_amd_rate_aocc500_znver5_A1.py -b intrate
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 intrate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
6. /proc/cpuinfo
model name      : AMD EPYC 9535 64-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 26
model          : 2
stepping        : 1
microcode      : 0xb00211e
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
```

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## Platform Notes (Continued)

```
TLB size      : 192 4K pages
cpu cores    : 64
siblings     : 128
2 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids
0-3,8-11,16-19,24-27,32-35,40-43,48-51,56-59,64-67,72-75,80-83,88-91,96-99,104-107,112-115,120-123
physical id 1: core ids
0-3,8-11,16-19,24-27,32-35,40-43,48-51,56-59,64-67,72-75,80-83,88-91,96-99,104-107,112-115,120-123
physical id 0: apicids
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,
240-247
physical id 1: apicids
256-263,272-279,288-295,304-311,320-327,336-343,352-359,368-375,384-391,400-407,416-423,432-439,448-455,4
64-471,480-487,496-503
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.39.3:

|                      |   |
|----------------------|---|
| Architecture:        | x86_64  |
| CPU op-mode(s):      | 32-bit, 64-bit  |
| Address sizes:       | 52 bits physical, 57 bits virtual   |
| Byte Order:          | Little Endian   |
| CPU(s):              | 256   |
| On-line CPU(s) list: | 0-255   |
| Vendor ID:           | AuthenticAMD  |
| BIOS Vendor ID:      | Advanced Micro Devices, Inc.  |
| Model name:          | AMD EPYC 9535 64-Core Processor   |
| BIOS Model name:     | AMD EPYC 9535 64-Core Processor   |
| BIOS CPU family:     | Unknown CPU @ 2.4GHz  |
| CPU family:          | 107   |
| Model:               | 26  |
| Thread(s) per core:  | 2   |
| Core(s) per socket:  | 64  |
| Socket(s):           | 2   |
| Stepping:            | 1   |
| Frequency boost:     | enabled   |
| CPU(s) scaling MHz:  | 57%   |
| CPU max MHz:         | 4307.8120   |
| CPU min MHz:         | 1500.0000   |
| BogoMIPS:            | 4792.22   |
| Flags:               | fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat<br>pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb<br>rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid<br>extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid<br>sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm<br>cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch<br>osw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext<br>perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2<br>ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2<br>smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap<br>avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt<br>xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total<br>cqmq_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf<br>xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock<br>nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter<br>pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi |

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## Platform Notes (Continued)

```
avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_lld debug_swap
```

Virtualization:

AMD-V

L1d cache:

6 MiB (128 instances)

L1i cache:

4 MiB (128 instances)

L2 cache:

128 MiB (128 instances)

L3 cache:

512 MiB (32 instances)

NUMA node(s):

8

NUMA node0 CPU(s):

0-15,128-143

NUMA node1 CPU(s):

16-31,144-159

NUMA node2 CPU(s):

32-47,160-175

NUMA node3 CPU(s):

48-63,176-191

NUMA node4 CPU(s):

64-79,192-207

NUMA node5 CPU(s):

80-95,208-223

NUMA node6 CPU(s):

96-111,224-239

NUMA node7 CPU(s):

112-127,240-255

Vulnerability Gather data sampling:

Not affected

Vulnerability Itlb multihit:

Not affected

Vulnerability Llft:

Not affected

Vulnerability Mds:

Not affected

Vulnerability Meltdown:

Not affected

Vulnerability Mmio stale data:

Not affected

Vulnerability Reg file data sampling:

Not affected

Vulnerability Retbleed:

Not affected

Vulnerability Spec rstack overflow:

Not affected

Vulnerability Spec store bypass:

Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1:

Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization

Vulnerability Spectre v2:

Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP

always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 48K      | 6M       | 12   | Data        | 1     | 64    | 1        | 64             |
| L1i  | 32K      | 4M       | 8    | Instruction | 1     | 64    | 1        | 64             |
| L2   | 1M       | 128M     | 16   | Unified     | 2     | 1024  | 1        | 64             |
| L3   | 16M      | 512M     | 16   | Unified     | 3     | 16384 | 1        | 64             |

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-15,128-143

node 0 size: 193112 MB

node 0 free: 192699 MB

node 1 cpus: 16-31,144-159

node 1 size: 193488 MB

node 1 free: 193107 MB

node 2 cpus: 32-47,160-175

node 2 size: 193527 MB

node 2 free: 193087 MB

node 3 cpus: 48-63,176-191

node 3 size: 193527 MB

node 3 free: 193081 MB

node 4 cpus: 64-79,192-207

node 4 size: 193527 MB

node 4 free: 193069 MB

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## Platform Notes (Continued)

```
node 5 cpus: 80-95,208-223
node 5 size: 193527 MB
node 5 free: 193098 MB
node 6 cpus: 96-111,224-239
node 6 size: 193527 MB
node 6 free: 193063 MB
node 7 cpus: 112-127,240-255
node 7 size: 193375 MB
node 7 free: 192914 MB
node distances:
node   0   1   2   3   4   5   6   7
  0: 10  12  12  12  32  32  32  32
  1: 12  10  12  12  32  32  32  32
  2: 12  12  10  12  32  32  32  32
  3: 12  12  12  10  32  32  32  32
  4: 32  32  32  32  10  12  12  12
  5: 32  32  32  32  12  10  12  12
  6: 32  32  32  32  12  12  10  12
  7: 32  32  32  32  12  12  12  10

-----
9. /proc/meminfo
MemTotal:      1584756836 kB

-----
10. who -r
run-level 3 Apr 25 06:14

-----
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target    Status
multi-user        running

-----
12. Services, from systemctl list-unit-files
STATE          UNIT FILES
enabled        YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi
                issue-generator kbdsettings klog lvm2-monitor nscd nvmefc-boot-connections
                nvmf-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore
                virtqemud wickedd wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemdr-remount-fs
disabled       autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                chrony console-getty cups cups-browsed debug-shell dnsmasq ebtables exchange-bmc-os-info
                firewalld fsidd gpm grub2-once haveged hv_fcphy_daemon hv_kvp_daemon hv_vss_daemon
                hwloc-dump-hwdata ipmi ipmievfd iscsid issue-add-ssh-keys kexec-load ksm
                kvm_stat libvirt-guests lunmask man-db-create multipathd munge nfs nfs-blkmap nfs-server
                nfsserver rpcbind rpmconfigcheck rsyncd rtkit-daemon salt-minion serial-getty@ slurmd
                smartd_generate_opts snmpd snmptrapd strongswan strongswan-starter svnserve
                systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-nspawn@
                systemd-sysext systemd-time-wait-sync systemd-timesyncd tcasd udisks2 virtinterfaced
                virtlockd virtlogd virtnetworkd virtnodeudevd virtnwfilterd virtsecretd virtstoraged yplibind
indirect       pcscd systemd-userdbd tftp wickedd

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=46707549-98f2-46fd-888c-f84af8b63bd
splash=silent
mitigations=auto
quiet
```

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## Platform Notes (Continued)

security=apparmor

```
14. cpupower frequency-info
analyzing CPU 204:
    current policy: frequency should be within 1.50 GHz and 2.40 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.

    boost state support:
        Supported: yes
        Active: yes
```

```
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space       0
vm.compaction_proactiveness    20
vm.dirty_background_bytes       0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            1
```

```
16. /sys/kernel/mm/transparent_hugepage
defrag           [always] defer defer+madvise madvise never
enabled          [always] madvise never
hpage_pmd_size  2097152
shmem_enabled    always within_size advise [never] deny force
```

```
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared          256
max_ptes_swap            64
pages_to_scan            4096
scan_sleep_millisecs    10000
```

```
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

```
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type     Size   Used  Avail Use% Mounted on
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

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## Cisco Systems

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2.40 GHz Processor)

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SPECrate®2017\_int\_peak = 1450

CPU2017 License: 9019

Test Date: Feb-2025

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Hardware Availability: Oct-2024

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## Platform Notes (Continued)

```
/dev/nvme0n1p2 btrfs 894G 43G 848G 5% /home
```

```
-----  
20. /sys/devices/virtual/dmi/id  
Vendor: Cisco Systems Inc  
Product: UCSX-215C-M8  
Product Family: Cisco UCS Rack Server  
Serial: FCH282172G8
```

```
-----  
21. dmidecode  
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the  
"DMTF SMBIOS" standard.  
Memory:  
16x 0xCE00 M321R8GA0PB2-CCPEC 64 GB 2 rank 6400, configured at 6000  
8x 0xCE00 M321R8GA0PB2-CCPKC 64 GB 2 rank 6400, configured at 6000
```

```
-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Cisco Systems, Inc.  
BIOS Version: X215M8.4.3.5f.0.0218251523  
BIOS Date: 02/18/2025  
BIOS Revision: 5.35
```

## Compiler Version Notes

```
=====| 502.gcc_r(peak)  
-----  
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----
```

```
=====| 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)  
| 557.xz_r(base, peak)  
-----  
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----
```

```
=====| 502.gcc_r(peak)  
-----  
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----
```

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## Compiler Version Notes (Continued)

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Fortran | 548.exchange2\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64

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## Base Portability Flags (Continued)

541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdaloc-ext -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdaloc-ext
-ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500
-lamdlibm -lflang -lamdaloc -ldl
```

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

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## Base Other Flags (Continued)

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: basepeak = yes

502.gcc\_r: -m32 -f1to -Wl,-mllvm -Wl,-ldist-scalar-expand  
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner

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Software Availability: Sep-2024

## Peak Optimization Flags (Continued)

502.gcc\_r (continued):

```
-z muldefs -Ofast -march=znver5 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdaloc
```

505.mcf\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc-ext -ldl

525.x264\_r: basepeak = yes

557.xz\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc-ext -ldl

C++ benchmarks:

520.omnetpp\_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -fno-PIE
-no-pie -fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lamdalloc-ext
-ldl

523.xalancbmk\_r: basepeak = yes

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## Peak Optimization Flags (Continued)

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

## Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

502.gcc\_r: -L/usr/lib32 -Wno-unused-command-line-argument  
-L/home/work/cpu2017/v119/aocc5/1316/amd\_rate\_aocc500\_znver5\_A\_lib/lib32

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.html>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.xml>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.xml>

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