SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

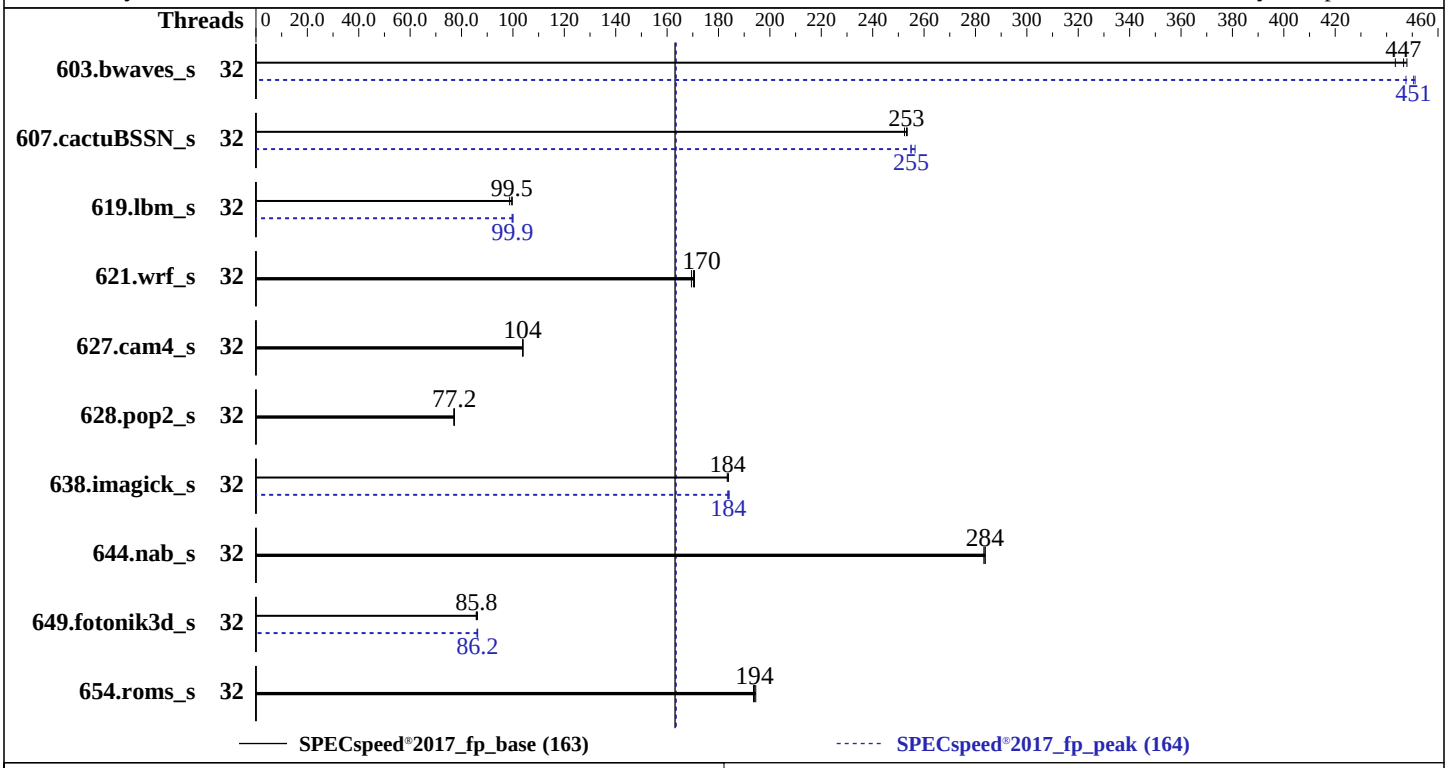
ProLiant DL145 Gen11
(2.05 GHz, AMD EPYC 8324PN)

SPECspeed®2017_fp_base = 163

SPECspeed®2017_fp_peak = 164

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jan-2025
Hardware Availability: Oct-2024
Software Availability: Sep-2024



Hardware

CPU Name:	AMD EPYC 8324PN
Max MHz:	3000
Nominal:	2050
Enabled:	32 cores, 1 chip
Orderable:	1 chip
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	128 MB I+D on chip per chip, 16 MB shared / 4 cores
Other:	None
Memory:	192 GB (6 x 32 GB 2Rx8 PC5-4800B-R)
Storage:	1 x 480 GB SATA SSD
Other:	CPU Cooling: Air

Software

OS:	SUSE Linux Enterprise Server 15 SP6 Kernel 6.4.0-150600.21-default
Compiler:	C/C++/Fortran: Version 5.0.0 of AOCC
Parallel:	Yes
Firmware:	HPE BIOS Version v1.20 08/09/2024 released Aug-2024
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	None
Power Management:	BIOS set to prefer performance at the cost of additional power usage

SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL145 Gen11
(2.05 GHz, AMD EPYC 8324PN)

SPECspeed®2017_fp_base = 163

SPECspeed®2017_fp_peak = 164

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jan-2025
Hardware Availability: Oct-2024
Software Availability: Sep-2024

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	32	132	448	133	443	**132**	**447**	32	131	451	132	448	**131**	**451**
607.cactuBSSN_s	32	65.8	253	**65.8**	**253**	66.1	252	32	65.0	257	**65.4**	**255**	65.4	255
619.lbm_s	32	53.1	98.7	52.5	99.8	**52.6**	**99.5**	32	52.4	100	**52.4**	**99.9**	52.6	99.7
621.wrf_s	32	78.0	169	**77.7**	**170**	77.5	171	32	78.0	169	**77.7**	**170**	77.5	171
627.cam4_s	32	**85.4**	**104**	85.3	104	85.4	104	32	**85.4**	**104**	85.3	104	85.4	104
628.pop2_s	32	154	77.0	**154**	**77.2**	154	77.2	32	154	77.0	**154**	**77.2**	154	77.2
638.imagick_s	32	**78.5**	**184**	78.7	183	78.5	184	32	78.6	184	78.3	184	**78.4**	**184**
644.nab_s	32	**61.6**	**284**	61.6	284	61.7	283	32	**61.6**	**284**	61.6	284	61.7	283
649.fotonik3d_s	32	106	85.8	106	86.2	**106**	**85.8**	32	**106**	**86.2**	106	86.3	106	86.0
654.roms_s	32	81.3	194	**81.1**	**194**	81.0	194	32	81.3	194	**81.1**	**194**	81.0	194

SPECspeed®2017_fp_base = 163

SPECspeed®2017_fp_peak = 164

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-31"
LD_LIBRARY_PATH =
      "/home/CPU2017/amd_speed_aocc500_znver5_A_lib/lib:/home/CPU2017/amd_speed_aocc500_znver5_A_lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "32"

Environment variables set by runcpu during the 603.bwaves_s peak run:
GOMP_CPU_AFFINITY = "0-31"

Environment variables set by runcpu during the 607.cactuBSSN_s peak run:
GOMP_CPU_AFFINITY = "0-31"

Environment variables set by runcpu during the 619.lbm_s peak run:
GOMP_CPU_AFFINITY = "0-31"

Environment variables set by runcpu during the 638.imagick_s peak run:
GOMP_CPU_AFFINITY = "0-31"

Environment variables set by runcpu during the 649.fotonik3d_s peak run:
GOMP_CPU_AFFINITY = "0-31"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
 BIOS Configuration
 Workload Profile set to General Peak Frequency Compute
 AMD SMT Option set to Disabled
 Determinism Control set to Manual
 Performance Determinism set to Power Deterministic
 AMD Periodic Directory Rinse Tuning set to Cache-Bound
 Memory Patrol Scrubbing set to Disabled
 ACPI CST C2 Latency set to 18 microseconds
 Thermal Configuration set to Maximum Cooling
 The reference code/AGESA version used in this ROM is version Siena-PI 1.0.0

 Sysinfo program /home/CPU2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Sun Jan  5 01:59:27 2025

 SUT (System Under Test) info as seen by some common utilities.
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   01:59:27 up 1 min,  3 users,  load average: 0.42, 0.24, 0.09
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 771636
  max locked memory       (kbytes, -l) 2097152
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 771636
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize=31
 sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
 sshd: root [priv]
 sshd: root@notty
 bash -c cd $SPEC/ && $SPEC/fpspeed.sh
 python3 ./run_fpspeed.py
 /bin/bash ./amd_speed_aocc500_znver5_A1.sh
 runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fpspeed
 runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
   --runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.fpspeed.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : AMD EPYC 8324PN 32-Core Processor
    vendor_id       : AuthenticAMD
    cpu family      : 25
    model           : 160
    stepping        : 2
    microcode       : 0xaa00215
    bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
    TLB size        : 3584 4K pages
    cpu cores       : 32
    siblings        : 32
    1 physical ids (chips)
    32 processors (hardware threads)
    physical id 0: core ids 0-31
    physical id 0: apicids 0-31
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.39.3:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        52 bits physical, 57 bits virtual
  Byte Order:                           Little Endian
  CPU(s):                               32
  On-line CPU(s) list:                  0-31
  Vendor ID:                            AuthenticAMD
  BIOS Vendor ID:                       Advanced Micro Devices, Inc.
  Model name:                           AMD EPYC 8324PN 32-Core Processor
  BIOS Model name:                      AMD EPYC 8324PN 32-Core Processor                CPU @ 2.0GHz
  BIOS CPU family:                      107
  CPU family:                           25
  Model:                                160
  Thread(s) per core:                   1
  Core(s) per socket:                   32
  Socket(s):                            1
  Stepping:                             2
  BogoMIPS:                             4093.43
  Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                        pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                                        rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
```

(Continued on next page)

Platform Notes (Continued)

```
                                    extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                                    sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm
                                    cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                                    osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                                    perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                                    ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase bmi1 avx2 smep bmi2
                                    erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                                    clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                                    xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                                    user_shstk avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd
                                    amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean
                                    flushbyasid decodeassists pausefilter pfthreshold avic
                                    v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku
                                    ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                                    avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_l1d
                                    debug_swap
  Virtualization:                   AMD-V
  L1d cache:                        1 MiB (32 instances)
  L1i cache:                        1 MiB (32 instances)
  L2 cache:                         32 MiB (32 instances)
  L3 cache:                         128 MiB (8 instances)
  NUMA node(s):                     1
  NUMA node0 CPU(s):                0-31
  Vulnerability Gather data sampling: Not affected
  Vulnerability Itlb multihit:      Not affected
  Vulnerability L1tf:               Not affected
  Vulnerability Mds:                Not affected
  Vulnerability Meltdown:           Not affected
  Vulnerability Mmio stale data:    Not affected
  Vulnerability Reg file data sampling: Not affected
  Vulnerability Retbleed:           Not affected
  Vulnerability Spec rstack overflow: Mitigation; Safe RET
  Vulnerability Spec store bypass:  Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:         Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:         Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                                    disabled; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
  Vulnerability Srbds:              Not affected
  Vulnerability Tsx async abort:    Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       32K       1M    8 Data            1    64        1             64
     L1i       32K       1M    8 Instruction     1    64        1             64
     L2         1M      32M    8 Unified         2  2048        1             64
     L3        16M     128M   16 Unified         3 16384        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0-31
  node 0 size: 192934 MB
  node 0 free: 192009 MB
  node distances:
  node   0
    0:  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       197564740 kB
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
10. who -r
  run-level 3 Jan 5 01:58

------------------------------------------------------------
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          apparmor auditd cron getty@ irqbalance issue-generator kbdsettings lvm2-monitor postfix
                   purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4
                   wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell
                   grub2-once haveged hwloc-dump-hwdata issue-add-ssh-keys kexec-load lunmask rpmconfigcheck
                   serial-getty@ systemd-boot-check-no-failures systemd-confext systemd-network-generator
                   systemd-sysext systemd-time-wait-sync systemd-timesyncd
  indirect         systemd-userdbd wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
  root=UUID=45afd261-73b0-43a7-a23e-94db089f1753
  splash=silent
  resume=/dev/disk/by-uuid/d6c60601-2238-4347-950d-7321063e97c9
  mitigations=auto
  quiet
  security=apparmor

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 10:
    Unable to determine current policy
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing               0
  kernel.randomize_va_space           0
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                      8
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                       1
  vm.watermark_boost_factor       15000
```

(Continued on next page)

Platform Notes (Continued)

```
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                1

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          [always] defer defer+madvise madvise never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6

------------------------------------------------------------
19. Disk information
SPEC is set to: /home/CPU2017
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/sda3      xfs   476G  104G  373G  22% /home

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         HPE
    Product:        ProLiant DL145 Gen11
    Product Family: ProLiant
    Serial:         DL145GEN11

------------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    6x Hynix HMCG88MEBRA115N 32 GB 2 rank 4800


------------------------------------------------------------
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       HPE
   BIOS Version:      1.20
   BIOS Date:         08/09/2024
   BIOS Revision:     1.20
   Firmware Revision: 1.62
```

Compiler Version Notes

```
==============================================================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`clang`

Fortran benchmarks:
`flang`

(Continued on next page)

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
flang clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang

Benchmarks using both Fortran and C:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3

(Continued on next page)

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang

Benchmarks using Fortran, C, and C++:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt
-mllvm -loop-unswitch-threshold=200000 -mllvm -unroll-threshold=100
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Base Other Flags

C benchmarks:
-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:
-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:
-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:
-Wno-return-type -Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:
clang

Fortran benchmarks:
flang

(Continued on next page)

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
flang clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

638.imagick_s: Same as 619.lbm_s

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -fscalar-transform -fvector-transform
-mllvm -reduce-array-computations=3 -Mrecursive
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

649.fotonik3d_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP

(Continued on next page)

Peak Optimization Flags (Continued)

649.fotonik3d_s (continued):
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -flto -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm
-lamdalloc -lflang

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -DSPEC_OPENMP
-fremap-arrays -fstrip-mining -fstruct-layout=9
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mllvm -unroll-threshold=100
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Peak Other Flags

C benchmarks:
-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:
-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:
-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:
-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Siena-rev1.0.html
http://www.spec.org/cpu2017/flags/aocc500-flags.html

You can also download the XML flags sources by saving the following links:

`http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Siena-rev1.0.xml`
`http://www.spec.org/cpu2017/flags/aocc500-flags.xml`

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-01-04 15:29:26-0500.
Report generated on 2025-02-11 17:13:41 by CPU2017 PS formatter v6716.
Originally published on 2025-02-11.