# SPEC CPU®2017 Floating Point Rate Result

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## Dell Inc.

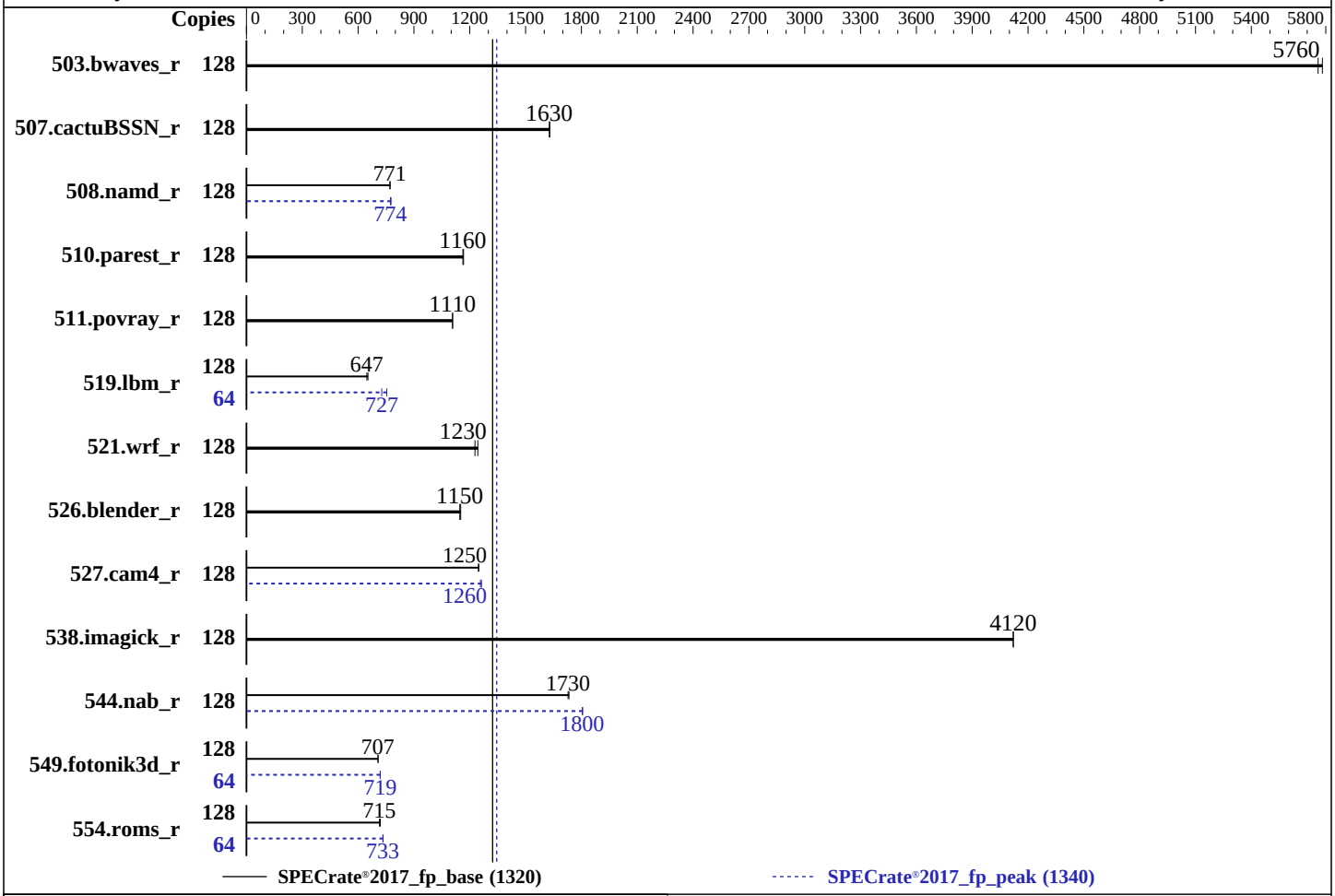
PowerEdge R6725 (AMD EPYC 9355 32-Core Processor)

SPECrate®2017_fp_base = 1320

SPECrate®2017_fp_peak = 1340

**CPU2017 License:** 6573
**Test Sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test Date:** Oct-2024
**Hardware Availability:** Nov-2024
**Software Availability:** Oct-2024




## Hardware

| | |
|---|---|
| CPU Name: | AMD EPYC 9355 |
| Max MHz: | 4400 |
| Nominal: | 3550 |
| Enabled: | 64 cores, 2 chips, 2 threads/core |
| Orderable: | 1,2 chips |
| Cache L1: | 32 KB I + 48 KB D on chip per core |
| L2: | 1 MB I+D on chip per core |
| L3: | 256 MB I+D on chip per chip, 32 MB shared / 4 cores |
| Other: | None |
| Memory: | 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R, running at 6000) |
| Storage: | 80 GB on tmpfs |
| Other: | CPU Cooling: Air |

## Software

| | |
|---|---|
| OS: | Ubuntu 24.04 LTS 6.8.0-41-generic |
| Compiler: | C/C++/Fortran: Version 5.0.0 of AOCC |
| Parallel: | No |
| Firmware: | Version 1.0.3 released Oct-2024 |
| File System: | tmpfs |
| System State: | Run level 5 (graphical multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 64-bit |
| Other: | None |
| Power Management: | BIOS and OS set to prefer performance at the cost of additional power usage. |

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r | 128 | **223** | **5760** | 222 | 5780 | | | 128 | **223** | **5760** | 222 | 5780 | | |
| 507.cactuBSSN_r | 128 | **99.6** | **1630** | 99.4 | 1630 | | | 128 | **99.6** | **1630** | 99.4 | 1630 | | |
| 508.namd_r | 128 | **158** | **771** | 157 | 772 | | | 128 | 157 | 777 | **157** | **774** | | |
| 510.parest_r | 128 | 287 | 1170 | **288** | **1160** | | | 128 | 287 | 1170 | **288** | **1160** | | |
| 511.povray_r | 128 | **270** | **1110** | 270 | 1110 | | | 128 | **270** | **1110** | 270 | 1110 | | |
| 519.lbm_r | 128 | **209** | **647** | 207 | 653 | | | 64 | **92.8** | **727** | 89.4 | 754 | | |
| 521.wrf_r | 128 | 231 | 1240 | **233** | **1230** | | | 128 | 231 | 1240 | **233** | **1230** | | |
| 526.blender_r | 128 | 169 | 1150 | **170** | **1150** | | | 128 | 169 | 1150 | **170** | **1150** | | |
| 527.cam4_r | 128 | 179 | 1250 | **180** | **1250** | | | 128 | **178** | **1260** | 177 | 1260 | | |
| 538.imagick_r | 128 | 77.2 | 4120 | **77.3** | **4120** | | | 128 | 77.2 | 4120 | **77.3** | **4120** | | |
| 544.nab_r | 128 | 124 | 1730 | **125** | **1730** | | | 128 | 119 | 1810 | **119** | **1800** | | |
| 549.fotonik3d_r | 128 | 705 | 707 | **706** | **707** | | | 64 | 347 | 719 | **347** | **719** | | |
| 554.roms_r | 128 | **285** | **715** | 283 | 719 | | | 64 | **139** | **733** | 138 | 734 | | |

**SPECrate®2017_fp_base = 1320**

**SPECrate®2017_fp_peak = 1340**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

## Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
```

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## Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

## Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1/amd_rate_aocc500_znver5_A_lib/lib:/mnt/ramdisk/cpu2017-1
     .1.9-aocc500-znerv5_A1/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

## General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.


 Benchmark run from a 80 GB ramdisk created with the cmd: "mount -t tmpfs -o size=80G tmpfs /mnt/ramdisk"
```

## Platform Notes

```
 BIOS Settings:
          Virtualization Technology : Disabled
              NUMA Nodes Per Socket : 4
                    Customized cTDP : 300
                     Customized PPT : 300

                     System Profile : Custom
                           C-States : Disabled
                Memory Patrol Scrub : Disabled
  PCI ASPM L1 Link Power Management : Disabled
    Periodic Directory Rinse Tuning : Blended
                Determinism Control : Manual
                 Determinism Slider : Power Determinism
                     Optimizer Mode : Enabled
                Adaptive Allocation : Enabled
                 Dram Refresh Delay : Performance
                DIMM Self Healing -
      on Uncorrectable Memory Error : Disabled

 Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on SLR6728-R6725 Thu Oct 31 21:06:48 2024

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
```

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## Platform Notes (Continued)


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```
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux SLR6728-R6725 6.8.0-41-generic #41-Ubuntu SMP PREEMPT_DYNAMIC Fri Aug  2 20:41:06 UTC 2024 x86_64
  x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   21:06:48 up  2:37,  1 user,  load average: 3.88, 61.96, 100.04
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                18:29   21.00s  1.25s  0.37s /bin/bash ./amd_rate_aocc500_znver5_A1.sh

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  time(seconds)        unlimited
  file(blocks)         unlimited
  data(kbytes)         unlimited
  stack(kbytes)        unlimited
  coredump(blocks)     0
  memory(kbytes)       unlimited
  locked memory(kbytes) 2097152
  process              6189332
  nofiles              1024
  vmemory(kbytes)      unlimited
  locks                unlimited
  rtprio               0

------------------------------------------------------------
5. sysinfo process ancestry
 /sbin/init
```


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## Platform Notes (Continued)

```
 /bin/login -p --
 -bash
 /bin/bash /home/DellFiles/bin/DELL_rate.sh
 /bin/bash /home/DellFiles/bin/dell-run-main.sh rate
 /bin/bash /home/DellFiles/bin/dell-run-main.sh rate
 /bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-VERS=6.0.5 --output_format
   html,pdf,txt
 python3 ./run_amd_rate_aocc500_znver5_A1.py
 /bin/bash ./amd_rate_aocc500_znver5_A1.sh
 runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define DL-BIOS-NPS=4
   --define DL-VERS=6.0.5 --output_format html,pdf,txt fprate
 runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
   DL-BIOS-NPS=4 --define DL-VERS=6.0.5 --output_format html,pdf,txt --nopower --runmode rate --tune
   base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : AMD EPYC 9355 32-Core Processor
    vendor_id       : AuthenticAMD
    cpu family      : 26
    model           : 2
    stepping        : 1
    microcode       : 0xb00211a
    bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
    TLB size        : 192 4K pages
    cpu cores       : 32
    siblings        : 64
    2 physical ids (chips)
    128 processors (hardware threads)
    physical id 0: core ids 0-3,8-11,16-19,24-27,32-35,40-43,48-51,56-59
    physical id 1: core ids 0-3,8-11,16-19,24-27,32-35,40-43,48-51,56-59
    physical id 0: apicids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119
    physical id 1: apicids 128-135,144-151,160-167,176-183,192-199,208-215,224-231,240-247
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.39.3:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        52 bits physical, 57 bits virtual
  Byte Order:                           Little Endian
  CPU(s):                               128
  On-line CPU(s) list:                  0-127
  Vendor ID:                            AuthenticAMD
  BIOS Vendor ID:                       AMD
  Model name:                           AMD EPYC 9355 32-Core Processor
  BIOS Model name:                      AMD EPYC 9355 32-Core Processor                 CPU @ 3.5GHz
  BIOS CPU family:                      107
  CPU family:                           26
  Model:                                2
  Thread(s) per core:                   2
  Core(s) per socket:                   32
  Socket(s):                            2
  Stepping:                             1
```

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## Platform Notes (Continued)

```
   Frequency boost:                  enabled
   CPU(s) scaling MHz:               80%
   CPU max MHz:                      4413.2319
   CPU min MHz:                      1500.0000
   BogoMIPS:                         7090.52
   Flags:                            fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                     pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                                     rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                                     extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                                     sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                                     cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                                     osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                                     perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                                     ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
                                     smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                                     avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                                     xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                     cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                                     xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
                                     nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                                     pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
                                     avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
                                     avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
                                     movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
                                     flush_l1d debug_swap
   L1d cache:                        3 MiB (64 instances)
   L1i cache:                        2 MiB (64 instances)
   L2 cache:                         64 MiB (64 instances)
   L3 cache:                         512 MiB (16 instances)
   NUMA node(s):                     8
   NUMA node0 CPU(s):                0-7,64-71
   NUMA node1 CPU(s):                8-15,72-79
   NUMA node2 CPU(s):                16-23,80-87
   NUMA node3 CPU(s):                24-31,88-95
   NUMA node4 CPU(s):                32-39,96-103
   NUMA node5 CPU(s):                40-47,104-111
   NUMA node6 CPU(s):                48-55,112-119
   NUMA node7 CPU(s):                56-63,120-127
   Vulnerability Gather data sampling: Not affected
   Vulnerability Itlb multihit:      Not affected
   Vulnerability L1tf:               Not affected
   Vulnerability Mds:                Not affected
   Vulnerability Meltdown:           Not affected
   Vulnerability Mmio stale data:    Not affected
   Vulnerability Reg file data sampling: Not affected
   Vulnerability Retbleed:           Not affected
   Vulnerability Spec rstack overflow: Not affected
   Vulnerability Spec store bypass:  Mitigation; Speculative Store Bypass disabled via prctl
   Vulnerability Spectre v1:         Mitigation; usercopy/swapgs barriers and __user pointer sanitization
   Vulnerability Spectre v2:         Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                                     always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
   Vulnerability Srbds:              Not affected
   Vulnerability Tsx async abort:    Not affected

 From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
      L1d       48K       3M   12 Data            1    64        1             64
      L1i       32K       2M    8 Instruction     1    64        1             64
      L2         1M      64M   16 Unified         2  1024        1             64
      L3        32M     512M   16 Unified         3 32768        1             64
```

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## Platform Notes (Continued)

```
------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 8 nodes (0-7)
  node 0 cpus: 0-7,64-71
  node 0 size: 192799 MB
  node 0 free: 191974 MB
  node 1 cpus: 8-15,72-79
  node 1 size: 193528 MB
  node 1 free: 192598 MB
  node 2 cpus: 16-23,80-87
  node 2 size: 193528 MB
  node 2 free: 192862 MB
  node 3 cpus: 24-31,88-95
  node 3 size: 193469 MB
  node 3 free: 187870 MB
  node 4 cpus: 32-39,96-103
  node 4 size: 193528 MB
  node 4 free: 192755 MB
  node 5 cpus: 40-47,104-111
  node 5 size: 193528 MB
  node 5 free: 192784 MB
  node 6 cpus: 48-55,112-119
  node 6 size: 193528 MB
  node 6 free: 192707 MB
  node 7 cpus: 56-63,120-127
  node 7 size: 193493 MB
  node 7 free: 192729 MB
  node distances:
  node   0   1   2   3   4   5   6   7
    0:  10  12  12  12  32  32  32  32
    1:  12  10  12  12  32  32  32  32
    2:  12  12  10  12  32  32  32  32
    3:  12  12  12  10  32  32  32  32
    4:  32  32  32  32  10  12  12  12
    5:  32  32  32  32  12  10  12  12
    6:  32  32  32  32  12  12  10  12
    7:  32  32  32  32  12  12  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1584541600 kB

------------------------------------------------------------
10. who -r
  run-level 5 Oct 31 18:29

------------------------------------------------------------
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8)
  Default Target  Status
  graphical       running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE           UNIT FILES
  enabled         ModemManager apparmor apport blk-availability cloud-config cloud-final cloud-init
                  cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
                  grub-common grub-initrd-fallback keyboard-setup lm-sensors lvm2-monitor multipathd
                  networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb
```

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## Platform Notes (Continued)

```
                  sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved
                  systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2 ufw vgauth
  enabled-runtime netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
  disabled        console-getty debug-shell iscsid nftables rsync serial-getty@ ssh
                  systemd-boot-check-no-failures systemd-confext systemd-network-generator
                  systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
                  systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
                  systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
                  systemd-time-wait-sync upower
  indirect        systemd-sysupdate systemd-sysupdate-reboot uuidd
  masked          cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.8.0-41-generic
  root=UUID=646610b2-1524-4713-b470-eb06f7216655
  ro

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 43:
    current policy: frequency should be within 1.50 GHz and 3.55 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes
      Boost States: 0
      Total States: 3
      Pstate-P0:  19800MHz

------------------------------------------------------------
15. tuned-adm active
  Current active profile: latency-performance

------------------------------------------------------------
16. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           0
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio           3
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                      8
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                       1
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                1

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage
  defrag          [always] defer defer+madvise madvise never
```

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## Platform Notes (Continued)

```
   enabled         [always] madvise never
   hpage_pmd_size  2097152
   shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
18. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
19. OS release
  From /etc/*-release /etc/*-version
  os-release Ubuntu 24.04 LTS

------------------------------------------------------------
20. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1
  Filesystem     Type   Size  Used Avail Use% Mounted on
  tmpfs          tmpfs   80G  3.3G   77G   5% /mnt/ramdisk

------------------------------------------------------------
21. /sys/devices/virtual/dmi/id
    Vendor:         Dell Inc.
    Product:        PowerEdge R6725
    Product Family: PowerEdge
    Serial:         SLR6728

------------------------------------------------------------
22. dmidecode
  Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    24x 80CE000080CE M321R8GA0PB2-CCPPC 64 GB 2 rank 6400, configured at 6000


------------------------------------------------------------
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       Dell Inc.
   BIOS Version:      1.0.3
   BIOS Date:         10/28/2024
   BIOS Revision:     1.0
```

## Compiler Version Notes

```
==============================================================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

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## Compiler Version Notes (Continued)

```
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
------------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
------------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
```

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## Compiler Version Notes (Continued)

```
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------
```

## Base Compiler Invocation

C benchmarks:
`clang`

C++ benchmarks:
`clang++`

Fortran benchmarks:
`flang`

Benchmarks using both Fortran and C:
`flang clang`

Benchmarks using both C and C++:
`clang++ clang`

Benchmarks using Fortran, C, and C++:
`clang++ clang flang`

## Base Portability Flags

503.bwaves_r: `-DSPEC_LP64`
507.cactuBSSN_r: `-DSPEC_LP64`
508.namd_r: `-DSPEC_LP64`
510.parest_r: `-DSPEC_LP64`
511.povray_r: `-DSPEC_LP64`
519.lbm_r: `-DSPEC_LP64`
521.wrf_r: `-DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64`
526.blender_r: `-funsigned-char -DSPEC_LP64`
527.cam4_r: `-DSPEC_CASE_FLAG -DSPEC_LP64`
538.imagick_r: `-DSPEC_LP64`
544.nab_r: `-DSPEC_LP64`
549.fotonik3d_r: `-DSPEC_LP64`
554.roms_r: `-DSPEC_LP64`

## Base Optimization Flags

C benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

C++ benchmarks:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl

Benchmarks using both Fortran and C:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

Benchmarks using both C and C++:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3

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## Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl

Benchmarks using Fortran, C, and C++:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

## Base Other Flags

C benchmarks:
-Wno-unused-command-line-argument

C++ benchmarks:
-Wno-unused-command-line-argument

Fortran benchmarks:
-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:
-Wno-unused-command-line-argument

Benchmarks using both C and C++:
-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:
-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

538.imagick_r: basepeak = yes

544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000

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## Peak Optimization Flags (Continued)

544.nab_r (continued):
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

C++ benchmarks:

508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

510.parest_r: basepeak = yes

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang

554.roms_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm
-lamdalloc -ldl -lflang

Benchmarks using both Fortran and C:

521.wrf_r: basepeak = yes

527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -reduce-array-computations=3 -zopt -Mrecursive

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## Peak Optimization Flags (Continued)

527.cam4_r (continued):
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang

Benchmarks using both C and C++:

511.povray_r: basepeak = yes

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

## Peak Other Flags

C benchmarks:
-Wno-unused-command-line-argument

C++ benchmarks:
-Wno-unused-command-line-argument

Fortran benchmarks:
-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:
-Wno-unused-command-line-argument

Benchmarks using both C and C++:
-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:
-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/aocc500-flags.html
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.4.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/aocc500-flags.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.4.xml

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