



# SPEC CPU®2017 Floating Point Rate Result

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**Fujitsu**

PRIMERGY RX2540 M7, Intel Xeon Gold 6554S,  
2.20GHz

**SPECrate®2017\_fp\_base = 824**

**SPECrate®2017\_fp\_peak = Not Run**

**CPU2017 License:** 19

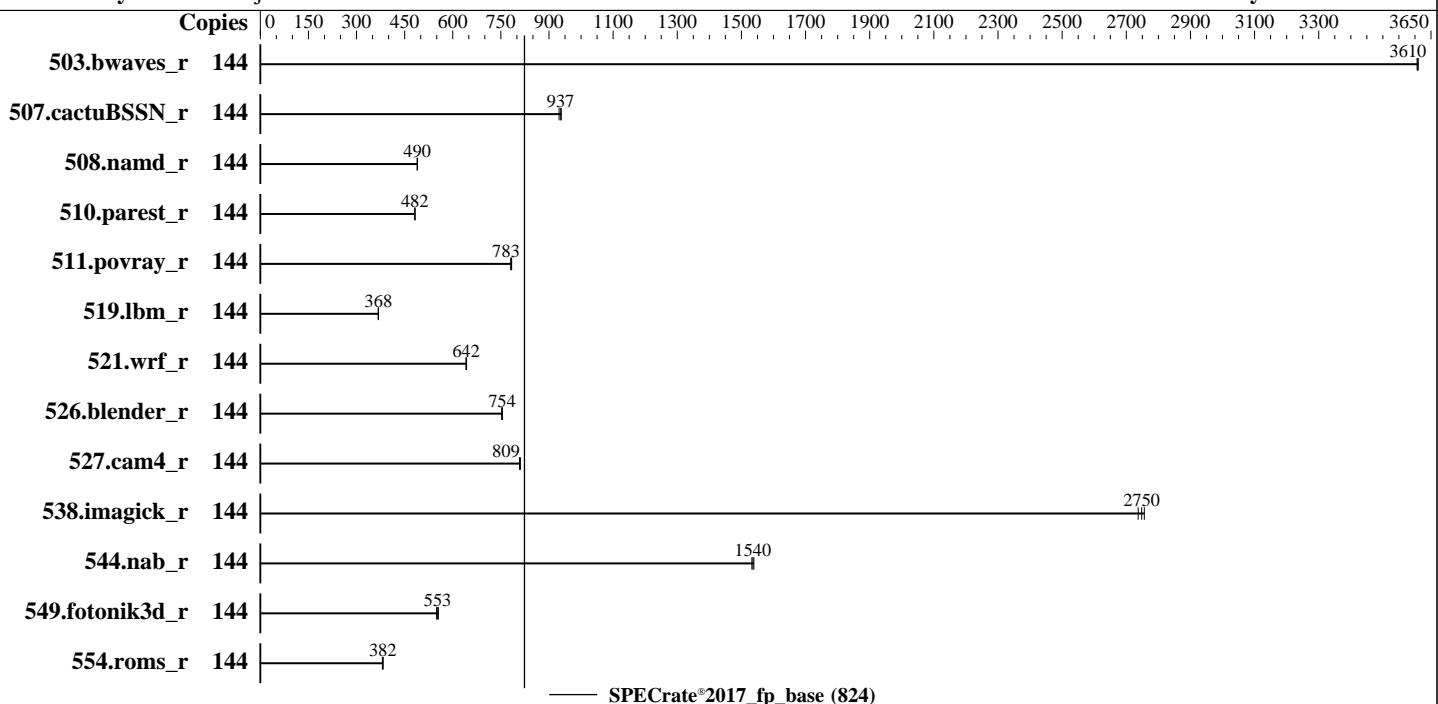
**Test Sponsor:** Fujitsu

**Tested by:** Fujitsu

**Test Date:** Apr-2024

**Hardware Availability:** Apr-2024

**Software Availability:** Dec-2023



| <b>Hardware</b> |   | <b>Software</b>   |   |
|-----------------|---|-------------------|---|
| CPU Name:       | Intel Xeon Gold 6554S                               | OS:               | SUSE Linux Enterprise Server 15 SP5                                   |
| Max MHz:        | 4000  | Compiler:         | 5.14.21-150500.53-default   |
| Nominal:        | 2200  | Parallel:         | C/C++: Version 2024.0.2 of Intel oneAPI DPC++/C++ Compiler for Linux; |
| Enabled:        | 72 cores, 2 chips, 2 threads/core                   | Firmware:         | Fortran: Version 2024.0.2 of Intel Fortran Compiler for Linux;        |
| Orderable:      | 1,2 chips   | File System:      | No  |
| Cache L1:       | 32 KB I + 48 KB D on chip per core                  | System State:     | Fujitsu BIOS Version V1.0.0.0 R2.4.0 for D3983-A1x. Released Apr-2024 |
| L2:             | 2 MB I+D on chip per core                           | Base Pointers:    | tested as V1.0.0.0 R2.1.2 for D3983-A1x Dec-2023                      |
| L3:             | 180 MB I+D on chip per chip                         | Peak Pointers:    | btrfs   |
| Other:          | None  | Other:            | Run level 3 (multi-user)  |
| Memory:         | 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 5200) | Power Management: | 64-bit  |
| Storage:        | 1 x SATA M.2, 960GB                                 |                   | Not Applicable  |
| Other:          | CPU Cooling: Air                                    |                   | jemalloc memory allocator V5.0.1                                      |
|                 |   |                   | BIOS set to prefer performance at the cost of additional power usage  |



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## Results Table

| Benchmark        | Base   |            |             |             |             |            |             | Peak   |         |       |         |       |         |       |
|------------------|--------|------------|-------------|-------------|-------------|------------|-------------|--------|---------|-------|---------|-------|---------|-------|
|                  | Copies | Seconds    | Ratio       | Seconds     | Ratio       | Seconds    | Ratio       | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r     | 144    | <b>400</b> | <b>3610</b> | 400         | 3610        | 400        | 3610        |        |         |       |         |       |         |       |
| 507.cactusBSSN_r | 144    | <b>195</b> | <b>937</b>  | 194         | 938         | 196        | 932         |        |         |       |         |       |         |       |
| 508.namd_r       | 144    | <b>279</b> | <b>490</b>  | 280         | 489         | 279        | 490         |        |         |       |         |       |         |       |
| 510.parest_r     | 144    | 781        | 482         | 782         | 482         | <b>782</b> | <b>482</b>  |        |         |       |         |       |         |       |
| 511.povray_r     | 144    | 431        | 781         | <b>430</b>  | <b>783</b>  | 429        | 784         |        |         |       |         |       |         |       |
| 519.lbm_r        | 144    | 413        | 368         | 412         | 368         | <b>412</b> | <b>368</b>  |        |         |       |         |       |         |       |
| 521.wrf_r        | 144    | 503        | 642         | <b>503</b>  | <b>642</b>  | 502        | 643         |        |         |       |         |       |         |       |
| 526.blender_r    | 144    | 291        | 753         | <b>291</b>  | <b>754</b>  | 291        | 755         |        |         |       |         |       |         |       |
| 527.cam4_r       | 144    | 312        | 808         | <b>311</b>  | <b>809</b>  | 311        | 811         |        |         |       |         |       |         |       |
| 538.imagick_r    | 144    | 131        | 2740        | 130         | 2760        | <b>130</b> | <b>2750</b> |        |         |       |         |       |         |       |
| 544.nab_r        | 144    | 157        | 1540        | <b>158</b>  | <b>1540</b> | 158        | 1530        |        |         |       |         |       |         |       |
| 549.fotonik3d_r  | 144    | 1021       | 549         | <b>1015</b> | <b>553</b>  | 1011       | 555         |        |         |       |         |       |         |       |
| 554.roms_r       | 144    | 601        | 381         | 598         | 383         | <b>599</b> | <b>382</b>  |        |         |       |         |       |         |       |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/benchmark/speccpu-24.0/lib/intel64:/home/benchmark/speccpu-24.0/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3 > /proc/sys/vm/drop\_caches

runcpu command invoked through numactl i.e.:

numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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## General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS configuration:

Intel Virtualization Technology = Disabled

Utilization Profile = Unbalanced

CPU Performance Boost = Aggressive

SNC (Sub NUMA) = Enable SNC2

Fan Control = Full

Sysinfo program /home/benchmark/speccpu-24.0/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Wed Apr 17 01:01:06 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents

1. uname -a  
2. w  
3. Username  
4. ulimit -a  
5. sysinfo process ancestry  
6. /proc/cpuinfo  
7. lscpu  
8. numactl --hardware  
9. /proc/meminfo  
10. who -r  
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)  
12. Services, from systemctl list-unit-files  
13. Linux kernel boot-time arguments, from /proc/cmdline  
14. cpupower frequency-info  
15. sysctl  
16. /sys/kernel/mm/transparent\_hugepage  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged  
18. OS release  
19. Disk information  
20. /sys/devices/virtual/dmi/id  
21. dmidecode  
22. BIOS

-----  
1. uname -a  
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)  
x86\_64 x86\_64 x86\_64 GNU/Linux

-----  
2. w  
01:01:06 up 9:43, 1 user, load average: 0.58, 47.07, 98.85  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

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## Platform Notes (Continued)

```
root      tty1      -          15:23    9:36m  3.17s  0.65s -bash
```

```
-----  
3. Username  
From environment variable $USER: root
```

```
-----  
4. ulimit -a  
core file size          (blocks, -c) unlimited  
data seg size           (kbytes, -d) unlimited  
scheduling priority     (-e) 0  
file size               (blocks, -f) unlimited  
pending signals          (-i) 4124920  
max locked memory       (kbytes, -l) 64  
max memory size         (kbytes, -m) unlimited  
open files              (-n) 1024  
pipe size               (512 bytes, -p) 8  
POSIX message queues    (bytes, -q) 819200  
real-time priority       (-r) 0  
stack size               (kbytes, -s) unlimited  
cpu time                (seconds, -t) unlimited  
max user processes       (-u) 4124920  
virtual memory           (kbytes, -v) unlimited  
file locks               (-x) unlimited
```

```
-----  
5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize 30  
login -- root  
-bash  
-bash  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 -c  
  ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=72 --define physicalfirst  
  --define invoke_with_interleave --define drop_caches --tune base -o all fprate  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 --configfile  
  ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=72 --define physicalfirst  
  --define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode  
  rate --tune base --size refrate fprate --nopreenv --note-preenv --logfile  
  $SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2  
specperl $SPEC/bin/sysinfo  
$SPEC = /home/benchmark/speccpu-24.0
```

```
-----  
6. /proc/cpuinfo  
model name          : INTEL(R) XEON(R) GOLD 6554S  
vendor_id           : GenuineIntel  
cpu family          : 6  
model               : 207  
stepping             : 2  
microcode           : 0x210001a0  
bugs                : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrss_pbrss  
cpu cores            : 36  
siblings             : 72  
2 physical ids (chips)  
144 processors (hardware threads)  
physical id 0: core ids 0-35  
physical id 1: core ids 0-35  
physical id 0: apicids 0-71  
physical id 1: apicids 128-199
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for

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## Platform Notes (Continued)

virtualized systems. Use the above data carefully.

7. lscpu

```
From lscpu from util-linux 2.37.4:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 144
On-line CPU(s) list: 0-143
Vendor ID: GenuineIntel
Model name: INTEL(R) XEON(R) GOLD 6554S
CPU family: 6
Model: 207
Thread(s) per core: 2
Core(s) per socket: 36
Socket(s): 2
Stepping: 2
CPU max MHz: 4000.0000
CPU min MHz: 800.0000
BogoMIPS: 4400.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
      clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
      lm constant_tsc art arch_perfmon pebs bts rep_good noopl xtopology
      nonstop_tsc cpuid aperf mperf tsc_known_freq pni pclmulqdq dtes64 monitor
      ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
      x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
      abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single
      cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle
      avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
      avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
      xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
      cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hwp
      hwp_act_window hwp_epp hwp_pkg_req hfi avx512vbmi umip pku ospke waitpkg
      avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
      avx512_vpocntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
      enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
      amx_tile flush_ll1d arch_capabilities
L1d cache: 3.4 MiB (72 instances)
L1i cache: 2.3 MiB (72 instances)
L2 cache: 144 MiB (72 instances)
L3 cache: 360 MiB (2 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-17,72-89
NUMA node1 CPU(s): 18-35,90-107
NUMA node2 CPU(s): 36-53,108-125
NUMA node3 CPU(s): 54-71,126-143
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW sequence
Vulnerability Srbds: Not affected
```

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## Platform Notes (Continued)

Vulnerability Tsx async abort: Not affected

```
From lscpu --cache:
  NAME ONE-SIZE ALL-SIZE WAYS TYPE      LEVEL    SETS PHY-LINE COHERENCY-SIZE
  L1d     48K     3.4M   12 Data          1       64        1           64
  L1i     32K     2.3M    8 Instruction   1       64        1           64
  L2      2M      144M   16 Unified       2      2048        1           64
  L3     180M     360M   20 Unified       3     147456        1           64
```

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-17,72-89
node 0 size: 257617 MB
node 0 free: 256379 MB
node 1 cpus: 18-35,90-107
node 1 size: 258038 MB
node 1 free: 256884 MB
node 2 cpus: 36-53,108-125
node 2 size: 258038 MB
node 2 free: 256842 MB
node 3 cpus: 54-71,126-143
node 3 size: 257564 MB
node 3 free: 256447 MB
node distances:
node   0   1   2   3
  0: 10 12 21 21
  1: 12 10 21 21
  2: 21 21 10 12
  3: 21 21 12 10
```

-----  
9. /proc/meminfo

```
MemTotal: 1056010364 kB
```

-----  
10. who -r  
run-level 3 Apr 16 15:18

-----  
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
Default Target Status
multi-user running

-----  
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron
 display-manager getty@ irqbalance issue-generator kbdsettings kdump kdump-early klog
 lvm2-monitor nscd postfix purge-kernels rollback rsyslog sep5 smartd sshd systemd-pstore
 wickedd wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemdr remount-fs
disabled accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl
 ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables
 exchange-bmc-os-info firewalld gpm grub2-once haveged haveged-switch-root ipmi ipmievfd
 issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb
 ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@
 smartd\_generate\_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures
 systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2

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## Platform Notes (Continued)

```
update-system-flatpaks upower vncserver@  
indirect      wickedd  
  
-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default  
root=UUID=d177d129-775e-44ff-bf2a-8f0e7a7a7b1b  
splash=silent  
quiet  
security=apparmor  
crashkernel=401M,high  
crashkernel=72M,low  
mitigations=auto  
  
-----  
14. cpupower frequency-info  
analyzing CPU 0:  
    current policy: frequency should be within 800 MHz and 4.00 GHz.  
              The governor "powersave" may decide which speed to use  
              within this range.  
    boost state support:  
      Supported: yes  
      Active: yes  
  
-----  
15. sysctl  
kernel.numa_balancing      1  
kernel.randomize_va_space   2  
vm.compaction_proactiveness 20  
vm.dirty_background_bytes   0  
vm.dirty_background_ratio   10  
vm.dirty_bytes              0  
vm.dirty_expire_centisecs  3000  
vm.dirty_ratio              20  
vm.dirty_writeback_centisecs 500  
vm.dirtytime_expire_seconds 43200  
vm.extfrag_threshold       500  
vm.min_unmapped_ratio      1  
vm.nr_hugepages             0  
vm.nr_hugepages_mempolicy   0  
vm.nr_overcommit_hugepages  0  
vm.swappiness                60  
vm.watermark_boost_factor   15000  
vm.watermark_scale_factor   10  
vm.zone_reclaim_mode        0  
  
-----  
16. /sys/kernel/mm/transparent_hugepage  
defrag           always defer defer+madvise [madvise] never  
enabled          [always] madvise never  
hpage_pmd_size  2097152  
shmem_enabled   always within_size advise [never] deny force  
  
-----  
17. /sys/kernel/mm/transparent_hugepage/khugepaged  
alloc_sleep_millisecs  60000  
defrag            1  
max_ptes_none     511  
max_ptes_shared   256  
max_ptes_swap     64
```

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## Platform Notes (Continued)

```
pages_to_scan      4096
scan_sleep_millisecs 10000
```

```
-----  
18. OS release  
From /etc/*-release /etc/*-version  
os-release SUSE Linux Enterprise Server 15 SP5
```

```
-----  
19. Disk information  
SPEC is set to: /home/benchmark/speccpu-24.0  
Filesystem      Type   Size  Used Avail Use% Mounted on  
/dev/sda2        btrfs  892G  67G  824G  8%  /home
```

```
-----  
20. /sys/devices/virtual/dmi/id  
Vendor:          FUJITSU  
Product:         PRIMERGY RX2540 M7  
Product Family: SERVER  
Serial:          EWCEXXXXXX
```

```
-----  
21. dmidecode  
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the  
"DMTF SMBIOS" standard.  
Memory:  
    16x Hynix HMCG94AGBRA181N 64 GB 2 rank 5600, configured at 5200
```

```
-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor:      FUJITSU  
BIOS Version:     V1.0.0.0 R2.1.2 for D3983-A1x  
BIOS Date:        12/21/2023  
BIOS Revision:    2.1  
Firmware Revision: 2.36
```

## Compiler Version Notes

```
=====| 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
```

```
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

```
=====| 508.namd_r(base) 510.parest_r(base)
```

```
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

```
=====| 511.povray_r(base) 526.blender_r(base)
```

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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
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=====  
C++, C, Fortran | 507.cactuBSSN\_r(base)

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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
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=====  
Fortran | 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
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=====  
Fortran, C | 521.wrf\_r(base) 527.cam4\_r(base)

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## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx



# SPEC CPU®2017 Floating Point Rate Result

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Fujitsu

PRIMERGY RX2540 M7, Intel Xeon Gold 6554S,  
2.20GHz

SPECrate®2017\_fp\_base = 824

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Apr-2024

Hardware Availability: Apr-2024

Software Availability: Dec-2023

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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Fujitsu

PRIMERGY RX2540 M7, Intel Xeon Gold 6554S,  
2.20GHz

SPECrate®2017\_fp\_base = 824

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Apr-2024

Hardware Availability: Apr-2024

Software Availability: Dec-2023

## Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

```
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -futto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-EMR-RevD.html>  
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-EMR-RevD.xml>  
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

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