



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(2.20 GHz, Intel Xeon Gold 5520+)

**SPECspeed®2017\_fp\_base = 302**

**SPECspeed®2017\_fp\_peak = Not Run**

**CPU2017 License:** 9017

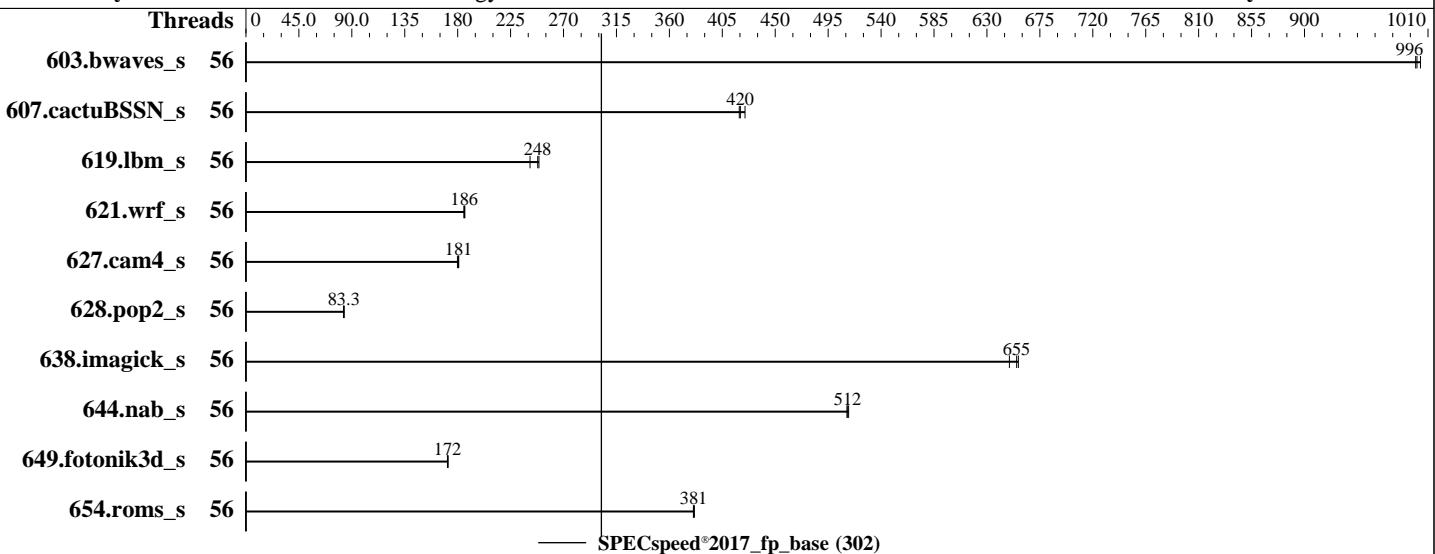
**Test Date:** Mar-2024

**Test Sponsor:** Lenovo Global Technology

**Hardware Availability:** Feb-2024

**Tested by:** Lenovo Global Technology

**Software Availability:** Mar-2024



### Hardware

CPU Name: Intel Xeon Gold 5520+  
Max MHz: 4000  
Nominal: 2200  
Enabled: 56 cores, 2 chips  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 52.5 MB I+D on chip per chip  
Other: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)  
Storage: 1 x 960 GB SATA SSD  
Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP4  
Compiler: Kernel 5.14.21-150400.22-default  
C/C++: Version 2024.0.2 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2024.0.2 of Intel Fortran Compiler for Linux;  
Parallel: Yes  
Firmware: Lenovo BIOS Version ESE123C 3.12 released Feb-2024  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(2.20 GHz, Intel Xeon Gold 5520+)

SPECspeed®2017\_fp\_base = 302

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Mar-2024

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	56	<b>59.3</b>	<b>996</b>	59.3	995	59.1	999							
607.cactuBSSN_s	56	39.3	424	39.7	419	<b>39.7</b>	<b>420</b>							
619.lbm_s	56	21.0	249	<b>21.1</b>	<b>248</b>	21.7	242							
621.wrf_s	56	<b>71.2</b>	<b>186</b>	71.4	185	71.1	186							
627.cam4_s	56	49.0	181	49.3	180	<b>49.0</b>	<b>181</b>							
628.pop2_s	56	143	82.9	142	83.5	<b>143</b>	<b>83.3</b>							
638.imagick_s	56	22.2	649	<b>22.0</b>	<b>655</b>	22.0	657							
644.nab_s	56	34.1	512	<b>34.2</b>	<b>512</b>	34.2	511							
649.fotonik3d_s	56	53.2	171	<b>53.1</b>	<b>172</b>	53.0	172							
654.roms_s	56	41.4	380	41.3	381	<b>41.4</b>	<b>381</b>							

SPECspeed®2017\_fp\_base = 302

SPECspeed®2017\_fp\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-ic2024.0.2/lib/intel64:/home/cpu2017-1.1.9-ic2024.0.2/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(2.20 GHz, Intel Xeon Gold 5520+)

SPECspeed®2017\_fp\_base = 302

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Mar-2024

## Platform Notes

### BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode

C-States set to Legacy

Hyper-Threading set to Disabled

Adjacent Cache Prefetch set to Disabled

AMP Prefetch set to Enable

```
Sysinfo program /home/cpu2017-1.1.9-ic2024.0.2/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu Mar 21 15:48:17 2024
```

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

- 1. uname -a
- 2. w
- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. sysctl
- 16. /sys/kernel/mm/transparent\_hugepage
- 17. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

```
1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
15:48:17 up 3 min, 1 user, load average: 0.08, 0.18, 0.09
USER      TTY      FROM             LOGIN@     IDLE    JCPU    PCPU WHAT
root      tty1      -           15:45    25.00s  0.77s  0.00s /bin/bash ./speccpu_ny.sh
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(2.20 GHz, Intel Xeon Gold 5520+)

SPECspeed®2017\_fp\_base = 302

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Mar-2024

## Platform Notes (Continued)

```
file size          (blocks, -f) unlimited
pending signals   (-i) 4127058
max locked memory (kbytes, -l) 64
max memory size   (kbytes, -m) unlimited
open files         (-n) 1024
pipe size          (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size          (kbytes, -s) unlimited
cpu time            (seconds, -t) unlimited
max user processes (-u) 4127058
virtual memory      (kbytes, -v) unlimited
file locks          (-x) unlimited
```

```
-----  
5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize 30  
login -- root  
-bash  
/bin/bash ./speccpu_ny.sh  
/bin/bash ./speccpu_ny.sh  
runcpu --nobuild --action validate --define default-platform-flags -c  
  ic2024.0.2-lin-sapphirerapids-speed-20231213.cfg --define cores=56 --tune base -o all --define drop_caches  
  fpspeed  
runcpu --nobuild --action validate --define default-platform-flags --configfile  
  ic2024.0.2-lin-sapphirerapids-speed-20231213.cfg --define cores=56 --tune base --output_format all  
  --define drop_caches --nopower --runmode speed --tune base --size refspeed fpspeed --nopreenv  
  --note-preenv --logfile $SPEC/tmp/CPU2017.082/templogs/preenv.fpspeed.082.0.log --lognum 082.0  
  --from_runcpu 2  
specperl $SPEC/bin/sysinfo  
$SPEC = /home/cpu2017-1.1.9-ic2024.0.2
```

```
-----  
6. /proc/cpuinfo  
model name      : INTEL(R) XEON(R) GOLD 5520+
vendor_id        : GenuineIntel
cpu family       : 6
model           : 207
stepping         : 2
microcode        : 0x21000200
bugs             : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores        : 28
siblings         : 28
2 physical ids (chips)
56 processors (hardware threads)
physical id 0: core ids 0-27
physical id 1: core ids 0-27
physical id 0: apicids 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54
physical id 1: apicids
128,130,132,134,136,138,140,142,144,146,148,150,152,154,156,158,160,162,164,166,168,170,172,174,176,178,1
80,182
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
-----  
7. lscpu
```

```
From lscpu from util-linux 2.37.2:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(2.20 GHz, Intel Xeon Gold 5520+)

SPECspeed®2017\_fp\_base = 302

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Mar-2024

## Platform Notes (Continued)

```

Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 56
On-line CPU(s) list: 0-55
Vendor ID: GenuineIntel
Model name: INTEL(R) XEON(R) GOLD 5520+
CPU family: 6
Model: 207
Thread(s) per core: 1
Core(s) per socket: 28
Socket(s): 2
Stepping: 2
BogoMIPS: 4400.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
       nonstop_tsc cpuid aperfmpf tsc_known_freq pni pclmulqdq dtes64 monitor
       ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xptr pdcm pcid dca sse4_1
       sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
      lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
       invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
       vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
       bm12 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
       avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
       xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
       cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
       avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
       avx512_vnni avx512_bitlg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
       cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
       arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities
Virtualization: VT-x
L1d cache: 2.6 MiB (56 instances)
L1i cache: 1.8 MiB (56 instances)
L2 cache: 112 MiB (56 instances)
L3 cache: 105 MiB (2 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-27
NUMA node1 CPU(s): 28-55
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.6M	12	Data	1	64	1	64
L1i	32K	1.8M	8	Instruction	1	64	1	64
L2	2M	112M	16	Unified	2	2048	1	64
L3	52.5M	105M	15	Unified	3	57344	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-27

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(2.20 GHz, Intel Xeon Gold 5520+)

SPECspeed®2017\_fp\_base = 302

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Mar-2024

## Platform Notes (Continued)

```
node 0 size: 515719 MB
node 0 free: 514599 MB
node 1 cpus: 28-55
node 1 size: 516068 MB
node 1 free: 515345 MB
node distances:
node 0 1
 0: 10 21
 1: 21 10

-----
9. /proc/meminfo
MemTotal: 1056551192 kB

-----
10. who -r
run-level 3 Mar 21 15:45

-----
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
Default Target Status
multi-user running

-----
12. Services, from systemctl list-unit-files
STATE          UNIT FILES
enabled        YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance
                  issue-generator kbdsettings klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog
                  smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime   systemd-remount-fs
disabled       autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                  chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
                  firewalld gpm grub2-once haveged-switch-root hwloc-dump-hwdata ipmi ipmiev
                  issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rdisc
                  rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd
                  systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                  systemd-time-wait-sync systemd-timesyncd
generated      ntp_sync
indirect       wickedd

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=efe3d3bb-d17b-48bc-af3c-7ee429916327
splash=silent
mitigations=auto
quiet
security=apparmor

-----
14. cpupower frequency-info
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes

-----
15. sysctl
kernel.numa_balancing
```



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(2.20 GHz, Intel Xeon Gold 5520+)

SPECspeed®2017\_fp\_base = 302

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Mar-2024

## Platform Notes (Continued)

```
kernel.randomize_va_space          2
vm.compaction_proactiveness       20
vm.dirty_background_bytes          0
vm.dirty_background_ratio         10
vm.dirty_bytes                    0
vm.dirty_expire_centisecs        3000
vm.dirty_ratio                   20
vm.dirty_writeback_centisecs     500
vm.dirtytime_expire_seconds      43200
vm.extfrag_threshold             500
vm.min_unmapped_ratio            1
vm.nr_hugepages                  0
vm.nr_hugepages_mempolicy        0
vm.nr_overcommit_hugepages       0
vm.swappiness                     60
vm.watermark_boost_factor        15000
vm.watermark_scale_factor        10
vm.zone_reclaim_mode             0

-----
16. /sys/kernel/mm/transparent_hugepage
    defrag           always defer defer+madvise [madvise] never
    enabled          [always] madvise never
    hpage_pmd_size  2097152
    shmem_enabled   always within_size advise [never] deny force

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag               1
    max_ptes_none        511
    max_ptes_shared      256
    max_ptes_swap        64
    pages_to_scan        4096
    scan_sleep_millisecs 10000

-----
18. OS release
    From /etc/*-release /etc/*-version
    os-release SUSE Linux Enterprise Server 15 SP4

-----
19. Disk information
    SPEC is set to: /home/cpu2017-1.1.9-ic2024.0.2
    Filesystem  Type  Size  Used Avail Use% Mounted on
    /dev/sda2    xfs   894G  164G  731G  19%  /

-----
20. /sys/devices/virtual/dmi/id
    Vendor:          Lenovo
    Product:         ThinkSystem SR630 V3 MB,EGS,DDR5,NY,LU
    Product Family:  ThinkSystem
    Serial:          1234567890

-----
21. dmidecode
    Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
    The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
    "DMTF SMBIOS" standard.
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(2.20 GHz, Intel Xeon Gold 5520+)

SPECspeed®2017\_fp\_base = 302

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Mar-2024

## Platform Notes (Continued)

### Memory:

13x Samsung M321R8GA0PB0-CWMKH 64 GB 2 rank 5600, configured at 4800  
3x Samsung M321R8GA0PB0-CWMXH 64 GB 2 rank 5600, configured at 4800

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo  
BIOS Version: ESE123C-3.12  
BIOS Date: 02/22/2024  
BIOS Revision: 3.12  
Firmware Revision: 3.90

## Compiler Version Notes

=====

C | 619.lbm\_s(base) 638.imagick\_s(base) 644.nab\_s(base)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

=====

C++, C, Fortran | 607.cactusBSSN\_s(base)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

=====

Fortran | 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

=====

Fortran, C | 621.wrf\_s(base) 627.cam4\_s(base) 628.pop2\_s(base)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

## Base Compiler Invocation

C benchmarks:

icx

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(2.20 GHz, Intel Xeon Gold 5520+)

SPECspeed®2017\_fp\_base = 302

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Mar-2024

## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
627.cam4\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
628.pop2\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
-assume byterecl  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp  
-DSPEC\_OPENMP -Wno-implicit-int -mprefer-vector-width=512  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -DSPEC\_OPENMP -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fopenmp -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp  
-DSPEC\_OPENMP -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(2.20 GHz, Intel Xeon Gold 5520+)

SPECspeed®2017\_fp\_base = 302

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Mar-2024

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -futo -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC\_OPENMP -Wno-implicit-int  
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte  
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html>  
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml>  
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-03-21 03:48:17-0400.

Report generated on 2024-04-09 15:50:43 by CPU2017 PDF formatter v6716.

Originally published on 2024-04-09.