



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

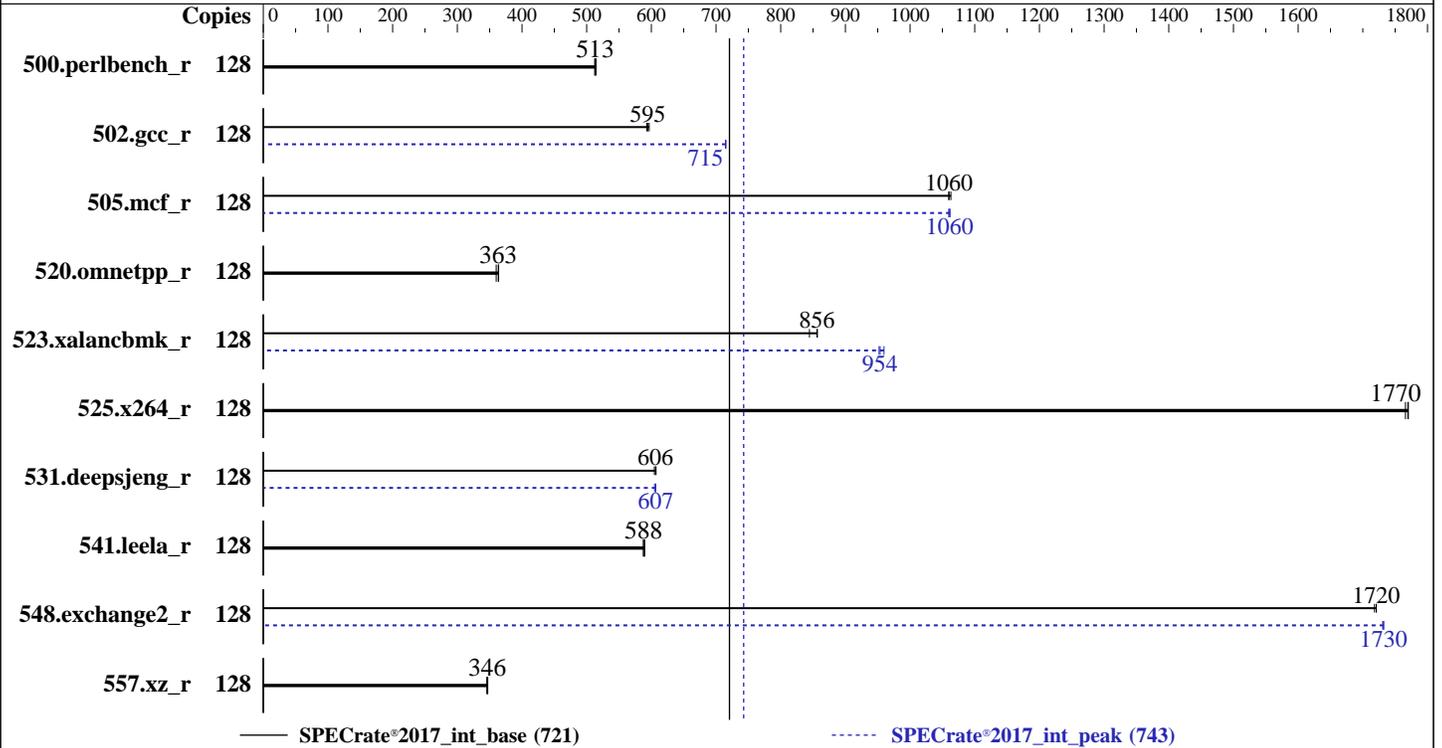
Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

CPU2017 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: Jan-2024  
Hardware Availability: Nov-2022  
Software Availability: Nov-2023



### Hardware

CPU Name: AMD EPYC 9334  
Max MHz: 3900  
Nominal: 2700  
Enabled: 64 cores, 2 chips, 2 threads/core  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 128 MB I+D on chip per chip, 32 MB shared / 8 cores  
Other: None  
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-4800B-R)  
Storage: 1 x 800 GB NVMe SSD  
Other: None

### Software

OS: Ubuntu 22.04.3 LTS  
Kernel 5.15.0-91-generic  
Compiler: C/C++/Fortran: Version 4.0.0 of AOCC  
Parallel: No  
Firmware: Version 1.6 released Nov-2023  
File System: ext4  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other: None  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

CPU2017 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: Jan-2024  
Hardware Availability: Nov-2022  
Software Availability: Nov-2023

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	128	396	514	398	512	<b>397</b>	<b>513</b>	128	396	514	398	512	<b>397</b>	<b>513</b>
502.gcc_r	128	304	597	<b>305</b>	<b>595</b>	306	593	128	254	715	253	715	<b>254</b>	<b>715</b>
505.mcf_r	128	<b>195</b>	<b>1060</b>	195	1060	195	1060	128	195	1060	195	1060	<b>195</b>	<b>1060</b>
520.omnetpp_r	128	466	360	<b>462</b>	<b>363</b>	462	364	128	466	360	<b>462</b>	<b>363</b>	462	364
523.xalancbmk_r	128	158	857	160	844	<b>158</b>	<b>856</b>	128	142	952	141	960	<b>142</b>	<b>954</b>
525.x264_r	128	<b>127</b>	<b>1770</b>	127	1770	127	1770	128	<b>127</b>	<b>1770</b>	127	1770	127	1770
531.deepsjeng_r	128	<b>242</b>	<b>606</b>	242	607	243	605	128	<b>242</b>	<b>607</b>	242	607	242	605
541.leela_r	128	<b>361</b>	<b>588</b>	359	590	361	588	128	<b>361</b>	<b>588</b>	359	590	361	588
548.exchange2_r	128	<b>195</b>	<b>1720</b>	195	1720	195	1720	128	194	1730	<b>194</b>	<b>1730</b>	194	1730
557.xz_r	128	<b>399</b>	<b>346</b>	400	346	398	347	128	<b>399</b>	<b>346</b>	400	346	398	347

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runccpu command invoked through numactl i.e.:  
numactl --interleave=all runccpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

OS set to performance mode via cpupower frequency-set -g performance  
To enable Transparent Hugepages (THP) only on request for base runs,  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.  
To enable THP for all allocations for peak runs,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/cpu2017/amd_rate_aocc400_znver4_A_lib/lib:/home/cpu2017/amd_rate_aocc400_znver4_A_lib/lib32:"  
MALLOC_CONF = "retain:true"
```

Environment variables set by runcpu during the 523.xalancbmk\_r peak run:

```
MALLOC_CONF = "thp:never"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS Settings:

```
NUMA Nodes Per Socket = NPS4  
Determinism Control = Manual  
Determinism Enable = Disable Performance Determinism  
cTDP Control = Manual  
cTDP = 240  
Package Power Limit Control = Manual  
Package Power Limit = 240
```

```
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on as-2025hs-tnr-9334 Thu Jan 11 08:42:34 2024
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.11)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

### Platform Notes (Continued)

```
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
```

```
-----
1. uname -a
Linux as-2025hs-tnr-9334 5.15.0-91-generic #101-Ubuntu SMP Tue Nov 14 13:30:08 UTC 2023 x86_64 x86_64
x86_64 GNU/Linux
```

```
-----
2. w
08:42:34 up 39 min, 4 users, load average: 0.16, 0.05, 0.02
USER      TTY      FROM          LOGIN@      IDLE        JCPU      PCPU      WHAT
lab       tty1     -             08:06       36:31      1:13      0.01s    -bash
lab       pts/0    -             08:06       10.00s     2.49s     1:13     sudo su -
lab       tty2     -             08:29       13:30      0.24s     0.01s    -bash
lab       pts/1    -             08:29       7:34       0.01s     0.20s    sudo su -
```

```
-----
3. Username
From environment variable $USER:  root
From the command 'logname':      lab
```

```
-----
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)       unlimited
stack(kbytes)      unlimited
coredump(blocks)   0
memory(kbytes)     unlimited
locked memory(kbytes) 2097152
process            6190951
nofiles            1024
vmemory(kbytes)    unlimited
locks              unlimited
rtprio             0
```

```
-----
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
sudo su -
sudo su -
su -
-bash
python3 ./run_amd_rate_aocc400_znver4_A1.py
/bin/bash ./amd_rate_aocc400_znver4_A1.sh
runcpu --config amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 intrate
runcpu --configfile amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

### Platform Notes (Continued)

6. /proc/cpuinfo

```

model name      : AMD EPYC 9334 32-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping       : 1
microcode      : 0xa10113e
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size      : 3584 4K pages
cpu cores     : 32
siblings      : 64
2 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 0: apicids 0-63
physical id 1: apicids 64-127

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                128
On-line CPU(s) list:  0-127
Vendor ID:             AuthenticAMD
Model name:            AMD EPYC 9334 32-Core Processor
CPU family:            25
Model:                 17
Thread(s) per core:   2
Core(s) per socket:   32
Socket(s):             2
Stepping:              1
Frequency boost:       enabled
CPU max MHz:           3910.2529
CPU min MHz:           1500.0000
BogoMIPS:              5400.01
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp
                        lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf
                        rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic
                        movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic
                        cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce
                        topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3
                        cdp_l3 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall
                        fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
                        rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw
                        avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
                        cqm_mbm_total cqm_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru
                        wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale
                        vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic
                        v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke avx512_vbmi2
                        gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57
                        rdpid overflow_recov succor smca fsrm flush_l1d

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

### Platform Notes (Continued)

```

Virtualization: AMD-V
L1d cache: 2 MiB (64 instances)
L1i cache: 2 MiB (64 instances)
L2 cache: 64 MiB (64 instances)
L3 cache: 256 MiB (8 instances)
NUMA node(s): 8
NUMA node0 CPU(s): 0-7,64-71
NUMA node1 CPU(s): 8-15,72-79
NUMA node2 CPU(s): 16-23,80-87
NUMA node3 CPU(s): 24-31,88-95
NUMA node4 CPU(s): 32-39,96-103
NUMA node5 CPU(s): 40-47,104-111
NUMA node6 CPU(s): 48-55,112-119
NUMA node7 CPU(s): 56-63,120-127
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Mitigation; safe RET
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB
filling, PBRBS-eIBRS Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	2M	8	Data	1	64	1	64
L1i	32K	2M	8	Instruction	1	64	1	64
L2	1M	64M	8	Unified	2	2048	1	64
L3	32M	256M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-7,64-71
node 0 size: 193221 MB
node 0 free: 192692 MB
node 1 cpus: 8-15,72-79
node 1 size: 193531 MB
node 1 free: 193044 MB
node 2 cpus: 16-23,80-87
node 2 size: 193531 MB
node 2 free: 192689 MB
node 3 cpus: 24-31,88-95
node 3 size: 193484 MB
node 3 free: 193020 MB
node 4 cpus: 32-39,96-103
node 4 size: 193531 MB
node 4 free: 193118 MB
node 5 cpus: 40-47,104-111
node 5 size: 193531 MB
node 5 free: 193184 MB
node 6 cpus: 48-55,112-119
node 6 size: 193531 MB

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

### Platform Notes (Continued)

```

node 6 free: 193187 MB
node 7 cpus: 56-63,120-127
node 7 size: 193485 MB
node 7 free: 193144 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 12 12 32 32 32 32
1:  12 10 12 12 32 32 32 32
2:  12 12 10 12 32 32 32 32
3:  12 12 12 10 32 32 32 32
4:  32 32 32 32 10 12 12 12
5:  32 32 32 32 12 10 12 12
6:  32 32 32 32 12 12 10 12
7:  32 32 32 32 12 12 12 10

```

```

-----
9. /proc/meminfo
MemTotal:      1584999448 kB

```

```

-----
10. who -r
run-level 3 Jan 11 08:05

```

```

-----
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.11)
Default Target Status
multi-user      degraded

```

```

-----
12. Failed units, from systemctl list-units --state=failed
UNIT                                LOAD    ACTIVE SUB    DESCRIPTION
* systemd-networkd-wait-online.service loaded failed failed Wait for Network to be Configured

```

```

-----
13. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback irqbalance keyboard-setup lm-sensors lvm2-monitor lxd-agent multipathd networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb ssh systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw vgauth
enabled-runtime netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled apparmor console-getty debug-shell iscsid nftables rsync serial-getty@ systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync upower
generated apport
indirect uuid
masked cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcS screen-cleanup sudo x11-common

```

```

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.15.0-91-generic
root=UUID=63a5bd49-1a2b-4fc5-945b-bc07b22b6218
ro

```

```

-----
15. cpupower frequency-info
analyzing CPU 0:

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

### Platform Notes (Continued)

current policy: frequency should be within 1.50 GHz and 2.70 GHz.  
The governor "performance" may decide which speed to use within this range.

boost state support:  
Supported: yes  
Active: yes  
Boost States: 0  
Total States: 3  
Pstate-P0: 2700MHz

```

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  8
vm.dirty_writeback_centisecs    500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
-----

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag          [always] defer defer+madvise madvise never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force
-----

```

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs   10000
-----

```

```

-----
19. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 22.04.3 LTS
-----

```

```

-----
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p2 ext4  732G  52G  644G   8% /
-----

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

### Platform Notes (Continued)

21. /sys/devices/virtual/dmi/id  
Vendor: Supermicro  
Product: Super Server  
Product Family: SMC H13  
Serial: 0123456789

22. dmidecode  
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
24x Micron Technology MTC40F2046S1RC48BA1 64 GB 2 rank 4800

23. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: 1.6  
BIOS Date: 11/16/2023  
BIOS Revision: 5.27

### Compiler Version Notes

C | 502.gcc\_r(peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

C | 502.gcc\_r(peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

### Compiler Version Notes (Continued)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====  
C++ | 523.xalancbmk\_r(peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base, peak) 541.leela\_r(base, peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====  
C++ | 523.xalancbmk\_r(peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base, peak) 541.leela\_r(base, peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====  
Fortran | 548.exchange2\_r(base, peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

### Base Compiler Invocation

C benchmarks:  
clang

(Continued on next page)



# SPEC CPU<sup>®</sup>2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate<sup>®</sup>2017\_int\_base = 721

SPECrate<sup>®</sup>2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

## Base Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-z muldefs -O3 -march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc
```

C++ benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -z muldefs -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang
-lamdalloc-ext
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

## Base Optimization Flags (Continued)

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

## Peak Portability Flags (Continued)

520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: basepeak = yes

502.gcc\_r: -m32 -flto -z muldefs -Ofast -march=znver4  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline  
-lamdalloc

505.mcf\_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lflang -lamdalloc

525.x264\_r: basepeak = yes

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: -m32 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-do-block-reorder=aggressive  
-fno-loop-reroll -Ofast -march=znver4 -fveclib=AMDLIBM  
-ffast-math -finline-aggressive  
-mllvm -unroll-threshold=100

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Jan-2024  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2023

## Peak Optimization Flags (Continued)

523.xalancbmk\_r (continued):

```
-mllvm -reduce-array-computations=3 -zopt  
-mllvm -do-block-reorder=aggressive  
-fvirtual-function-elimination -fvisibility=hidden  
-lamdalloc-ext
```

```
531.deepsjeng_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3  
-march=znver4 -fveclib=AMDLIBM -ffast-math  
-mllvm -unroll-threshold=100 -finline-aggressive  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -zopt  
-fvirtual-function-elimination -fvisibility=hidden  
-lamdlibm -lamdalloc-ext
```

541.leela\_r: basepeak = yes

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM  
-ffast-math -fepilog-vectorization-of-inductions  
-mllvm -optimize-strided-mem-cost -floop-transform  
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm  
-lflang -lamdalloc
```

## Peak Other Flags

C benchmarks (except as noted below):

```
-Wno-unused-command-line-argument
```

```
502.gcc_r: -L/usr/lib32 -Wno-unused-command-line-argument  
-L/home/work/cpu2017/v119/aocc4/znver4/rate/amd_rate_aocc400_znver4_A_lib/lib32
```

C++ benchmarks (except as noted below):

```
-Wno-unused-command-line-argument
```

```
523.xalancbmk_r: -L/usr/lib32 -Wno-unused-command-line-argument  
-L/home/work/cpu2017/v119/aocc4/znver4/rate/amd_rate_aocc400_znver4_A_lib/lib32
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2025HS-TNR  
(H13DSH , AMD EPYC 9334)

SPECrate®2017\_int\_base = 721

SPECrate®2017\_int\_peak = 743

**CPU2017 License:** 001176

**Test Sponsor:** Supermicro

**Tested by:** Supermicro

**Test Date:** Jan-2024

**Hardware Availability:** Nov-2022

**Software Availability:** Nov-2023

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags-A1.2.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Genoa-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags-A1.2.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Genoa-revC.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-01-11 03:42:34-0500.

Report generated on 2024-01-30 23:28:52 by CPU2017 PDF formatter v6716.

Originally published on 2024-01-30.