SPEC CPU®2017 Integer Rate Result

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Dell Inc.

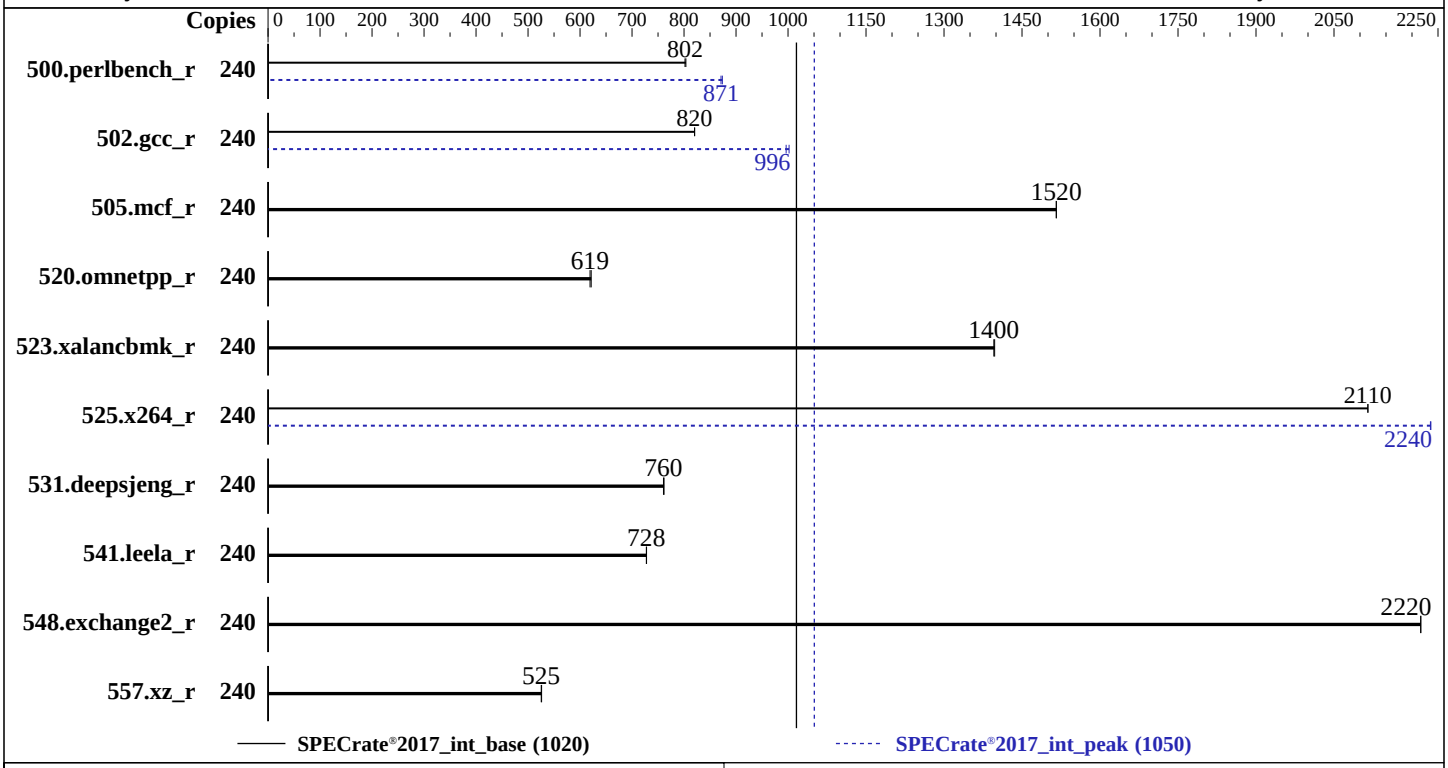
PowerEdge R760 (Intel Xeon Platinum 8580)

SPECrate®2017_int_base = 1020

SPECrate®2017_int_peak = 1050

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2023
Hardware Availability: Feb-2024
Software Availability: Dec-2023



Hardware

CPU Name:	Intel Xeon Platinum 8580
Max MHz:	4000
Nominal:	2000
Enabled:	120 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	300 MB I+D on chip per chip
Other:	None
Memory:	1 TB (16 x 64 GB 2Rx4 PC5-5600B-R)
Storage:	130 GB on tmpfs
Other:	None

Software

OS:	SUSE Linux Enterprise Server 15 SP5 5.14.21-150500.53-default
Compiler:	C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
Parallel:	No
Firmware:	Version 1.9.12 released Nov-2023
File System:	tmpfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage.

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	240	475	804	**476**	**802**			240	437	874	**439**	**871**		
502.gcc_r	240	414	821	**414**	**820**			240	339	1000	**341**	**996**		
505.mcf_r	240	**256**	**1520**	256	1520			240	**256**	**1520**	256	1520		
520.omnetpp_r	240	507	621	**509**	**619**			240	507	621	**509**	**619**		
523.xalancbmk_r	240	181	1400	**182**	**1400**			240	181	1400	**182**	**1400**		
525.x264_r	240	**199**	**2110**	199	2120			240	**188**	**2240**	188	2240		
531.deepsjeng_r	240	**362**	**760**	361	762			240	**362**	**760**	361	762		
541.leela_r	240	546	728	**546**	**728**			240	546	728	**546**	**728**		
548.exchange2_r	240	284	2220	**284**	**2220**			240	284	2220	**284**	**2220**		
557.xz_r	240	493	526	**493**	**525**			240	493	526	**493**	**525**		

SPECrate®2017_int_base = 1020

SPECrate®2017_int_peak = 1050

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/ia32:/mnt
     /ramdisk/cpu2017-1.1.9-ic2023.2.3/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

(Continued on next page)

General Notes (Continued)

```
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Benchmark run from a 130 GB ramdisk created with the cmd: "mount -t tmpfs -o size=130G tmpfs /mnt/ramdisk"
```

Platform Notes

```
BIOS settings:
                 ADDDC Setting : Disabled
        DIMM Self Healing on
    Uncorrectable Memory Error : Disabled

     Virtualization Technology : Disabled
       DCU Streamer Prefetcher : Disabled
              Sub NUMA Cluster : 2-way Clustering
                  LLC Prefetch : Disabled
           Dead Line LLC Alloc : Disabled
                Optimizer Mode : Enabled

                System Profile : Custom
          CPU Power Management : Maximum Performance
                           C1E : Disabled
                      C States : Autonomous
           Memory Patrol Scrub : Disabled
      Energy Efficiency Policy : Performance
             PCI ASPM L1 Link
               Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Nov 27 13:58:04 2023

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. sysctl
15. /sys/kernel/mm/transparent_hugepage
16. /sys/kernel/mm/transparent_hugepage/khugepaged
17. OS release
```

(Continued on next page)

Platform Notes (Continued)

```
18. Disk information
19. /sys/devices/virtual/dmi/id
20. dmidecode
21. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   13:58:04 up 2 min,  1 user,  load average: 0.26, 0.29, 0.12
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                13:57   60.00s  0.88s  0.00s /bin/bash ./dell-run-speccpu.sh rate
  --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define
  DL-VERS=v4.8.4 --output_format html,pdf,txt --define DL-LQC=1

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 4124118
  max locked memory       (kbytes, -l) 64
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 4124118
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize 29
 login -- root
 -bash
 /bin/bash ./DELL_rate.sh
 /bin/bash ./dell-run-main.sh rate
 /bin/bash ./dell-run-main.sh rate
 /bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1
   --define DL-BIOS-adddcD=1 --define DL-VERS=v4.8.4 --output_format html,pdf,txt --define DL-LQC=1
 /bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1
   --define DL-BIOS-adddcD=1 --define DL-VERS=v4.8.4 --output_format html,pdf,txt --define DL-LQC=1
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=240 -c
   ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=120 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base,peak -o all --define DL-BIOS-SNC=2
   --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define
   DL-BIOS-adddcD=1 --define DL-VERS=v4.8.4 --output_format html,pdf,txt --define DL-LQC=1 intrate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=240 --configfile
```

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Platform Notes (Continued)

```
    ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=120 --define physicalfirst
    --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --define
    DL-BIOS-SNC=2 --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define
    DL-BIOS-adddcD=1 --define DL-VERS=v4.8.4 --output_format html,pdf,txt --define DL-LQC=1 --nopower
    --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
    $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3

------------------------------------------------------------
6. /proc/cpuinfo
     model name      : INTEL(R) XEON(R) PLATINUM 8580
     vendor_id       : GenuineIntel
     cpu family      : 6
     model           : 207
     stepping        : 2
     microcode       : 0x210001b0
     bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
     cpu cores       : 60
     siblings        : 120
     2 physical ids (chips)
     240 processors (hardware threads)
     physical id 0: core ids 0-59
     physical id 1: core ids 0-59
     physical id 0: apicids 0-119
     physical id 1: apicids 128-247
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   46 bits physical, 57 bits virtual
  Byte Order:                      Little Endian
  CPU(s):                          240
  On-line CPU(s) list:             0-239
  Vendor ID:                       GenuineIntel
  Model name:                      INTEL(R) XEON(R) PLATINUM 8580
  CPU family:                      6
  Model:                           207
  Thread(s) per core:              2
  Core(s) per socket:              60
  Socket(s):                       2
  Stepping:                        2
  BogoMIPS:                        4000.00
  Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                   clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                   lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                   nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                   ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                                   x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                                   abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                                   cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle
                                   avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                                   avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                                   xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                   cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
```

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Platform Notes (Continued)

```
                        avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                        avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                        cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                        arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities
  L1d cache:            5.6 MiB (120 instances)
  L1i cache:            3.8 MiB (120 instances)
  L2 cache:             240 MiB (120 instances)
  L3 cache:             600 MiB (2 instances)
  NUMA node(s):         4
  NUMA node0 CPU(s):    0,4,8,12,16,20,24,28,32,36,42,46,50,54,58,62,66,70,74,78,82,86,90,94,98,10
                        2,106,108,112,116,120,124,128,132,136,140,144,148,152,156,162,166,170,174,
                        178,182,186,190,194,198,202,206,210,214,218,222,226,228,232,236
  NUMA node1 CPU(s):    2,6,10,14,18,22,26,30,34,38,40,44,48,52,56,60,64,68,72,76,80,84,88,92,96,1
                        00,104,110,114,118,122,126,130,134,138,142,146,150,154,158,160,164,168,172
                        ,176,180,184,188,192,196,200,204,208,212,216,220,224,230,234,238
  NUMA node2 CPU(s):    1,5,7,11,15,19,23,25,29,33,37,41,45,49,53,57,61,65,69,73,77,83,87,91,95,99
                        ,103,109,113,117,121,125,127,131,135,139,143,145,149,153,157,161,165,169,1
                        73,177,181,185,189,193,197,203,207,211,215,219,223,229,233,237
  NUMA node3 CPU(s):    3,9,13,17,21,27,31,35,39,43,47,51,55,59,63,67,71,75,79,81,85,89,93,97,101,
                        105,107,111,115,119,123,129,133,137,141,147,151,155,159,163,167,171,175,17
                        9,183,187,191,195,199,201,205,209,213,217,221,225,227,231,235,239
  Vulnerability Itlb multihit:     Not affected
  Vulnerability L1tf:              Not affected
  Vulnerability Mds:               Not affected
  Vulnerability Meltdown:          Not affected
  Vulnerability Mmio stale data:   Not affected
  Vulnerability Retbleed:          Not affected
  Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
  Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:        Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
                                   sequence
  Vulnerability Srbds:             Not affected
  Vulnerability Tsx async abort:   Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL   SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     5.6M   12 Data            1     64        1             64
     L1i       32K     3.8M    8 Instruction     1     64        1             64
     L2         2M     240M   16 Unified         2   2048        1             64
     L3       300M     600M   20 Unified         3 245760        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus:
  0,4,8,12,16,20,24,28,32,36,42,46,50,54,58,62,66,70,74,78,82,86,90,94,98,102,106,108,112,116,120,124,128,132
  ,136,140,144,148,152,156,162,166,170,174,178,182,186,190,194,198,202,206,210,214,218,222,226,228,232,236
  node 0 size: 257379 MB
  node 0 free: 256804 MB
  node 1 cpus:
  2,6,10,14,18,22,26,30,34,38,40,44,48,52,56,60,64,68,72,76,80,84,88,92,96,100,104,110,114,118,122,126,130,13
  4,138,142,146,150,154,158,160,164,168,172,176,180,184,188,192,196,200,204,208,212,216,220,224,230,234,238
  node 1 size: 258032 MB
  node 1 free: 247009 MB
  node 2 cpus:
  1,5,7,11,15,19,23,25,29,33,37,41,45,49,53,57,61,65,69,73,77,83,87,91,95,99,103,109,113,117,121,125,127,131,
  135,139,143,145,149,153,157,161,165,169,173,177,181,185,189,193,197,203,207,211,215,219,223,229,233,237
  node 2 size: 258032 MB
  node 2 free: 257258 MB
```

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Platform Notes (Continued)

```
  node 3 cpus:
  3,9,13,17,21,27,31,35,39,43,47,51,55,59,63,67,71,75,79,81,85,89,93,97,101,105,107,111,115,119,123,129,133,1
  37,141,147,151,155,159,163,167,171,175,179,183,187,191,195,199,201,205,209,213,217,221,225,227,231,235,239
  node 3 size: 257608 MB
  node 3 free: 256992 MB
  node distances:
  node   0   1   2   3
    0:  10  12  21  21
    1:  12  10  21  21
    2:  21  21  10  12
    3:  21  21  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1055798188 kB

------------------------------------------------------------
10. who -r
  run-level 3 Nov 27 13:56

------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings kdump
                   kdump-early nvmefc-boot-connections postfix purge-kernels rollback sshd systemd-pstore
                   wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebtables
                   grub2-once haveged haveged-switch-root issue-add-ssh-keys kexec-load lunmask nfs
                   nfs-blkmap nvmf-autoconnect rpcbind rpmconfigcheck rsyncd serial-getty@
                   systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                   systemd-time-wait-sync systemd-timesyncd
  indirect         wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
  root=UUID=b8c6d51c-250a-4d03-852b-82b1db828fe3
  splash=silent
  mitigations=auto
  quiet
  security=apparmor
  crashkernel=386M,high
  crashkernel=72M,low

------------------------------------------------------------
14. sysctl
  kernel.numa_balancing              1
  kernel.randomize_va_space          2
  vm.compaction_proactiveness       20
  vm.dirty_background_bytes          0
  vm.dirty_background_ratio         10
  vm.dirty_bytes                     0
  vm.dirty_expire_centisecs       3000
  vm.dirty_ratio                    20
```

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Platform Notes (Continued)

```
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
15. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
17. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP5

------------------------------------------------------------
18. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
  Filesystem     Type   Size  Used Avail Use% Mounted on
  tmpfs          tmpfs  130G  5.1G  125G   4% /mnt/ramdisk

------------------------------------------------------------
19. /sys/devices/virtual/dmi/id
    Vendor:         Dell Inc.
    Product:        PowerEdge R760
    Product Family: PowerEdge
    Serial:         SLR7602

------------------------------------------------------------
20. dmidecode
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    16x 002C0632002C MTC40F2046S1RC56BG1 64 GB 2 rank 5600


------------------------------------------------------------
21. BIOS
(This section combines info from /sys/devices and dmidecode.)
```

(Continued on next page)

Platform Notes (Continued)

```
    BIOS Vendor:       Dell Inc.
    BIOS Version:      1.9.12
    BIOS Date:         11/10/2023
    BIOS Revision:     1.9
```

Compiler Version Notes

```
==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
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==============================================================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
        | 541.leela_r(base, peak)
--------------------------------------------------------------------------------------------------------------
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==============================================================================================================
Fortran | 548.exchange2_r(base, peak)
--------------------------------------------------------------------------------------------------------------
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--------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

(Continued on next page)

Base Compiler Invocation (Continued)

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

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Peak Optimization Flags (Continued)

505.mcf_r: `basepeak = yes`

525.x264_r: `-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fno-alias -L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin -lqkmalloc`

557.xz_r: `basepeak = yes`

C++ benchmarks:

520.omnetpp_r: `basepeak = yes`

523.xalancbmk_r: `basepeak = yes`

531.deepsjeng_r: `basepeak = yes`

541.leela_r: `basepeak = yes`

Fortran benchmarks:

548.exchange2_r: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml`

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