SPEC CPU®2017 Floating Point Rate Result

Lenovo Global Technology

ThinkSystem SR630 V3
(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_fp_base =   1120
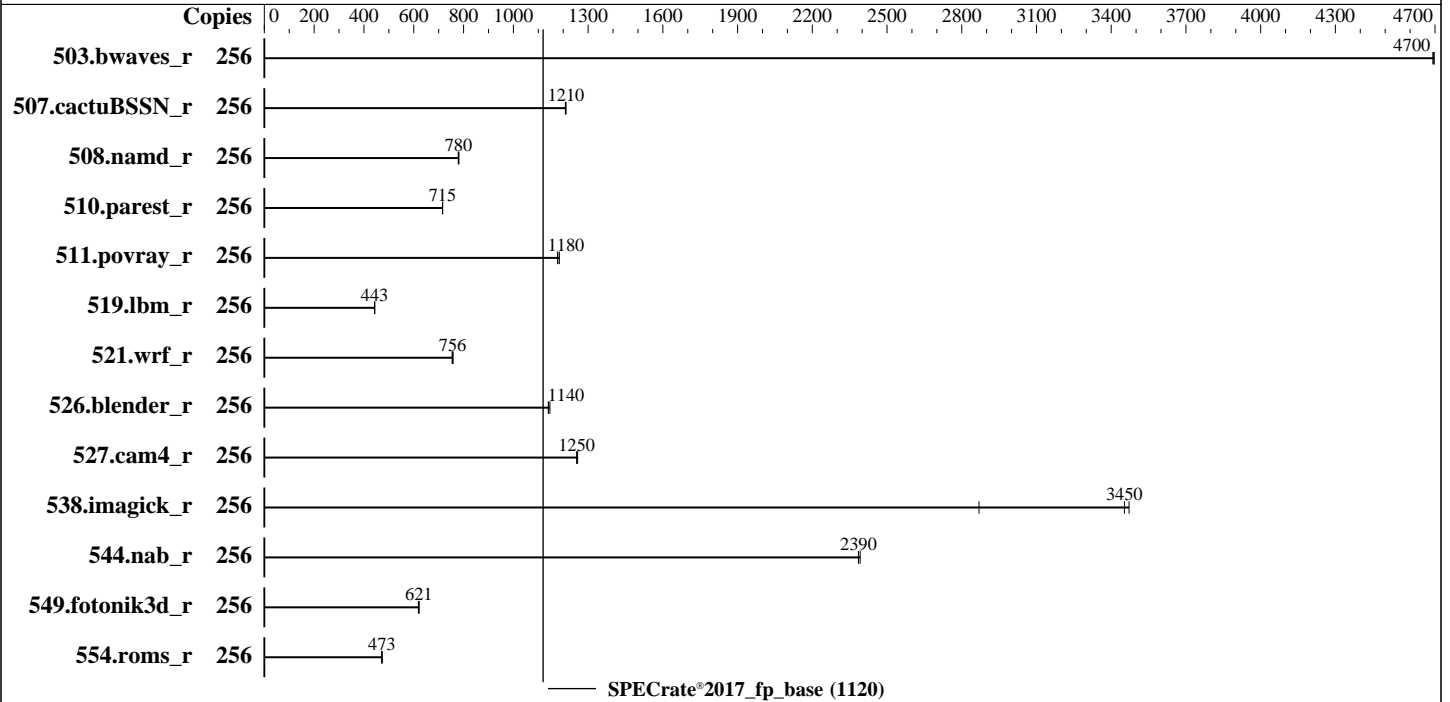
SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Nov-2023
Hardware Availability: Feb-2024
Software Availability: Jun-2023

Benchmark	Copies	Result
503.bwaves_r	256	4700
507.cactuBSSN_r	256	1210
508.namd_r	256	780
510.parest_r	256	715
511.povray_r	256	1180
519.lbm_r	256	443
521.wrf_r	256	756
526.blender_r	256	1140
527.cam4_r	256	1250
538.imagick_r	256	3450
544.nab_r	256	2390
549.fotonik3d_r	256	621
554.roms_r	256	473

SPECrate®2017_fp_base (1120)

Hardware

CPU Name: Intel Xeon Platinum 8592+
Max MHz: 3900
Nominal: 1900
Enabled: 128 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 320 MB I+D on chip per chip
 Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R)
Storage: 1 x 960 GB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4
 Kernel 5.14.21-150400.22-default
Compiler: C/C++: Version 2023.2 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.2 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Lenovo BIOS Version ESE121P 3.10 released Oct-2023
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage

Standard Performance Evaluation Corporation (info@spec.org)
https://www.spec.org/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	256	547	4700	**547**	**4700**	547	4690							
507.cactuBSSN_r	256	268	1210	268	1210	**268**	**1210**							
508.namd_r	256	**312**	**780**	311	781	312	779							
510.parest_r	256	**936**	**715**	935	716	936	715							
511.povray_r	256	**507**	**1180**	505	1180	508	1180							
519.lbm_r	256	609	443	609	443	**609**	**443**							
521.wrf_r	256	**759**	**756**	761	754	757	758							
526.blender_r	256	342	1140	**342**	**1140**	340	1150							
527.cam4_r	256	357	1250	**357**	**1250**	356	1260							
538.imagick_r	256	**184**	**3450**	222	2870	183	3470							
544.nab_r	256	181	2390	**181**	**2390**	180	2390							
549.fotonik3d_r	256	1613	619	**1607**	**621**	1607	621							
554.roms_r	256	860	473	864	471	**861**	**473**							

SPECrate®2017_fp_base =	**1120**
SPECrate®2017_fp_peak =	**Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-ic2023.2/lib/intel64:/home/cpu2017-1.1.9-ic2023.2/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>   /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.

(Continued on next page)

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General Notes (Continued)

```
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
SNC set to SNC2
LLC Prefetch set to Disabled
AMP Prefetch set to Enable

 Sysinfo program /home/cpu2017-1.1.9-ic2023.2/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on test1 Mon Nov 20 02:24:38 2023

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
 ------------------------------------------------------------


 ------------------------------------------------------------
 1. uname -a
   Linux test1 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222) x86_64
   x86_64 x86_64 GNU/Linux


 ------------------------------------------------------------
 2. w
   02:24:38 up 2 days,  9:23,  1 user,  load average: 150.81, 229.01, 244.92
   USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
   root     tty1     -                Fri17    2days  1.16s  0.01s sh 1.sh
```

(Continued on next page)

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Platform Notes (Continued)

```
          ----------------------------------------------------------
          3. Username
            From environment variable $USER:  root


          ----------------------------------------------------------
          4. ulimit -a
            core file size          (blocks, -c) unlimited
            data seg size           (kbytes, -d) unlimited
            scheduling priority             (-e) 0
            file size               (blocks, -f) unlimited
            pending signals                 (-i) 4126600
            max locked memory       (kbytes, -l) 64
            max memory size         (kbytes, -m) unlimited
            open files                      (-n) 1024
            pipe size            (512 bytes, -p) 8
            POSIX message queues     (bytes, -q) 819200
            real-time priority              (-r) 0
            stack size              (kbytes, -s) unlimited
            cpu time               (seconds, -t) unlimited
            max user processes              (-u) 4126600
            virtual memory          (kbytes, -v) unlimited
            file locks                      (-x) unlimited


          ----------------------------------------------------------
          5. sysinfo process ancestry
           /usr/lib/systemd/systemd --switched-root --system --deserialize 30
           login -- root
           -bash
           sh 1.sh
           sh 1.sh
           runcpu --nobuild --action validate --define default-platform-flags --define numcopies=256 -c
             ic2023.2-lin-sapphirerapids-rate-20230622.cfg --define smt-on --define cores=128 --define physicalfirst
             --define invoke_with_interleave --define drop_caches --tune base -o all fprate
           runcpu --nobuild --action validate --define default-platform-flags --define numcopies=256 --configfile
             ic2023.2-lin-sapphirerapids-rate-20230622.cfg --define smt-on --define cores=128 --define physicalfirst
             --define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
             rate --tune base --size refrate fprate --nopreenv --note-preenv --logfile
             $SPEC/tmp/CPU2017.121/templogs/preenv.fprate.121.0.log --lognum 121.0 --from_runcpu 2
           specperl $SPEC/bin/sysinfo
          $SPEC = /home/cpu2017-1.1.9-ic2023.2


          ----------------------------------------------------------
          6. /proc/cpuinfo
             model name       : INTEL(R) XEON(R) PLATINUM 8592+
             vendor_id        : GenuineIntel
             cpu family       : 6
             model            : 207
             stepping         : 2
             microcode        : 0x21000190
             bugs             : spectre_v1 spectre_v2 spec_store_bypass swapgs
             cpu cores        : 64
             siblings         : 128
             2 physical ids (chips)
             256 processors (hardware threads)
             physical id 0: core ids 0-63
             physical id 1: core ids 0-63
             physical id 0: apicids 0-127
             physical id 1: apicids 128-255
            Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
```

(Continued on next page)

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Platform Notes (Continued)

```
   virtualized systems.  Use the above data carefully.


    -----------------------------------------------------------
   7. lscpu

   From lscpu from util-linux 2.37.2:
     Architecture:            x86_64
     CPU op-mode(s):          32-bit, 64-bit
     Address sizes:           46 bits physical, 57 bits virtual
     Byte Order:              Little Endian
     CPU(s):                  256
     On-line CPU(s) list:     0-255
     Vendor ID:               GenuineIntel
     Model name:              INTEL(R) XEON(R) PLATINUM 8592+
     CPU family:              6
     Model:                   207
     Thread(s) per core:      2
     Core(s) per socket:      64
     Socket(s):               2
     Stepping:                2
     BogoMIPS:                3800.00
     Flags:                   fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                              clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                              lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                              nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 ds_cpl
                              vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                              x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                              abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                              cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority
                              ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
                              rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
                              intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves
                              cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local avx_vnni avx512_bf16
                              wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku ospke waitpkg
                              avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                              avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                              enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
                              amx_tile flush_l1d arch_capabilities
     Virtualization:          VT-x
     L1d cache:               6 MiB (128 instances)
     L1i cache:               4 MiB (128 instances)
     L2 cache:                256 MiB (128 instances)
     L3 cache:                640 MiB (2 instances)
     NUMA node(s):            4
     NUMA node0 CPU(s):       0-31,128-159
     NUMA node1 CPU(s):       32-63,160-191
     NUMA node2 CPU(s):       64-95,192-223
     NUMA node3 CPU(s):       96-127,224-255
     Vulnerability Itlb multihit:     Not affected
     Vulnerability L1tf:              Not affected
     Vulnerability Mds:               Not affected
     Vulnerability Meltdown:          Not affected
     Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
     Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user pointer sanitization
     Vulnerability Spectre v2:        Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
     Vulnerability Srbds:             Not affected
     Vulnerability Tsx async abort:   Not affected

   From lscpu --cache:
         NAME ONE-SIZE ALL-SIZE WAYS TYPE         LEVEL   SETS PHY-LINE COHERENCY-SIZE
```

(Continued on next page)

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Platform Notes (Continued)

```
    L1d       48K       6M  12 Data          1    64      1          64
    L1i       32K       4M   8 Instruction   1    64      1          64
    L2         2M     256M  16 Unified       2  2048      1          64
    L3       320M     640M  20 Unified       3 262144     1          64


    --------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0-31,128-159
  node 0 size: 257695 MB
  node 0 free: 255633 MB
  node 1 cpus: 32-63,160-191
  node 1 size: 258031 MB
  node 1 free: 256697 MB
  node 2 cpus: 64-95,192-223
  node 2 size: 258031 MB
  node 2 free: 256687 MB
  node 3 cpus: 96-127,224-255
  node 3 size: 257914 MB
  node 3 free: 256606 MB
  node distances:
  node   0   1   2   3
    0:  10  12  21  21
    1:  12  10  21  21
    2:  21  21  10  12
    3:  21  21  12  10


    --------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1056434204 kB


    --------------------------------------------------------------
10. who -r
  run-level 3 Nov 17 17:03


    --------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
  Default Target  Status
  multi-user      running


    --------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance iscsi
                   issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog
                   smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                   chronyd console-getty cups cups-browsed debug-shell ebtables exchange-bmc-os-info
                   firewalld gpm grub2-once haveged-switch-root ipmi ipmievd iscsi-init iscsid iscsiuio
                   issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb rdisc
                   rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts smb snmpd snmptrapd
                   systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                   systemd-time-wait-sync systemd-timesyncd
  generated        ntp_sync
  indirect         wickedd


    --------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
```

(Continued on next page)

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Platform Notes (Continued)

```
  BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
  root=UUID=f976c541-a329-4c54-ba84-4be16556ee18
  splash=silent
  mitigations=auto
  quiet
  security=apparmor


  ----------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 0:
    Unable to determine current policy
    boost state support:
      Supported: yes
      Active: yes


  ----------------------------------------------------------
15. sysctl
  kernel.numa_balancing            1
  kernel.randomize_va_space        2
  vm.compaction_proactiveness      20
  vm.dirty_background_bytes        0
  vm.dirty_background_ratio        10
  vm.dirty_bytes                   0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                   20
  vm.dirty_writeback_centisecs     500
  vm.dirtytime_expire_seconds      43200
  vm.extfrag_threshold             500
  vm.min_unmapped_ratio            1
  vm.nr_hugepages                  0
  vm.nr_hugepages_mempolicy        0
  vm.nr_overcommit_hugepages       0
  vm.swappiness                    60
  vm.watermark_boost_factor        15000
  vm.watermark_scale_factor        10
  vm.zone_reclaim_mode             0


  ----------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag         always defer defer+madvise [madvise] never
  enabled        [always] madvise never
  hpage_pmd_size 2097152
  shmem_enabled   always within_size advise [never] deny force


  ----------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                  1
  max_ptes_none           511
  max_ptes_shared         256
  max_ptes_swap           64
  pages_to_scan           4096
  scan_sleep_millisecs    10000

  ----------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP4

  ----------------------------------------------------------
```

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Platform Notes (Continued)

```
   19. Disk information
 SPEC is set to: /home/cpu2017-1.1.9-ic2023.2
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sdb2       xfs   894G  181G  714G  21% /

   --------------------------------------------------------------
   20. /sys/devices/virtual/dmi/id
       Vendor:         Lenovo
       Product:        ThinkSystem SR630 V3
       Product Family: ThinkSystem
       Serial:         1234567890

   --------------------------------------------------------------
   21. dmidecode
     Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
     The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
     "DMTF SMBIOS" standard.
     Memory:
       16x Samsung M321R8GA0PB0-CWMKH 64 GB 2 rank 5600


   --------------------------------------------------------------
   22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
     BIOS Vendor:       Lenovo
     BIOS Version:      ESE121P-3.10
     BIOS Date:         10/27/2023
     BIOS Revision:     3.10
     Firmware Revision: 3.90
```

Compiler Version Notes

```
==============================================================================
C               | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++             | 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++, C          | 511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base)
------------------------------------------------------------------------------
```

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Compiler Version Notes (Continued)

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------


==============================================================================================
Fortran        | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------


==============================================================================================
Fortran, C     | 521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifx`

Benchmarks using both Fortran and C:
`ifx icx`

Benchmarks using both C and C++:
`icpx icx`

Benchmarks using Fortran, C, and C++:
`icpx icx ifx`

Base Portability Flags

503.bwaves_r: `-DSPEC_LP64`
507.cactuBSSN_r: `-DSPEC_LP64`

(Continued on next page)

　　Standard Performance Evaluation Corporation (info@spec.org)　　https://www.spec.org/

Lenovo Global Technology

SPECrate®2017_fp_base = 1120

ThinkSystem SR630 V3
(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Nov-2023
Hardware Availability: Feb-2024
Software Availability: Jun-2023

Base Portability Flags (Continued)

508.namd_r: `-DSPEC_LP64`
510.parest_r: `-DSPEC_LP64`
511.povray_r: `-DSPEC_LP64`
519.lbm_r: `-DSPEC_LP64`
521.wrf_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
526.blender_r: `-DSPEC_LP64 -DSPEC_LINUX -funsigned-char`
527.cam4_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
538.imagick_r: `-DSPEC_LP64`
544.nab_r: `-DSPEC_LP64`
549.fotonik3d_r: `-DSPEC_LP64`
554.roms_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

C++ benchmarks:
```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:
```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:
```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:
```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

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SPEC CPU®2017 Floating Point Rate Result

Lenovo Global Technology

ThinkSystem SR630 V3
(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_fp_base = 1120

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017		**Test Date:**	Nov-2023
Test Sponsor: Lenovo Global Technology		**Hardware Availability:**	Feb-2024
Tested by: Lenovo Global Technology		**Software Availability:**	Jun-2023

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-11-19 13:24:37-0500.
Report generated on 2024-01-03 16:15:24 by CPU2017 PDF formatter v6716.
Originally published on 2023-12-14.