SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

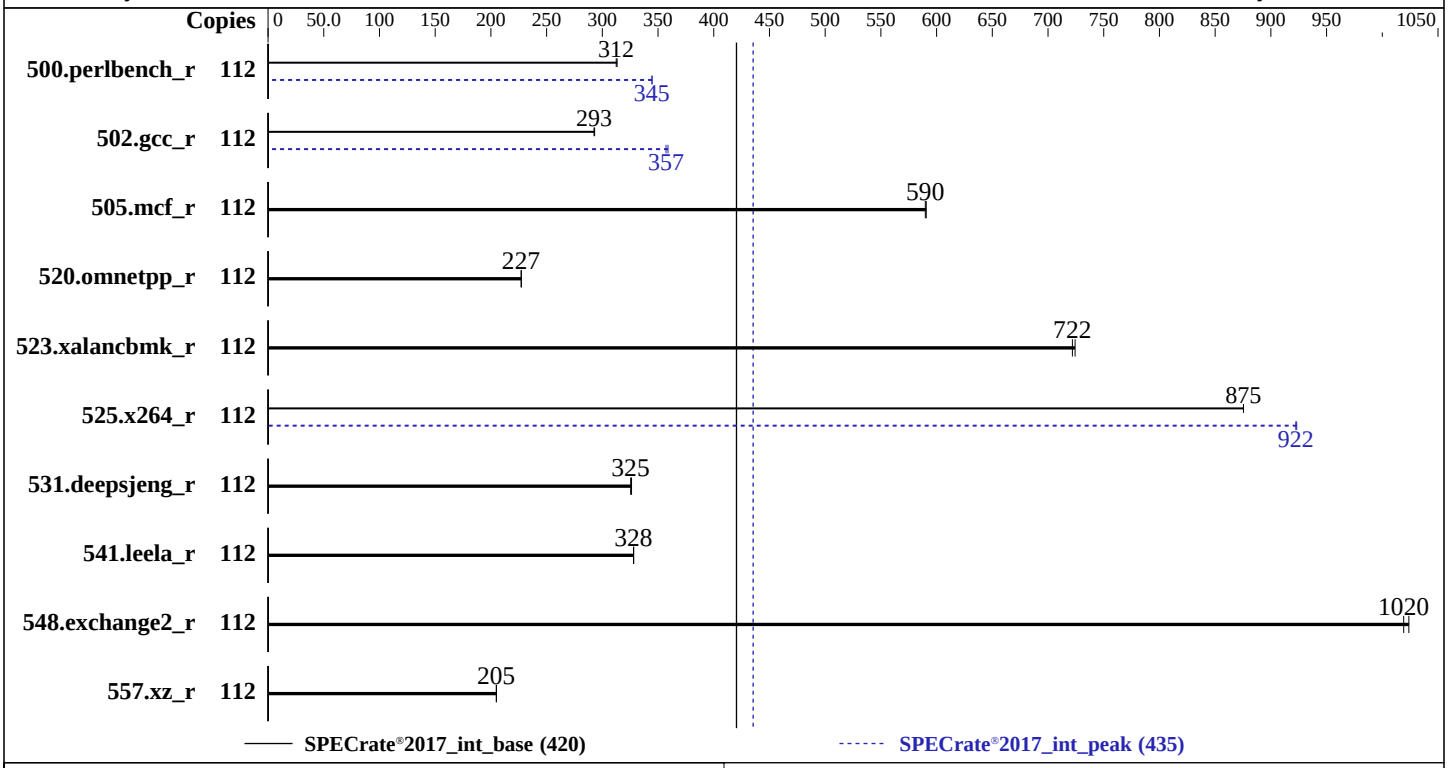
Precision 7960 Tower (Intel Xeon w9-3495X)

SPECrate®2017_int_base = 420

SPECrate®2017_int_peak = 435

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Oct-2023
Hardware Availability: Jul-2023
Software Availability: Dec-2022



Hardware

CPU Name:	Intel Xeon w9-3495X
Max MHz:	4800
Nominal:	1900
Enabled:	56 cores, 1 chip, 2 threads/core
Orderable:	1 chip
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	105 MB I+D on chip per chip
Other:	None
Memory:	1 TB (16 x 64 GB 2Rx4 PC5-4800B-R, running at 4400)
Storage:	80 GB on tmpfs
Other:	None

Software

OS:	SUSE Linux Enterprise Server 15 SP4 5.14.21-150400.22-default
Compiler:	C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;
Parallel:	No
Firmware:	Version 1.1.10 released Jul-2023
File System:	tmpfs
System State:	Run level 5 (graphical multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage.

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

Precision 7960 Tower (Intel Xeon w9-3495X)

SPECrate®2017_int_base = 420

SPECrate®2017_int_peak = 435

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Oct-2023
Hardware Availability: Jul-2023
Software Availability: Dec-2022

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	112	569	313	**571**	**312**			112	517	345	**517**	**345**		
502.gcc_r	112	**542**	**293**	541	293			112	**444**	**357**	442	359		
505.mcf_r	112	**307**	**590**	306	591			112	**307**	**590**	306	591		
520.omnetpp_r	112	**647**	**227**	646	227			112	**647**	**227**	646	227		
523.xalancbmk_r	112	**164**	**722**	163	724			112	**164**	**722**	163	724		
525.x264_r	112	224	875	**224**	**875**			112	**213**	**922**	212	923		
531.deepsjeng_r	112	**394**	**325**	393	326			112	**394**	**325**	393	326		
541.leela_r	112	565	328	**565**	**328**			112	565	328	**565**	**328**		
548.exchange2_r	112	287	1020	**288**	**1020**			112	287	1020	**288**	**1020**		
557.xz_r	112	590	205	**591**	**205**			112	590	205	**591**	**205**		

SPECrate®2017_int_base = 420

SPECrate®2017_int_peak = 435

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
SPEC has ruled that the compiler used for this result was performing a compilation
that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s
benchmarks using a priori knowledge of the SPEC code and dataset to perform a
transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4
https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer
publish results using this optimization.

This result is left in the SPEC results database for historical reference.
```

Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/lib/ia32:/mnt/ram
     disk/cpu2017-1.1.9-ic2023.0/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Benchmark run from a 80 GB ramdisk created with the cmd: "mount -t tmpfs -o size=80G tmpfs /mnt/ramdisk"
```

Platform Notes

```
BIOS settings:
     Virtualization Technology : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.0/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Oct 16 10:46:42 2023

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
------------------------------------------------------------

------------------------------------------------------------
```

(Continued on next page)

Platform Notes (Continued)

```
1. uname -a
  Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   10:46:42 up 11 min,  2 users,  load average: 0.38, 0.44, 0.48
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  dell     console  :0               10:35   11:38   0.00s  0.04s gdm-session-worker [pam/gdm-autologin]
  dell     :0       :0               10:35   ?xdm?   4:43   0.12s /usr/lib/gnome-session-binary

------------------------------------------------------------
3. Username
  From environment variable $USER:  root
  From the command 'logname':       dell

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) 0
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 4120955
  max locked memory       (kbytes, -l) 64
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 4120955
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize 30
 /usr/lib/systemd/systemd --user
 /usr/lib/gnome-terminal-server
 bash
 sudo ./DELL_rate.sh
 /bin/bash ./DELL_rate.sh
 /bin/bash ./dell-run-main.sh rate
 /bin/bash ./dell-run-main.sh rate
 /bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-3w.inc --define DL-BIOS-LogProc=1
   --define DL-BIOS-adddcD=1 --define DL-VERS=v4.8 --output_format html,pdf,txt
 /bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-3w.inc --define DL-BIOS-LogProc=1
   --define DL-BIOS-adddcD=1 --define DL-VERS=v4.8 --output_format html,pdf,txt
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=112 -c
   ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=56 --define physicalfirst
   --define no-numa --tune base,peak -o all --define drop_caches --define DL-LQC=1 --iterations 2 --define
   DL-BIOSinc=Dell-BIOS_Xeon-3w.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define
   DL-VERS=v4.8 --output_format html,pdf,txt intrate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=112 --configfile
   ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=56 --define physicalfirst
   --define no-numa --tune base,peak --output_format all --define drop_caches --define DL-LQC=1 --iterations
   2 --define DL-BIOSinc=Dell-BIOS_Xeon-3w.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define
   DL-VERS=v4.8 --output_format html,pdf,txt --nopower --runmode rate --tune base:peak --size refrate intrate
   --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0
```

(Continued on next page)

Platform Notes (Continued)

```
   --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.0

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) w9-3495X
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 143
    stepping        : 8
    microcode       : 0x2b0004b1
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs
    cpu cores       : 56
    siblings        : 112
    1 physical ids (chips)
    112 processors (hardware threads)
    physical id 0: core ids 0-55
    physical id 0: apicids 0-111
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.2:
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   52 bits physical, 57 bits virtual
  Byte Order:                      Little Endian
  CPU(s):                          112
  On-line CPU(s) list:             0-111
  Vendor ID:                       GenuineIntel
  Model name:                      Intel(R) Xeon(R) w9-3495X
  CPU family:                      6
  Model:                           143
  Thread(s) per core:              2
  Core(s) per socket:              56
  Socket(s):                       1
  Stepping:                        8
  CPU max MHz:                     4800.0000
  CPU min MHz:                     800.0000
  BogoMIPS:                        3792.00
  Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                   clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                   lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                   nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                   ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                                   sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                                   lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                                   invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
                                   tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle
                                   avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                                   avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                                   xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                   cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                                   arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku
                                   ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                                   tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                                   enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
```

(Continued on next page)

Platform Notes (Continued)

```
                                amx_tile flush_l1d arch_capabilities
  Virtualization:               VT-x
  L1d cache:                    2.6 MiB (56 instances)
  L1i cache:                    1.8 MiB (56 instances)
  L2 cache:                     112 MiB (56 instances)
  L3 cache:                     105 MiB (1 instance)
  NUMA node(s):                 1
  NUMA node0 CPU(s):            0-111
  Vulnerability Itlb multihit:  Not affected
  Vulnerability L1tf:           Not affected
  Vulnerability Mds:            Not affected
  Vulnerability Meltdown:       Not affected
  Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
  Vulnerability Spectre v1:     Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:     Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
  Vulnerability Srbds:          Not affected
  Vulnerability Tsx async abort: Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL   SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     2.6M   12 Data            1     64        1             64
     L1i       32K     1.8M    8 Instruction     1     64        1             64
     L2         2M     112M   16 Unified         2   2048        1             64
     L3       105M     105M   15 Unified         3 114688        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0-111
  node 0 size: 1030262 MB
  node 0 free: 1018684 MB
  node distances:
  node   0
    0:  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1054988880 kB

------------------------------------------------------------
10. who -r
  run-level 5 Oct 16 10:36

------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
  Default Target  Status
  graphical       running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE           UNIT FILES
  enabled         YaST2-Firstboot YaST2-Second-Stage apparmor auditd bluetooth cron display-manager
                  firewalld getty@ haveged irqbalance iscsi issue-generator kbdsettings kdump kdump-early
                  klog lvm2-monitor nscd nvmefc-boot-connections postfix purge-kernels rollback rsyslog
                  smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
  enabled-runtime systemd-remount-fs
  disabled        accounts-daemon appstream-sync-cache autofs autoyast-initscripts blk-availability
                  bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups
                  cups-browsed debug-shell ebtables exchange-bmc-os-info gpm grub2-once haveged-switch-root
```

(Continued on next page)

Platform Notes (Continued)

```
                  hwloc-dump-hwdata ipmi ipmievd iscsi-init iscsid iscsiuio issue-add-ssh-keys kexec-load
                  lunmask man-db-create multipathd nfs nfs-blkmap nmb nvmf-autoconnect ostree-remount rdisc
                  rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd
                  snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-network-generator
                  systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2 upower
  indirect        wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
  root=UUID=ef56451f-df00-4165-8d14-fdc6d29bd4a6
  splash=silent
  resume=/dev/disk/by-uuid/f3054370-acfc-47f1-9507-513a8eda8ff6
  mitigations=auto
  quiet
  security=apparmor
  crashkernel=305M,high
  crashkernel=72M,low

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 0:
    current policy: frequency should be within 800 MHz and 4.60 GHz.
                    The governor "powersave" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing               0
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
```

(Continued on next page)

Platform Notes (Continued)

```
   alloc_sleep_millisecs   60000
   defrag                      1
   max_ptes_none             511
   max_ptes_shared           256
   max_ptes_swap              64
   pages_to_scan            4096
   scan_sleep_millisecs    10000

 ------------------------------------------------------------
 18. OS release
   From /etc/*-release /etc/*-version
   os-release SUSE Linux Enterprise Server 15 SP4

 ------------------------------------------------------------
 19. Disk information
 SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.0
   Filesystem     Type   Size  Used Avail Use% Mounted on
   tmpfs          tmpfs   80G  4.2G   76G   6% /mnt/ramdisk

 ------------------------------------------------------------
 20. /sys/devices/virtual/dmi/id
     Vendor:         Dell Inc.
     Product:        Precision 7960 Tower
     Product Family: Precision
     Serial:         72LYTX3

 ------------------------------------------------------------
 21. dmidecode
   Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     16x 00AD00B300AD HMCG94AEBRA123N 64 GB 2 rank 4800, configured at 4400


 ------------------------------------------------------------
 22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Dell Inc.
    BIOS Version:      1.1.10
    BIOS Date:         07/27/2023
    BIOS Revision:     1.1
```

Compiler Version Notes

```
==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
```

(Continued on next page)

Compiler Version Notes (Continued)

```
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
------------------------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
        | 541.leela_r(base, peak)
------------------------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64

(Continued on next page)

Base Portability Flags (Continued)

523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

(Continued on next page)

Peak Optimization Flags (Continued)

520.omnetpp_r: `basepeak = yes`

523.xalancbmk_r: `basepeak = yes`

531.deepsjeng_r: `basepeak = yes`

541.leela_r: `basepeak = yes`

Fortran benchmarks:

548.exchange2_r: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64_revB.2023-10-11.html`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64_revB.2023-10-11.xml`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml`

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-10-16 04:46:42-0400.
Report generated on 2024-01-29 18:14:05 by CPU2017 PS formatter v6716.
Originally published on 2023-11-07.