



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2550 M7, Intel Xeon Silver 4416+, 2.00GHz

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

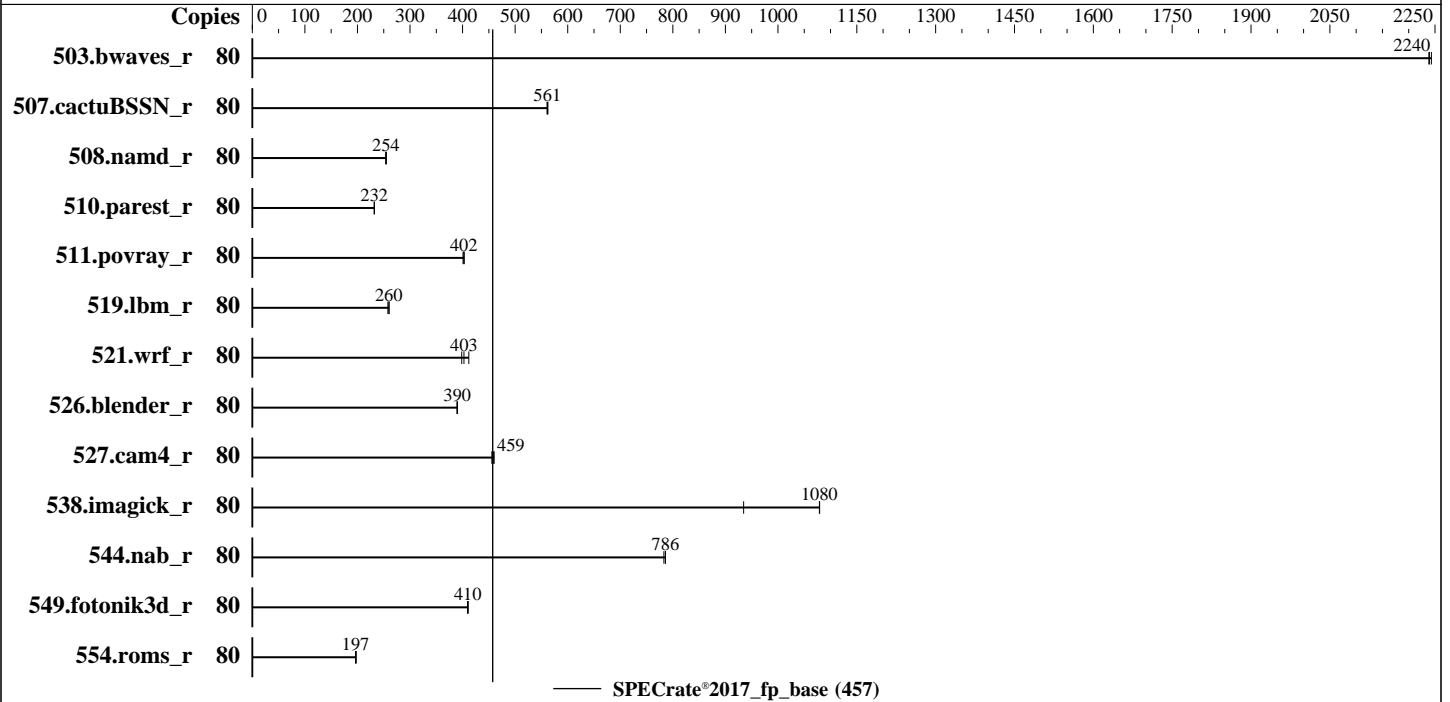
Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: May-2023

Hardware Availability: May-2023

Software Availability: Dec-2022



### Hardware

CPU Name: Intel Xeon Silver 4416+  
 Max MHz: 3900  
 Nominal: 2000  
 Enabled: 40 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 37.5 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R, running at 4000)  
 Storage: 1 x SATA M.2 SSD, 480GB  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP4 5.14.21-150400.24.33-default  
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Fujitsu BIOS Version V1.0.0.0 R1.10.0 for D3988-A1x. Released May-2023 tested as V1.0.0.0 R0.30.0 for D3988-A1x Feb-2023  
 File System: btrfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2550 M7, Intel Xeon Silver 4416+, 2.00GHz

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19  
Test Sponsor: Fujitsu  
Tested by: Fujitsu

Test Date: May-2023  
Hardware Availability: May-2023  
Software Availability: Dec-2022

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	80	<b>358</b>	<b>2240</b>	358	2240	358	2240							
507.cactuBSSN_r	80	180	562	181	561	<b>180</b>	<b>561</b>							
508.namd_r	80	<b>299</b>	<b>254</b>	299	254	298	255							
510.parest_r	80	901	232	<b>901</b>	<b>232</b>	902	232							
511.povray_r	80	<b>464</b>	<b>402</b>	463	403	466	401							
519.lbm_r	80	<b>324</b>	<b>260</b>	327	258	324	260							
521.wrf_r	80	450	398	435	412	<b>445</b>	<b>403</b>							
526.blender_r	80	313	389	<b>312</b>	<b>390</b>	312	390							
527.cam4_r	80	304	460	<b>305</b>	<b>459</b>	307	456							
538.imagick_r	80	<b>184</b>	<b>1080</b>	184	1080	213	935							
544.nab_r	80	<b>171</b>	<b>786</b>	171	786	172	783							
549.fotonik3d_r	80	759	411	761	409	<b>760</b>	<b>410</b>							
554.roms_r	80	643	198	647	197	<b>646</b>	<b>197</b>							

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/Benchmark/speccpu/lib/intel64:/home/Benchmark/speccpu/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2550 M7, Intel Xeon Silver 4416+, 2.00GHz

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test Date:** May-2023  
**Hardware Availability:** May-2023  
**Software Availability:** Dec-2022

### General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS configuration:  
Package C State limit = C0  
CPU Performance Boost = Aggressive  
SNC (Sub NUMA) = Enable SNC2

Sysinfo program /home/Benchmark/speccpu/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Mon May 15 12:47:07 2023

SUT (System Under Test) info as seen by some common utilities.

#### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a  
Linux localhost 5.14.21-150400.24.33-default #1 SMP PREEMPT\_DYNAMIC Fri Nov 4 13:55:06 UTC 2022 (76cfe60)  
x86\_64 x86\_64 x86\_64 GNU/Linux

2. w  
12:47:07 up 2 min, 1 user, load average: 2.31, 2.13, 0.89  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root tty1 - 12:46 11.00s 3.38s 0.16s -bash

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2550 M7, Intel Xeon Silver 4416+,  
2.00GHz

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test Date:** May-2023  
**Hardware Availability:** May-2023  
**Software Availability:** Dec-2022

## Platform Notes (Continued)

-----  
3. Username

From environment variable \$USER: root

-----  
4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size          (kbytes, -d) unlimited
scheduling priority    (-e) 0
file size              (blocks, -f) unlimited
pending signals        (-i) 4125351
max locked memory      (kbytes, -l) 64
max memory size        (kbytes, -m) unlimited
open files             (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority     (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes     (-u) 4125351
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited
```

-----  
5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=80 -c
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define cores=80 --define physicalfirst --define
invoke_with_interleave --define drop_caches --tune base -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=80 --configfile
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define cores=80 --define physicalfirst --define
invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode rate
--tune base --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/Benchmark/speccpu
```

-----  
6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Silver 4416+
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping      : 8
microcode     : 0x2b000161
bugs          : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores     : 20
siblings      : 40
2 physical ids (chips)
80 processors (hardware threads)
physical id 0: core ids 0-19
physical id 1: core ids 0-19
physical id 0: apicids 0-39
physical id 1: apicids 128-167
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2550 M7, Intel Xeon Silver 4416+, 2.00GHz

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test Date:** May-2023  
**Hardware Availability:** May-2023  
**Software Availability:** Dec-2022

## Platform Notes (Continued)

-----  
7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Address sizes:        46 bits physical, 57 bits virtual
Byte Order:           Little Endian
CPU(s):               80
On-line CPU(s) list:  0-79
Vendor ID:            GenuineIntel
Model name:           Intel(R) Xeon(R) Silver 4416+
CPU family:           6
Model:                143
Thread(s) per core:   2
Core(s) per socket:   20
Socket(s):            2
Stepping:             8
CPU max MHz:          3900.0000
CPU min MHz:          800.0000
BogoMIPS:             4000.00
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                    clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                    nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                    ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                    sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                    lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                    invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
                    tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle
                    avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                    avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                    xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                    cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                    arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku
                    ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                    tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                    enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
                    amx_tile flush_lld arch_capabilities

```

```

Virtualization:       VT-x
L1d cache:           1.9 MiB (40 instances)
L1i cache:           1.3 MiB (40 instances)
L2 cache:            80 MiB (40 instances)
L3 cache:            75 MiB (2 instances)
NUMA node(s):        4
NUMA node0 CPU(s):   0-9,40-49
NUMA node1 CPU(s):   10-19,50-59
NUMA node2 CPU(s):   20-29,60-69
NUMA node3 CPU(s):   30-39,70-79
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf:   Not affected
Vulnerability Mds:    Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW
                    sequence

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2550 M7, Intel Xeon Silver 4416+, 2.00GHz

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test Date:** May-2023  
**Hardware Availability:** May-2023  
**Software Availability:** Dec-2022

### Platform Notes (Continued)

Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.9M	12	Data	1	64	1	64
L1i	32K	1.3M	8	Instruction	1	64	1	64
L2	2M	80M	16	Unified	2	2048	1	64
L3	37.5M	75M	15	Unified	3	40960	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-9,40-49
node 0 size: 257589 MB
node 0 free: 256938 MB
node 1 cpus: 10-19,50-59
node 1 size: 258042 MB
node 1 free: 257433 MB
node 2 cpus: 20-29,60-69
node 2 size: 258042 MB
node 2 free: 257442 MB
node 3 cpus: 30-39,70-79
node 3 size: 257688 MB
node 3 free: 257084 MB
node distances:
node  0  1  2  3
0:  10 12 21 21
1:  12 10 21 21
2:  21 21 10 12
3:  21 21 12 10

```

9. /proc/meminfo

MemTotal: 1056116208 kB

10. who -r

run-level 3 May 15 12:45

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

Default Target	Status
multi-user	degraded

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
* sep5.service	loaded	failed	failed	systemd script to load sep5 driver at boot time

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor auditd bluetooth cron display-manager getty@ haveged irqbalance iscsi issue-generator kbdsettings kdump kdump-early klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog sep5 smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant
enabled-runtime	systemd-remount-fs
disabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2550 M7, Intel Xeon Silver 4416+,  
2.00GHz

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test Date:** May-2023  
**Hardware Availability:** May-2023  
**Software Availability:** Dec-2022

### Platform Notes (Continued)

```

appstream-sync-cache autofs autoyast-initscripts blk-availability bluetooth-mesh
boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed
debug-shell dmraid-activation dnsmasq ebttables exchange-bmc-os-info firewallld gpm
grub2-once haveged-switch-root ipmi ipmievdev iscsi-init iscsid iscsiuiio issue-add-ssh-keys
kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nm-cloud-setup nmb openvpn@
ostree-remount pppoe pppoe-server rdisc rpcbind rpmconfigcheck rsyncd rtkit-daemon
serial-getty@ smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd
systemd-boot-check-no-failures systemd-network-generator systemd-sysext
systemd-time-wait-sync systemd-timesyncd udisks2 upower wpa_supplicant@
indirect
pcscd saned@ wickedd

```

```

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.24.33-default
root=UUID=d977c1aa-2de5-45da-b4b1-9732e87ed311
splash=silent
mitigations=auto
quiet
security=apparmor
crashkernel=325M,high
crashkernel=72M,low

```

```

-----
15. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 3.90 GHz.
                  The governor "powersave" may decide which speed to use
                  within this range.
boost state support:
  Supported: yes
  Active: yes

```

```

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2550 M7, Intel Xeon Silver 4416+, 2.00GHz

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: May-2023

Hardware Availability: May-2023

Software Availability: Dec-2022

## Platform Notes (Continued)

```
-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000
-----
```

```
-----
19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4
-----
```

```
-----
20. Disk information
SPEC is set to: /home/Benchmark/speccpu
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 btrfs 445G 72G 373G 17% /home
-----
```

```
-----
21. /sys/devices/virtual/dmi/id
Vendor: FUJITSU
Product: PRIMERGY CX2550 M7
Product Family: SERVER
Serial: BBBB000001
-----
```

```
-----
22. dmidecode
Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
10x Samsung M321R8GA0BB0-CQKDG 64 GB 2 rank 4800, configured at 4000
2x Samsung M321R8GA0BB0-CQKEG 64 GB 2 rank 4800, configured at 4000
4x Samsung M321R8GA0BB0-CQKVG 64 GB 2 rank 4800, configured at 4000
-----
```

```
-----
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: FUJITSU
BIOS Version: V1.0.0.0 R0.30.0 for D3988-Alx
BIOS Date: 02/11/2023
BIOS Revision: 0.30
Firmware Revision: 2.20
-----
```

## Compiler Version Notes

```
-----
C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
-----
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----
```

(Continued on next page)





# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2550 M7, Intel Xeon Silver 4416+,  
2.00GHz

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test Date:** May-2023  
**Hardware Availability:** May-2023  
**Software Availability:** Dec-2022

### Compiler Version Notes (Continued)

=====  
C++ | 508.namd\_r(base) 510.parest\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
C++, C | 511.povray\_r(base) 526.blender\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
Fortran | 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
Fortran, C | 521.wrf\_r(base) 527.cam4\_r(base)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

### Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifx

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2550 M7, Intel Xeon Silver 4416+,  
2.00GHz

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: May-2023

Hardware Availability: May-2023

Software Availability: Dec-2022

## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
 507.cactuBSSN\_r: -DSPEC\_LP64  
 508.namd\_r: -DSPEC\_LP64  
 510.parest\_r: -DSPEC\_LP64  
 511.povray\_r: -DSPEC\_LP64  
 519.lbm\_r: -DSPEC\_LP64  
 521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
 526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
 527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
 538.imagick\_r: -DSPEC\_LP64  
 544.nab\_r: -DSPEC\_LP64  
 549.fotonik3d\_r: -DSPEC\_LP64  
 554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsaphirerapids -Ofast -ffast-math  
 -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
 -Wno-implicit-int -mprefer-vector-width=512 -ljemalloc  
 -L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsaphirerapids -Ofast  
 -ffast-math -flto -mfpmath=sse -funroll-loops  
 -qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc  
 -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsaphirerapids -Ofast -ffast-math -flto  
 -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2550 M7, Intel Xeon Silver 4416+,  
2.00GHz

SPECrate®2017\_fp\_base = 457

SPECrate®2017\_fp\_peak = Not Run

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test Date:** May-2023  
**Hardware Availability:** May-2023  
**Software Availability:** Dec-2022

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapfirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapfirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapfirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-SPR-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-SPR-RevB.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2023-05-14 23:47:06-0400.

Report generated on 2023-07-19 16:30:47 by CPU2017 PDF formatter v6716.

Originally published on 2023-07-19.