SPEC CPU®2017 Floating Point Speed Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

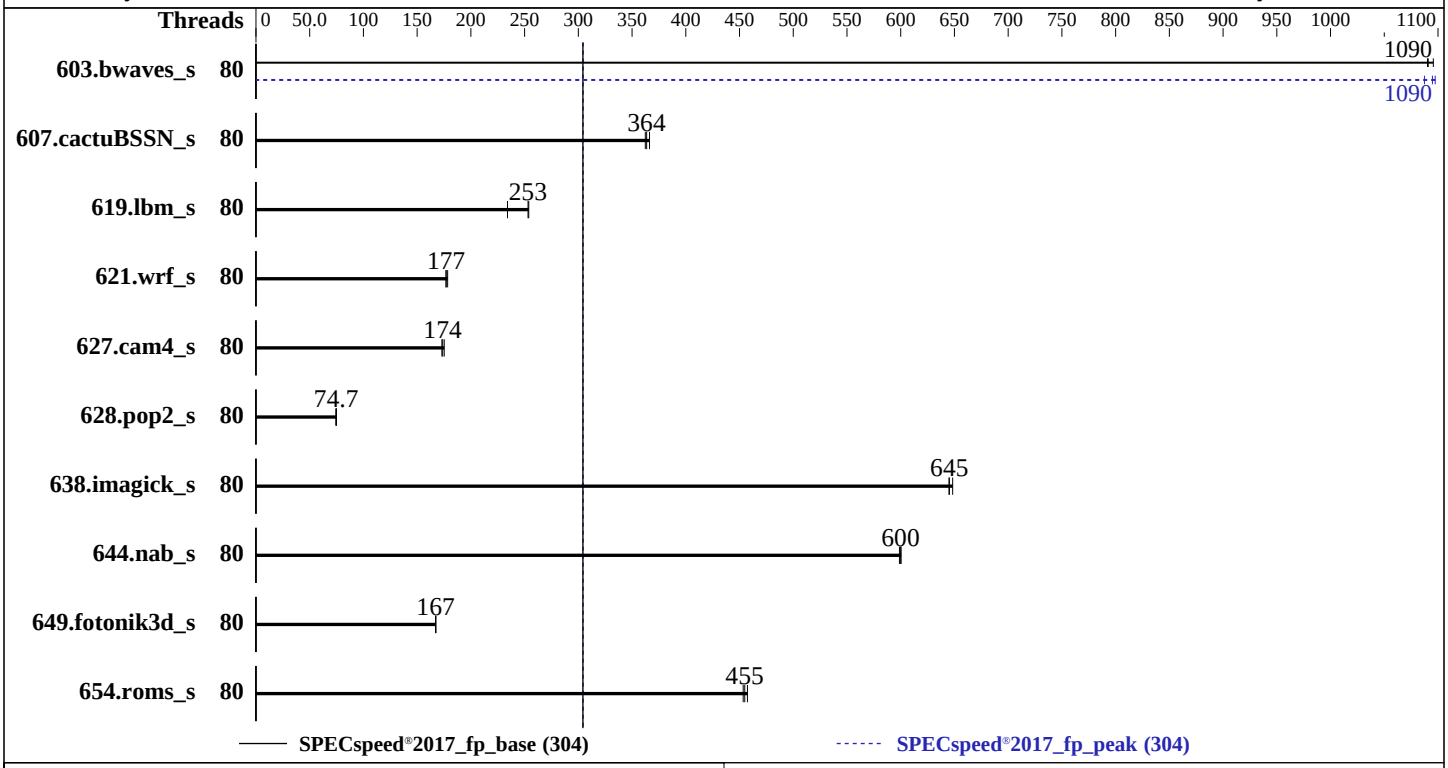
ProLiant ML350 Gen11
(2.00 GHz, Intel Xeon Platinum 8460Y+)

SPECspeed®2017_fp_base = 304

SPECspeed®2017_fp_peak = 304

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Feb-2023
Hardware Availability: Jan-2023
Software Availability: Nov-2022



Hardware

CPU Name:	Intel Xeon Platinum 8460Y+
Max MHz:	3700
Nominal:	2000
Enabled:	80 cores, 2 chips
Orderable:	1, 2 chip(s)
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	105 MB I+D on chip per chip
Other:	None
Memory:	1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)
Storage:	1 x 400 GB SATA SSD
Other:	None

Software

OS:	Red Hat Enterprise Linux release 9.0 (Plow) Kernel 5.14.0-70.13.1.el9_0.x86_64
Compiler:	C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
Parallel:	Yes
Firmware:	HPE BIOS Version v1.22 01/18/2023 released Jan-2023
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	80	**54.1**	**1090**	53.8	1100	54.1	1090	80	**53.9**	**1090**	54.3	1090	53.8	1100
607.cactuBSSN_s	80	46.0	362	**45.9**	**364**	45.5	366	80	46.0	362	**45.9**	**364**	45.5	366
619.lbm_s	80	22.4	234	20.6	254	**20.7**	**253**	80	22.4	234	20.6	254	**20.7**	**253**
621.wrf_s	80	**74.7**	**177**	74.8	177	74.1	178	80	**74.7**	**177**	74.8	177	74.1	178
627.cam4_s	80	**51.0**	**174**	50.6	175	51.2	173	80	**51.0**	**174**	50.6	175	51.2	173
628.pop2_s	80	**159**	**74.7**	159	74.9	160	74.4	80	**159**	**74.7**	159	74.9	160	74.4
638.imagick_s	80	**22.4**	**645**	22.4	645	22.3	648	80	**22.4**	**645**	22.4	645	22.3	648
644.nab_s	80	29.1	600	**29.1**	**600**	29.2	599	80	29.1	600	**29.1**	**600**	29.2	599
649.fotonik3d_s	80	54.5	167	54.5	167	**54.5**	**167**	80	54.5	167	54.5	167	**54.5**	**167**
654.roms_s	80	34.7	453	**34.6**	**455**	34.4	457	80	34.7	453	**34.6**	**455**	34.4	457

SPECspeed®2017_fp_base = 304

SPECspeed®2017_fp_peak = 304

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
IRQ balance service was stopped using "systemctl stop irqbalance.service"
tuned-adm profile was set to Throughput-Performance using "tuned-adm profile throughput-performance"
perf-bias for all the CPUs is set using "cpupower set -b 0"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
```

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General Notes (Continued)

```
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
The system ROM used for this result contains Intel microcode version 0x2b000161 for
the Intel Xeon Platinum 8460Y+ processor.
BIOS Configuration:
 Workload Profile set to General Peak Frequency Compute
 Thermal Configuration set to Maximum Cooling
 Intel Hyper-Threading set to Disabled
 Memory Patrol Scrubbing set to Disabled
 Last Level Cache (LLC) Prefetch set to Enabled
 Last Level Cache (LLC) Dead Line Allocation set to Disabled
 Enhanced Processor Performance Profile set to Aggressive
 Dead Block Predictor set to Enabled
 Workload Profile set to Custom
  Intel DMI Link Frequency set to Gen2 Speed
  Adjacent Sector Prefetch set to Disabled
  Minimum Processor Idle Power Package C-State set to No Package State

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost.localdomain Tue Feb 21 10:44:13 2023

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 250 (250-6.el9_0)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
```

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Platform Notes (Continued)

```
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
------------------------------------------------------------

------------------------------------------------------------
 1. uname -a
   Linux localhost.localdomain 5.14.0-70.13.1.el9_0.x86_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86_64
   x86_64 x86_64 GNU/Linux

------------------------------------------------------------
 2. w
    10:44:13 up 1 min,  0 users,  load average: 3.52, 2.27, 0.89
   USER     TTY        LOGIN@   IDLE   JCPU   PCPU WHAT

------------------------------------------------------------
 3. Username
   From environment variable $USER:  root

------------------------------------------------------------
 4. ulimit -a
   real-time non-blocking time  (microseconds, -R) unlimited
   core file size              (blocks, -c) 0
   data seg size               (kbytes, -d) unlimited
   scheduling priority                 (-e) 0
   file size                   (blocks, -f) unlimited
   pending signals                     (-i) 4127201
   max locked memory           (kbytes, -l) 64
   max memory size             (kbytes, -m) unlimited
   open files                          (-n) 1024
   pipe size                (512 bytes, -p) 8
   POSIX message queues         (bytes, -q) 819200
   real-time priority                  (-r) 0
   stack size                  (kbytes, -s) unlimited
   cpu time                   (seconds, -t) unlimited
   max user processes                  (-u) 4127201
   virtual memory              (kbytes, -v) unlimited
   file locks                          (-x) unlimited

------------------------------------------------------------
 5. sysinfo process ancestry
```

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Platform Notes (Continued)

```
 /usr/lib/systemd/systemd --switched-root --system --deserialize 27
 sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
 sshd: root [priv]
 sshd: root@notty
 bash -c cd $SPEC/ && $SPEC/fpspeed.sh
 runcpu --nobuild --action validate --define default-platform-flags -c
  ic2022.1-lin-core-avx512-speed-20220316.cfg --define cores=80 --tune base,peak -o all --define drop_caches
   fpspeed
 runcpu --nobuild --action validate --define default-platform-flags --configfile
  ic2022.1-lin-core-avx512-speed-20220316.cfg --define cores=80 --tune base,peak --output_format all
  --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed fpspeed --nopreenv
  --note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.fpspeed.001.0.log --lognum 001.0
   --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) Platinum 8460Y+
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 143
    stepping        : 6
    microcode       : 0x2b000161
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs
    cpu cores       : 40
    siblings        : 40
    2 physical ids (chips)
    80 processors (hardware threads)
    physical id 0: core ids 0-39
    physical id 1: core ids 0-39
    physical id 0: apicids
   0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62,64,66,68,70,72
    ,74,76,78
    physical id 1: apicids
   128,130,132,134,136,138,140,142,144,146,148,150,152,154,156,158,160,162,164,166,168,170,172,174,176,178,1
    80,182,184,186,188,190,192,194,196,198,200,202,204,206
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
   virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   46 bits physical, 57 bits virtual
```

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Platform Notes (Continued)

```
Byte Order:                      Little Endian
CPU(s):                          80
On-line CPU(s) list:             0-79
Vendor ID:                       GenuineIntel
BIOS Vendor ID:                  Intel(R) Corporation
Model name:                      Intel(R) Xeon(R) Platinum 8460Y+
BIOS Model name:                 Intel(R) Xeon(R) Platinum 8460Y+
CPU family:                      6
Model:                           143
Thread(s) per core:              1
Core(s) per socket:              40
Socket(s):                       2
Stepping:                        6
BogoMIPS:                        4000.00
Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                 lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                 nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                                 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                                 lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                                 invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                                 vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2
                                 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                                 clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                                 xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                                 split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                                 avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                                 avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                                 cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                                 arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities
Virtualization:                  VT-x
L1d cache:                       3.8 MiB (80 instances)
L1i cache:                       2.5 MiB (80 instances)
L2 cache:                        160 MiB (80 instances)
L3 cache:                        210 MiB (2 instances)
NUMA node(s):                    2
NUMA node0 CPU(s):               0-19,40-59
NUMA node1 CPU(s):               20-39,60-79
Vulnerability Itlb multihit:     Not affected
Vulnerability L1tf:              Not affected
Vulnerability Mds:               Not affected
Vulnerability Meltdown:          Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:        Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:             Not affected
```

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Platform Notes (Continued)

```
  Vulnerability Tsx async abort:   Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL   SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     3.8M   12 Data            1     64        1             64
     L1i       32K     2.5M    8 Instruction     1     64        1             64
     L2         2M     160M   16 Unified         2   2048        1             64
     L3       105M     210M   15 Unified         3 114688        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0-19,40-59
  node 0 size: 515763 MB
  node 0 free: 514489 MB
  node 1 cpus: 20-39,60-79
  node 1 size: 516076 MB
  node 1 free: 514759 MB
  node distances:
  node   0   1
    0:  10  20
    1:  20  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1056604412 kB

------------------------------------------------------------
10. who -r
  run-level 3 Feb 21 10:43

------------------------------------------------------------
11. Systemd service manager version: systemd 250 (250-6.el9_0)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond
                   dbus-broker firewalld getty@ irqbalance iscsi iscsi-onboot kdump libstoragemgmt
                   lvm2-monitor mdmonitor microcode multipathd nis-domainname rhsmcertd rpcbind rsyslog
                   selinux-autorelabel-mark sshd sssd systemd-network-generator udisks2 upower virtqemud
  enabled-runtime  systemd-remount-fs
  disabled         blk-availability brltty canberra-system-bootup canberra-system-shutdown
                   canberra-system-shutdown-reboot chrony-wait console-getty cpupower debug-shell dnsmasq
```

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Platform Notes (Continued)

```
                gssproxy httpd httpd@ hwloc-dump-hwdata ipa-custodia iscsid iscsiuio kvm_stat
              libvirt-guests libvirtd man-db-restart-cache-update ndctl-monitor nfs-blkmap nfs-server
             nftables nmb numad pmcd pmfind pmie pmie_farm pmlogger pmlogger_farm pmproxy radiusd rdisc
             rhsm rhsm-facts rpmdb-rebuild saslauthd serial-getty@ smb speech-dispatcherd sshd-keygen@
             systemd-boot-check-no-failures systemd-nspawn@ systemd-pstore systemd-sysext virtnetworkd
                 virtproxyd virtsecretd virtstoraged
  indirect       sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo virtlockd virtlogd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.13.1.el9_0.x86_64
  root=/dev/mapper/rhel-root
  ro
  resume=/dev/mapper/rhel-swap
  rd.lvm.lv=rhel/root
  rd.lvm.lv=rhel/swap

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 0:
    Unable to determine current policy
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0
```

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Platform Notes (Continued)

```
------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release     Red Hat Enterprise Linux 9.0 (Plow)
  redhat-release Red Hat Enterprise Linux release 9.0 (Plow)
  system-release Red Hat Enterprise Linux release 9.0 (Plow)

------------------------------------------------------------
19. Disk information
SPEC is set to: /home/cpu2017
  Filesystem            Type  Size  Used Avail Use% Mounted on
  /dev/mapper/rhel-home xfs   372G  151G  222G  41% /home

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         HPE
    Product:        ProLiant ML350 Gen11
    Product Family: ProLiant
    Serial:         CNX20800P7

------------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    16x Hynix HMCG94MEBRA121N 64 GB 2 rank 4800
```

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Platform Notes (Continued)

```
 ------------------------------------------------------------
 22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       HPE
    BIOS Version:      1.22
    BIOS Date:         01/18/2023
    BIOS Revision:     1.22
    Firmware Revision: 1.20
```

Compiler Version Notes

```
==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
                | 644.nab_s(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
                | 654.roms_s(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
                | 628.pop2_s(base, peak)
```

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Compiler Version Notes (Continued)

```
------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
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Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto

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Base Optimization Flags (Continued)

C benchmarks (continued):
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: `basepeak = yes`

638.imagick_s: `basepeak = yes`

644.nab_s: `basepeak = yes`

Fortran benchmarks:

603.bwaves_s: `-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs -align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

649.fotonik3d_s: `basepeak = yes`

654.roms_s: `basepeak = yes`

Benchmarks using both Fortran and C:

621.wrf_s: `basepeak = yes`

627.cam4_s: `basepeak = yes`

628.pop2_s: `basepeak = yes`

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev1.1.html`
`http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev1.1.xml`
`http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.xml`

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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