# SPEC CPU®2017 Integer Speed Result

## Hewlett Packard Enterprise
(2.90 GHz, Intel Xeon Gold 6326)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 11.5</th>
<th>SPECspeed®2017_int_peak = 11.8</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: HPE</td>
<td>Test Date: Dec-2021</td>
</tr>
<tr>
<td>Hardware Availability: Nov-2021</td>
<td>Software Availability: Dec-2020</td>
</tr>
</tbody>
</table>

### Hardware
- **CPU Name:** Intel Xeon Gold 6326
- **Max MHz:** 3500
- **Nominal:** 2900
- **Enabled:** 32 cores, 2 chips
- **Orderable:** 1, 2 chip(s)
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 24 MB I+D on chip per chip
- **Other:** None
- **Memory:** 2 TB (32 x 64 GB 2Rx4 PC4-3200AA-R)
- **Storage:** 1 x 800 GB SAS SSD, RAID 0
- **Other:** None

### Software
- **OS:** Red Hat Enterprise Linux 8.3 (Ootpa)
  - Kernel 4.18.0-240.el8.x86_64
- **Compiler:**
  - C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
  - Compiler Build 20201113 for Linux;
  - Fortran: Version 2021.1 of Intel Fortran Compiler
  - Classic Build 20201112 for Linux;
  - C/C++: Version 2021.1 of Intel C/C++ Compiler
  - Classic Build 20201112 for Linux
- **Parallel:** Yes
- **Firmware:** HPE BIOS Version I44 v1.54 11/03/2021 released
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>32</td>
<td>8.24</td>
<td>9.15</td>
</tr>
<tr>
<td>gcc</td>
<td>32</td>
<td>10.7</td>
<td>11.0</td>
</tr>
<tr>
<td>mcf</td>
<td>32</td>
<td>19.1</td>
<td>20.0</td>
</tr>
<tr>
<td>omnetpp</td>
<td>32</td>
<td>10.0</td>
<td>11.0</td>
</tr>
<tr>
<td>xalancbmk</td>
<td>32</td>
<td>13.3</td>
<td>14.0</td>
</tr>
<tr>
<td>x264</td>
<td>32</td>
<td>16.7</td>
<td>17.4</td>
</tr>
<tr>
<td>deepsjeng</td>
<td>32</td>
<td>5.93</td>
<td>6.00</td>
</tr>
<tr>
<td>leela</td>
<td>32</td>
<td>4.85</td>
<td>5.00</td>
</tr>
<tr>
<td>exchange2</td>
<td>32</td>
<td>19.4</td>
<td>19.5</td>
</tr>
<tr>
<td>xz</td>
<td>32</td>
<td>22.1</td>
<td>22.2</td>
</tr>
</tbody>
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Standard Performance Evaluation Corporation (info@spec.org)  https://www.spec.org/
SPEC CPU®2017 Integer Speed Result

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Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 480 Gen10 Plus
(2.90 GHz, Intel Xeon Gold 6326)  

SPECspeed®2017_int_base = 11.5  
SPECspeed®2017_int_peak = 11.8

Results Table

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<tr>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>32</td>
<td>248</td>
<td>7.15</td>
<td>248</td>
<td>7.15</td>
<td>247</td>
<td>7.17</td>
<td>32</td>
<td>215</td>
<td>8.24</td>
<td></td>
<td>216</td>
<td>8.22</td>
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<tr>
<td>602.gcc_s</td>
<td>32</td>
<td>373</td>
<td>10.7</td>
<td>377</td>
<td>10.6</td>
<td>372</td>
<td>10.7</td>
<td>32</td>
<td>362</td>
<td>11.0</td>
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<td>361</td>
<td>11.0</td>
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<tr>
<td>605.mcf_s</td>
<td>32</td>
<td>245</td>
<td>19.3</td>
<td>241</td>
<td>19.6</td>
<td>242</td>
<td>19.5</td>
<td>32</td>
<td>245</td>
<td>19.3</td>
<td></td>
<td>241</td>
<td>19.6</td>
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<tr>
<td>620.omnetpp_s</td>
<td>32</td>
<td>162</td>
<td>10.0</td>
<td>162</td>
<td>10.1</td>
<td>164</td>
<td>9.94</td>
<td>32</td>
<td>162</td>
<td>10.0</td>
<td></td>
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<td>10.1</td>
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<tr>
<td>623.xalancmk_s</td>
<td>32</td>
<td>107</td>
<td>13.3</td>
<td>106</td>
<td>13.4</td>
<td>106</td>
<td>13.3</td>
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<td>107</td>
<td>13.3</td>
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<td>106</td>
<td>13.4</td>
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<tr>
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<td>16.7</td>
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<td>16.7</td>
<td>106</td>
<td>16.7</td>
<td>32</td>
<td>101</td>
<td>17.4</td>
<td></td>
<td>102</td>
<td>17.3</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>32</td>
<td>242</td>
<td>5.92</td>
<td>242</td>
<td>5.93</td>
<td>242</td>
<td>5.93</td>
<td>32</td>
<td>242</td>
<td>5.92</td>
<td></td>
<td>242</td>
<td>5.93</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td>153</td>
<td>19.2</td>
<td>152</td>
<td>19.4</td>
<td>152</td>
<td>19.4</td>
<td>32</td>
<td>153</td>
<td>19.2</td>
<td></td>
<td>152</td>
<td>19.4</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>32</td>
<td>280</td>
<td>22.1</td>
<td>280</td>
<td>22.1</td>
<td>281</td>
<td>22.0</td>
<td>32</td>
<td>280</td>
<td>22.1</td>
<td></td>
<td>280</td>
<td>22.1</td>
</tr>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
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Synergy 480 Gen10 Plus  
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<td>Nov-2021</td>
</tr>
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</table>

### General Notes (Continued)

- Sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
- Submitted by: "Bucek, James" <james.bucek@hpe.com>
- Submitted: Wed Jan 12 10:02:51 EST 2022
- Submission: cpu2017-20220103-30711.sub

### Platform Notes

**BIOS Configuration:**
- Workload Profile set to General Peak Frequency Compute
- Intel Hyper-Threading set to Disabled
- Thermal Configuration set to Maximum Cooling
- Memory Patrol Scrubbing set to Disabled
- Advanced Memory Protection set to Advanced ECC
- Last Level Cache (LLC) Prefetch set to Enabled
- Last Level Cache (LLC) Dead Line Allocation set to Disabled
- Enhanced Processor Performance set to Enabled
- Workload Profile set to Custom
  - Energy/Performance Bias set to Balanced Power
  - DCU Stream Prefetcher set to Disabled
  - Adjacent Sector Prefetch set to Disabled
  - Minimum Processor Idle Power Package C-State set to No Package State
  - Numa Group Size Optimization set to Flat

**Sysinfo program** /home/cpu2017/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca6c64d  
running on localhost.localdomain Thu Dec 16 17:57:17 2021

**SUT (System Under Test) info as seen by some common utilities.**

For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

**From /proc/cpuinfo**

- model name: Intel(R) Xeon(R) Gold 6326 CPU @ 2.90GHz  
  - 2 "physical id"s (chips)  
  - 32 "processors"

**cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)**

- cpu cores: 16  
- siblings: 16  
- physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
  - physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

**From lscpu from util-linux 2.32.1:**

- Architecture: x86_64  
- CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
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CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6326 CPU @ 2.90GHz
Stepping: 6
CPU MHz: 2203.557
BogoMIPS: 5800.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 24576K
NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma cx16
xtr pdcm pcid dca ssse4_1 ssse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl64 rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 invpcid_single ssbd
mba ibpb stibp ibrs enhanced tpr_shadow vmni flexpriority ept vpid ept_ad
fgsbase tsc_adjust bml1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512_single ssbd
mib ibs ibp stibp ibrs enhanced tpr_shadow vmni flexpriority ept vpid ept_ad
fsgsbase tsc_adjust bml1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw
avx512vl xsaveopt xsave xasave xsave xasave qemu l1c qem_occup l1c qem_mbb_total
qem_mbb_local split_lock_detect wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku
ospe avx512_vbmi2 gfini vaes vpmcmulqdq avx512_vnni avx512_bitsal tme
avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 1002319 MB
node 0 free: 1031123 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 1 size: 1003525 MB
node 1 free: 1031384 MB

(Continued on next page)
Platform Notes (Continued)

node distances:

node  0  1
0:  10  20
1:  20  10

From /proc/meminfo
MemTotal:       2113495312 kB
HugePages_Total:   0
Hugepagesize:       2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prct1 and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

(Continued on next page)
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CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

run-level 3 Dec 16 17:55
SPEC is set to: /home/cpu2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/mapper/rhel-home</td>
<td>xfs</td>
<td>670G</td>
<td>112G</td>
<td>559G</td>
<td>17%</td>
<td>/home</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id
Vendor: HPE
Product: Synergy 480 Gen10 Plus
Product Family: Synergy
Serial: CN70330Q5F

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
32x Micron 36ASF8G72PZ-3G2B2 64 GB 2 rank 3200

BIOS:
BIOS Vendor: HPE
BIOS Version: I44
BIOS Date: 11/03/2021
BIOS Revision: 1.54
Firmware Revision: 2.50

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C | 600.perlbench_s(peak) |
|-----------------------------------|
| Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
|-----------------------------------|

==============================================================================
| C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak) |
|-----------------------------------|
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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**Compiler Version Notes (Continued)**

<table>
<thead>
<tr>
<th>Base Compiler Invocation</th>
</tr>
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</table>

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifort
Hewlett Packard Enterprise
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SPECspeed\textsuperscript{®}2017\textsubscript{int}\_peak = 11.8
SPECspeed\textsuperscript{®}2017\textsubscript{int}\_base = 11.5

Base Portability Flags

- 600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64
- 602.gcc\_s: -DSPEC\_LP64
- 605.mcf\_s: -DSPEC\_LP64
- 620.omnetpp\_s: -DSPEC\_LP64
- 623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX
- 625.x264\_s: -DSPEC\_LP64
- 631.deepsjeng\_s: -DSPEC\_LP64
- 641.leela\_s: -DSPEC\_LP64
- 648.exchange2\_s: -DSPEC\_LP64
- 657.xz\_s: -DSPEC\_LP64

Base Optimization Flags

C benchmarks:
- -DSPEC\_OPENMP -std=c11 -m64 -fioopenmp -Wl,-z,muldefs -xCORE-AVX512
- -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
- -qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
- -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
- -DSPEC\_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
  -lqkmalloc

Fortran benchmarks:
- -m64 -xcORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
- -nostandard-realloc-lhs -align array32byte -auto
- -mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks (except as noted below):
  icx
  600.perlbench\_s: cc

C++ benchmarks:
  icpx

(Continued on next page)
Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z, muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z, muldefs -fprofile-generator(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z, muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

(Continued on next page)
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Software Availability: Dec-2020

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Peak Optimization Flags (Continued)

641.leela_s: basepeak = yes

Fortran benchmarks:
648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml  
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revG.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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