## New H3C Technologies Co., Ltd.

### H3C UniServer R4300 G5 (Intel Xeon Gold 6338T)

**SPECrater®2017_fp_base = 341**

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_peak = Not Run</th>
</tr>
</thead>
</table>

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Dec-2021</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Jun-2021</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Dec-2020</td>
</tr>
</tbody>
</table>

### CPU2017 License: 9066

**Test Sponsor:** New H3C Technologies Co., Ltd.

**Tested by:** New H3C Technologies Co., Ltd.

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Gold 6338T</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz:</td>
<td>3400</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2100</td>
</tr>
<tr>
<td>Enabled:</td>
<td>48 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1,2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>36 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

| Memory: | 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R) |
| Storage: | 1 x 480GB SATA SSD |
| Other: | None |

### Software

| OS: | Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86_64 |
| Compiler: | C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; |
| Parallel: | No |
| Firmware: | Version 5.39 released Nov-2021 BIOS |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | Not Applicable |
| Other: | jemalloc memory allocator V5.0.1 |
| Power Management: | BIOS and OS set to prefer performance at the cost of additional power usage. |

### Performance Results

<table>
<thead>
<tr>
<th>benchmark</th>
<th>copies</th>
<th>copies</th>
<th>copies</th>
<th>copies</th>
<th>copies</th>
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</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>96</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>96</td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>508.namd_r</td>
<td>96</td>
<td>243</td>
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<td></td>
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<tr>
<td>510.parest_r</td>
<td>96</td>
<td>191</td>
<td>360</td>
<td></td>
<td></td>
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<tr>
<td>511.povray_r</td>
<td>96</td>
<td>257</td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>519.lbm_r</td>
<td>96</td>
<td>320</td>
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<td></td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>96</td>
<td>339</td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>526.blender_r</td>
<td>96</td>
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<td></td>
<td></td>
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<tr>
<td>527.cam4_r</td>
<td>96</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>96</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>96</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>96</td>
<td>222</td>
<td></td>
<td></td>
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</tr>
<tr>
<td>554.roms_r</td>
<td>96</td>
<td></td>
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<td></td>
</tr>
</tbody>
</table>

**SPECrate®2017_fp_base (341)**
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>96</td>
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<td>722</td>
<td>1331</td>
<td>723</td>
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<td>507.cactuBSSN_r</td>
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<td>260</td>
<td>467</td>
<td>260</td>
<td>467</td>
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<tr>
<td>508.namd_r</td>
<td>96</td>
<td>376</td>
<td>242</td>
<td>375</td>
<td>243</td>
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<tr>
<td>510.parest_r</td>
<td>96</td>
<td>1312</td>
<td>191</td>
<td>1318</td>
<td>191</td>
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<tr>
<td>511.povray_r</td>
<td>96</td>
<td>622</td>
<td>360</td>
<td>623</td>
<td>360</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>96</td>
<td>393</td>
<td>257</td>
<td>393</td>
<td>257</td>
</tr>
<tr>
<td>521.wrf_r</td>
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<td>690</td>
<td>312</td>
<td>671</td>
<td>321</td>
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<tr>
<td>526.blender_r</td>
<td>96</td>
<td>431</td>
<td>339</td>
<td>432</td>
<td>338</td>
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<tr>
<td>527.cam4_r</td>
<td>96</td>
<td>491</td>
<td>342</td>
<td>488</td>
<td>344</td>
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<tr>
<td>538.imagick_r</td>
<td>96</td>
<td>283</td>
<td>843</td>
<td>277</td>
<td>863</td>
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<tr>
<td>544.nab_r</td>
<td>96</td>
<td>296</td>
<td>547</td>
<td>293</td>
<td>551</td>
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<tr>
<td>549.fotonik3d_r</td>
<td>96</td>
<td>1682</td>
<td>222</td>
<td>1684</td>
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<td>554.roms_r</td>
<td>96</td>
<td>1026</td>
<td>149</td>
<td>1024</td>
<td>149</td>
</tr>
</tbody>
</table>

**SPECrate®2017_fp_base =** 341  
**SPECrate®2017_fp_peak =** Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

- LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
- MALLOC_CONF = "retain:true"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

(Continued on next page)
General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Set SNC (Sub NUMA) to Enabled
Set Power Performance Tuning to BIOS Controls EPB
Set Energy Performance BIAS to Performance
Set XPT Prefetch to Enabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e1acaf64d
running on localhost.localdomain Thu Dec 9 20:42:42 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6338T CPU @ 2.10GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95

(Continued on next page)
New H3C Technologies Co., Ltd.

H3C UniServer R4300 G5 (Intel Xeon Gold 6338T)

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

SPEC®2017_fp_base = 341
SPEC®2017_fp_peak = Not Run

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Dec-2021
Tested by: New H3C Technologies Co., Ltd.
Hardware Availability: Jun-2021
Software Availability: Dec-2020

Platform Notes (Continued)

Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6338T CPU @ 2.10GHz
Stepping: 6
CPU MHz: 2700.000
CPU max MHz: 3400.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node0 CPU(s): 0-11,48-59
NUMA node1 CPU(s): 12-23,60-71
NUMA node2 CPU(s): 24-35,72-83
NUMA node3 CPU(s): 36-47,84-95
Flags: fpu vme vmx Vân pse msr msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movpd popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs ibpb ibrs_enhanced tpr_shadow vmptrld vmptrst vmx flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cpqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsaves xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbms_total cqm_mbms_local wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vmbi umip pku ospke avx512_vmbi2 gfnl vaes vpcmullqdv avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_lld
arch_capabilities

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 48 49 50 51 52 53 54 55 56 57 58 59
node 0 size: 128352 MB
node 0 free: 121059 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 60 61 62 63 64 65 66 67 68 69 70 71

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R4300 G5 (Intel Xeon Gold 6338T)

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base</th>
<th>341</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Test Date:** Dec-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Dec-2020

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**Platform Notes (Continued)**

- node 1 size: 129018 MB  
- node 1 free: 123671 MB  
- node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 72 73 74 75 76 77 78 79 80 81 82 83  
- node 2 size: 129018 MB  
- node 2 free: 124211 MB  
- node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 84 85 86 87 88 89 90 91 92 93 94 95  
- node 3 size: 128988 MB  
- node 3 free: 124175 MB  
- node distances:  
  - node 0 size: 129018 MB  
  - node 0 free: 123671 MB  
  - node 1 size: 129018 MB  
  - node 1 free: 124211 MB  
  - node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 72 73 74 75 76 77 78 79 80 81 82 83  
  - node 2 size: 129018 MB  
  - node 2 free: 124211 MB  
  - node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 84 85 86 87 88 89 90 91 92 93 94 95  
  - node 3 size: 128988 MB  
  - node 3 free: 124175 MB  
  - node distances:  
    - node 0 sizes: 129018 MB  
    - node 0 free: 123671 MB  
    - node 1 sizes: 129018 MB  
    - node 1 free: 124211 MB  

- From /proc/meminfo  
  - MemTotal: 527747836 kB  
  - HugePages_Total: 0  
  - Hugepagesize: 2048 kB  

- /sbin/tuned-adm active  
  - Current active profile: throughput-performance  

- /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance  

- From /etc/*release* /etc/*version*  
  - os-release:  
    - NAME="Red Hat Enterprise Linux"  
    - VERSION="8.2 (Ootpa)"  
    - ID="rhel"  
    - ID_LIKE="fedora"  
    - VERSION_ID="8.2"  
    - PLATFORM_ID="platform:el8"  
    - PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"  
    - ANSI_COLOR="0;31"  
  - redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)  
  - system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)  
  - system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga  

- uname -a:  
  - Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020  
  - x86_64 x86_64 x86_64 GNU/Linux  

- Kernel self-reported vulnerability status:  
  - CVE-2018-12207 (iTLB Multihit): Not affected  

(Continued on next page)
New H3C Technologies Co., Ltd.
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Platform Notes (Continued)

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Dec 9 16:23
SPEC is set to: /home/speccpu

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 392G 130G 262G 34% /home

From /sys/devices/virtual/dmi/id
Product Family: Rack

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
  1x <BAD INDEX> <BAD INDEX> 32 GB 2 rank 3200
  15x Hynix HMA84GR7DJR4N-XN 32 GB 2 rank 3200
  16x NO DIMM NO DIMM

BIOS:
  BIOS Vendor: American Megatrends International, LLC.
  BIOS Version: 5.39
  BIOS Date: 11/17/2021
  BIOS Revision: 5.22

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C               | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
==============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,

(Continued on next page)
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Tested by: New H3C Technologies Co., Ltd.

### Compiler Version Notes (Continued)

Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
C++                                | 508.namd_r(base) 510.parest_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
C++, C                              | 511.povray_r(base) 526.blender_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
C++, C, Fortran                     | 507.cactuBSSN_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on 
Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
Fortran                             | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on 
Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
Fortran, C                          | 521.wrf_r(base) 527.cam4_r(base)
(Continued on next page)
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SPEC®2017 fp_base = 341
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Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactusBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.ibm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64

(Continued on next page)
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SPEC CPU®2017 Floating Point Rate Result

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**Base Portability Flags (Continued)**

554.roms_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
- w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- mbranches-within-32B-boundaries -ljemalloc
- L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
- w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- mbranches-within-32B-boundaries -ljemalloc
- L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
- w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
- qopt-prefetch -ffinite-math-only
- qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4
- nostandard-realloc-lhs -align array32byte -auto
- mbranches-within-32B-boundaries -ljemalloc
- L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
- w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo
- no-prec-div -qopt-prefetch -ffinite-math-only
- qopt-multiple-gather-scatter-by-shuffles
- mbranches-within-32B-boundaries -nostandard-realloc-lhs
- align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
- w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- mbranches-within-32B-boundaries -ljemalloc
- L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:
- w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- no-prec-div -qopt-prefetch -ffinite-math-only
- qopt-multiple-gather-scatter-by-shuffles
- mbranches-within-32B-boundaries -nostandard-realloc-lhs

(Continued on next page)
New H3C Technologies Co., Ltd. | SPECrate®2017_fp_base = 341
H3C UniServer R4300 G5 (Intel Xeon Gold 6338T) | SPECrate®2017_fp_peak = Not Run

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**SPEC CPU®2017 Floating Point Rate Result**

**Base Optimization Flags (Continued)**

Benchmarks using Fortran, C, and C++ (continued):
- `align array32byte` -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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