**SPEC CPU®2017 Integer Rate Result**

Dell Inc.  
PowerEdge C6420 (Intel Xeon Gold 6240R, 2.40 GHz)  

| Test Date:          | Nov-2021  
|---------------------|-----------  
| Hardware Availability: | Apr-2019  
| Software Availability: | May-2021  

**SPECrate®2017_int_base = 301**  
**SPECrate®2017_int_peak = 315**

### Hardware

<table>
<thead>
<tr>
<th>Name</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 6240R</td>
</tr>
<tr>
<td>Max MHz</td>
<td>4000</td>
</tr>
<tr>
<td>Nominal</td>
<td>2400</td>
</tr>
<tr>
<td>Enabled</td>
<td>48 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Cache L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>Cache L3</td>
<td>35.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)</td>
</tr>
<tr>
<td>Storage</td>
<td>125 GB on tmpfs</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Name</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>Red Hat Enterprise Linux 8.4 (Ootpa)</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>No</td>
</tr>
<tr>
<td>Firmware</td>
<td>Version 2.11.2 released Apr-2021</td>
</tr>
<tr>
<td>File System</td>
<td>tmpfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage.</td>
</tr>
</tbody>
</table>

---

At the peak point, the CPU attained a SPECrate®2017_int_base of 301 and a SPECrate®2017_int_peak of 315.
Dell Inc.  
PowerEdge C6420 (Intel Xeon Gold 6240R, 2.40 GHz)  

SPEC CPU®2017 Integer Rate Result  

SPECrate®2017_int_base = 301  
SPECrate®2017_int_peak = 315  

Results Table  

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>500.perlbench_r</td>
<td>96</td>
<td>745</td>
<td>205</td>
<td>730</td>
<td>209</td>
<td>96</td>
<td>622</td>
<td>246</td>
<td>621</td>
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<tr>
<td>502.gcc_r</td>
<td>96</td>
<td>592</td>
<td>230</td>
<td>577</td>
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<td>491</td>
<td>277</td>
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</tr>
<tr>
<td>505.mcf_r</td>
<td>96</td>
<td>318</td>
<td>487</td>
<td>309</td>
<td>502</td>
<td>96</td>
<td>318</td>
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<td>309</td>
<td>502</td>
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<td></td>
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<tr>
<td>520.omnetpp_r</td>
<td>96</td>
<td>676</td>
<td>186</td>
<td>672</td>
<td>187</td>
<td>96</td>
<td>676</td>
<td>186</td>
<td>672</td>
<td>187</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>96</td>
<td>264</td>
<td>384</td>
<td>258</td>
<td>394</td>
<td>96</td>
<td>264</td>
<td>384</td>
<td>258</td>
<td>394</td>
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</tr>
<tr>
<td>525.x264_r</td>
<td>96</td>
<td>268</td>
<td>627</td>
<td>263</td>
<td>638</td>
<td>96</td>
<td>251</td>
<td>670</td>
<td>251</td>
<td>670</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes  
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes  
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes  
Environment variables set by runcpu before the start of the run:  
LD_LIBRARY_PATH = 
"/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/lib/ia32:/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/je5.0.1-32"

MALLOC_CONF = "retain:true"

General Notes  
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM  
memory using Red Hat Enterprise Linux 8.1  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop_caches

(Continued on next page)
Dell Inc.

**PowerEdge C6420** (Intel Xeon Gold 6240R, 2.40 GHz)

<table>
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<tr>
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<tr>
<td>SPECrate®2017_int_peak = 315</td>
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</table>

---

**General Notes (Continued)**

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

---

**Platform Notes**

BIOS settings:

- Sub NUMA Cluster : 2-Way Clustering
- Virtualization Technology : Disabled
- System Profile : Custom
- CPU Power Management : Maximum Performance
- C1E : Disabled
- C States : Autonomous
- Memory Patrol Scrub : Disabled
- Energy Efficiency Policy : Performance
- CPU Interconnect Bus Link
- Power Management : Disabled
- PCI ASPM L1 Link
- Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2021.1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaf64d
running on localhost.localdomain Tue Nov  9 14:10:22 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see [https://www.spec.org/cpu2017/Docs/config.html#sysinfo](https://www.spec.org/cpu2017/Docs/config.html#sysinfo)

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Gold 6240R CPU @ 2.40GHz
- 2 "physical id"s (chips)
- 96 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following

(Continued on next page)
Dell Inc.

PowerEdge C6420 (Intel Xeon Gold 6240R, 2.40 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2021
Hardware Availability: Apr-2019
Software Availability: May-2021

SPEC CPU®2017 Integer Rate Result

SPECrate®2017_int_base = 301
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excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 6 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6240R CPU @ 2.40GHz
BIOS Model name: Intel(R) Xeon(R) Gold 6240R CPU @ 2.40GHz
Stepping: 7
CPU MHz: 2329.040
CPU max MHz: 4000.0000
CPU min MHz: 1000.0000
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s):
0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76,80,84,88,92
NUMA node1 CPU(s):
1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77,81,85,89,93
NUMA node2 CPU(s):
2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78,82,86,90,94
NUMA node3 CPU(s):
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrp pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppn vmm致电 mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust

(Continued on next page)
### Dell Inc.

**PowerEdge C6420 (Intel Xeon Gold 6240R, 2.40 GHz)**

#### SPEC CPU®2017 Integer Rate Result

<table>
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<td>301</td>
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<td>Software Availability:</td>
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</tbody>
</table>

#### Platform Notes (Continued)

```
# From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68 72 76 80 84 88 92
node 0 size: 95304 MB
node 0 free: 95062 MB
node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69 73 77 81 85 89 93
node 1 size: 96762 MB
node 1 free: 96406 MB
node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78 82 86 90 94
node 2 size: 96725 MB
node 2 free: 96535 MB
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71 75 79 83 87 91 95
node 3 size: 96762 MB
node 3 free: 87268 MB
node distances:
node 0 1 2 3
0: 10 21 11 21
1: 21 10 21 11
2: 11 21 10 21
3: 21 11 21 10
```

```
# From /proc/meminfo
MemTotal:       394808252 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

```
# /sbin/tuned-adm active
Current active profile: throughput-performance
```

```
# /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance
```

```
# From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.4 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
```

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Dell Inc.

PowerEdge C6420 (Intel Xeon Gold 6240R, 2.40 GHz)

SPECrate®2017_int_base = 301

SPECrate®2017_int_peak = 315

<table>
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<tr>
<th>CPU2017 License</th>
<th>Test Sponsor</th>
<th>Test Date</th>
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<tbody>
<tr>
<td>55</td>
<td>Dell Inc.</td>
<td>Nov-2021</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Hardware Availability</th>
<th>Software Availability</th>
</tr>
</thead>
<tbody>
<tr>
<td>Apr-2019</td>
<td>May-2021</td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

```plaintext
VERSION_ID="8.4"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

uname -a:
Linux localhost.localdomain 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): KVM: Mitigation: Split huge pages
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Not affected
CVE-2017-5753 (Spectre variant 1):
CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling): Mitigation: Enhanced IBRS, IBPB:

run-level 3 Nov 9 10:34

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-ic2021.1
Filesystem   Type Size Used Avail Use% Mounted on
  tmpfs     tmpfs  125G  4.4G  121G   4% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
  Vendor: Dell Inc.
  Product: PowerEdge C6420
  Product Family: PowerEdge

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
  1x 002C069D002C 36ASF4G72PZ-2G9E2 32 GB 2 rank 2933
  4x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
```

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Dell Inc.
PowerEdge C6420 (Intel Xeon Gold 6240R, 2.40 GHz)

SPECrater®2017_int_base = 301
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2021
Hardware Availability: Apr-2019
Software Availability: May-2021

Platform Notes (Continued)

6x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
1x 00AD069D00AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933

BIOS:
- BIOS Vendor: Dell Inc.
- BIOS Version: 2.11.2
- BIOS Date: 04/22/2021
- BIOS Revision: 2.11

(End of data from sysinfo program)

Compiler Version Notes

============================================================================
C | 500.perlbench_r(peak) 557.xz_r(peak)
============================================================================
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
  64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
============================================================================
C | 502.gcc_r(peak)
============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version
  2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
============================================================================
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
  525.x264_r(base, peak) 557.xz_r(base)
============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
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C | 500.perlbench_r(peak) 557.xz_r(peak)
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Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
## Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>Language</th>
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</tr>
</thead>
<tbody>
<tr>
<td>C</td>
<td>502.gcc_r(peak)</td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113</td>
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<td></td>
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<tr>
<td></td>
<td></td>
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<td>C</td>
<td>500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)</td>
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<td></td>
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</tr>
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<td>500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)</td>
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</tr>
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<td></td>
<td>531.deepsjeng_r(base, peak) 541.leela_r(base, peak)</td>
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# SPEC CPU®2017 Integer Rate Result

## Dell Inc.

**PowerEdge C6420 (Intel Xeon Gold 6240R, 2.40 GHz)**

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</table>

### SPECrate®2017_int_base = 301

### SPECrate®2017_int_peak = 315

### Compiler Version Notes (Continued)

```
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
Fortran | 548.exchange2_r(base, peak)
```

```
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
 Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

### Base Compiler Invocation

- **C benchmarks:** icx
- **C++ benchmarks:** icpx
- **Fortran benchmarks:** ifort

### Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

### Base Optimization Flags

```
C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
```
(Continued on next page)
## Base Optimization Flags (Continued)

C benchmarks (continued):
- `-mbranches-within-32B-boundaries`
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`
- `-lqkmalloc`

C++ benchmarks:
- `-w` `-m64` `-Wl,-z,muldefs` `-xCORE-AVX512` `-O3` `-ffast-math` `-flto`
- `-mfpmath=sse` `-funroll-loops` `-gopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries`
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`
- `-lqkmalloc`

Fortran benchmarks:
- `-w` `-m64` `-Wl,-z,muldefs` `-xCORE-AVX512` `-O3` `-ipo` `-no-prec-div`
- `-gopt-mem-layout-trans=4` `-nostandard-realloc-lhs` `-align array32byte`
- `-auto` `-mbranches-within-32B-boundaries`
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`
- `-lqkmalloc`

## Peak Compiler Invocation

C benchmarks (except as noted below):
- `icx`
- `500.perlbench_r:icx`
- `557.xz_r:icc`

C++ benchmarks:
- `icpx`

Fortran benchmarks:
- `ifort`

## Peak Portability Flags

- `500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64`
- `502.gcc_r: -D_FILE_OFFSET_BITS=64`
- `505.mcf_r: -DSPEC_LP64`
- `520.omnetpp_r: -DSPEC_LP64`
- `523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX`
Dell Inc.

PowerEdge C6420 (Intel Xeon Gold 6240R, 2.40 GHz)

SPECrate®2017_int_base = 301
SPECrate®2017_int_peak = 315

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2021
Hardware Availability: Apr-2019
Software Availability: May-2021

Peak Portability Flags (Continued)

525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leet_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-O3 -ffast-math -qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:

520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes

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SPEC CPU®2017 Integer Rate Result

Dell Inc.

PowerEdge C6420 (Intel Xeon Gold 6240R, 2.40 GHz)

SPECrater®2017_int_base = 301
SPECrater®2017_int_peak = 315

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2021
Hardware Availability: Apr-2019
Software Availability: May-2021

Peak Optimization Flags (Continued)

531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

Fortran benchmarks:
548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.4.xml

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