## SPEC CPU®2017 Integer Speed Result

**Dell Inc.**

PowerEdge M640 (Intel Xeon Gold 6208U, 2.90 GHz)

<table>
<thead>
<tr>
<th>SPECspeed\textsuperscript{®2017_int_peak} = 11.3</th>
<th>SPECspeed\textsuperscript{®2017_int_base} = 11.0</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dell Inc.</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>CPU2017 License: 55</td>
<td>Test Date: Nov-2021</td>
</tr>
<tr>
<td>Test Sponsor: Dell Inc.</td>
<td>Hardware Availability: Apr-2019</td>
</tr>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Software Availability: May-2021</td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed\textsuperscript{®2017_int_peak}</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>16</td>
<td>7.80</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>16</td>
<td>10.1</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>16</td>
<td>10.6</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>16</td>
<td>8.74</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>16</td>
<td>13.7</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>16</td>
<td>16.4</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>16</td>
<td>6.10</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>16</td>
<td>4.90</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>16</td>
<td>16.6</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>16</td>
<td>20.2</td>
</tr>
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</table>

### Software

<table>
<thead>
<tr>
<th>Component</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>Red Hat Enterprise Linux 8.4 (Ootpa) 4.18.0-305.el8.x86_64</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>Yes</td>
</tr>
<tr>
<td>Firmware</td>
<td>Version 2.12.2 released Jul-2021</td>
</tr>
<tr>
<td>File System</td>
<td>tmpfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator V5.0.1</td>
<td></td>
</tr>
<tr>
<td>Power Management</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage.</td>
</tr>
</tbody>
</table>

#### CPU Details

- **CPU Name**: Intel Xeon Gold 6208U
- **Max MHz**: 3900
- **Nominal**: 2900
- **Enabled**: 16 cores, 1 chip
- **Orderable**: 1 chip
- **Cache L1**: 32 KB I + 32 KB D on chip per core
- **L2**: 1 MB I+D on chip per core
- **L3**: 22 MB I+D on chip per chip
- **Other**: None
- **Memory**: 192 GB (6 x 32 GB 2Rx4 PC4-2933Y-R)
- **Storage**: 125 GB on tmpfs
- **Other**: None

#### Memory Details

- **Memory**: 192 GB (6 x 32 GB 2Rx4 PC4-2933Y-R)
- **Storage**: 125 GB on tmpfs
- **Other**: None

---

Page 1 | Standard Performance Evaluation Corporation (info@spec.org) | https://www.spec.org/
Dell Inc. PowerEdge M640 (Intel Xeon Gold 6208U, 2.90 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2021
Hardware Availability: Apr-2019
Software Availability: May-2021

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tbody>
<tr>
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<td>Base</td>
<td></td>
<td></td>
<td>Peak</td>
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<td></td>
<td></td>
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<tr>
<td></td>
<td>Threads</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Threads</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Threads</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Threads</td>
<td>Seconds</td>
</tr>
<tr>
<td>600.perlbench_s</td>
<td>16</td>
<td>273</td>
<td>6.50</td>
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<td>271</td>
<td>6.56</td>
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<td>277</td>
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<tr>
<td>602.gcc_s</td>
<td>16</td>
<td>393</td>
<td>10.1</td>
<td>16</td>
<td>383</td>
<td>10.4</td>
<td>16</td>
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<td>10.6</td>
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<td>375</td>
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<tr>
<td>605.mcf_s</td>
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<td>244</td>
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<td>16</td>
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<td>19.5</td>
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<td>244</td>
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<td>16</td>
<td>227</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>16</td>
<td>187</td>
<td>8.74</td>
<td>16</td>
<td>186</td>
<td>8.78</td>
<td>16</td>
<td>187</td>
<td>8.74</td>
<td>16</td>
<td>186</td>
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<tr>
<td>623.xalancbmk_s</td>
<td>16</td>
<td>103</td>
<td>13.8</td>
<td>16</td>
<td>103</td>
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<td>16</td>
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<td>13.8</td>
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<tr>
<td>625.x264_s</td>
<td>16</td>
<td>108</td>
<td>16.4</td>
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<td>16.4</td>
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<td>104</td>
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<td>631.deepsjeng_s</td>
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<td>6.11</td>
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</tr>
<tr>
<td>641.leelu_s</td>
<td>16</td>
<td>348</td>
<td>4.91</td>
<td>16</td>
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<td>4.90</td>
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<tr>
<td>648.exchange2_s</td>
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<td>177</td>
<td>16.6</td>
<td>16</td>
<td>175</td>
<td>16.8</td>
<td>16</td>
<td>177</td>
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<tr>
<td>657.x264_s</td>
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<td>305</td>
<td>20.3</td>
<td>16</td>
<td>306</td>
<td>20.2</td>
<td>16</td>
<td>305</td>
<td>20.3</td>
<td>16</td>
<td>306</td>
</tr>
</tbody>
</table>

SPECspeed®2017_int_base = 11.0
SPECspeed®2017_int_peak = 11.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/je5.0.1-64"
MALLOCC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
## Dell Inc.

**PowerEdge M640 (Intel Xeon Gold 6208U, 2.90 GHz)**

<table>
<thead>
<tr>
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<th>SPECspeed®2017_int_peak = 11.3</th>
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</table>

**CPU2017 License:** 55  
**Test Date:** Nov-2021  
**Test Sponsor:** Dell Inc.  
**Hardware Availability:** Apr-2019  
**Tested by:** Dell Inc.  
**Software Availability:** May-2021

### General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

### Platform Notes

**BIOS Settings:**  
- Logical Processor : Disabled  
- Virtualization Technology : Disabled  
- System Profile : Custom  
- CPU Power Management : Maximum Performance  
- C1E : Disabled  
- C States : Autonomous  
- Memory Patrol Scrub : Disabled  
- Energy Efficiency Policy : Performance  
- CPU Interconnect Bus Link  
  - Power Management : Disabled  
- PCI ASPM L1 Link  
  - Power Management : Disabled  

**Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2021.1/bin/sysinfo**  
**Rev:** r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca6c64d running on localhost.localdomain Fri Nov 5 03:34:32 2021

**SUT (System Under Test) info as seen by some common utilities.**  
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
- model name : Intel(R) Xeon(R) Gold 6208U CPU @ 2.90GHz  
  1 "physical id"s (chips)  
  16 "processors"  
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
  cpu cores : 16  
  siblings : 16  
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu from util-linux 2.32.1:
- Architecture: x86_64  
- CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
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**Dell Inc.**

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<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>11.0</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>11.3</td>
</tr>
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</table>

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**Tested by:** Dell Inc.

**Spec CPU 2017 Integer Speed Result**

**Test Date:** Nov-2021  
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---

**Platform Notes (Continued)**

Byte Order: Little Endian  
CPU(s): 16  
On-line CPU(s) list: 0-15  
Thread(s) per core: 1  
Core(s) per socket: 16  
Socket(s): 1  
NUMA node(s): 1  
Vendor ID: GenuineIntel  
BIOS Vendor ID: Intel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Gold 6208U CPU @ 2.90GHz  
BIOS Model name: Intel(R) Xeon(R) Gold 6208U CPU @ 2.90GHz  
Stepping: 7  
CPU MHz: 3600.283  
CPU max MHz: 3900.0000  
CPU min MHz: 1200.0000  
BogoMIPS: 5800.00  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 1024K  
L3 cache: 22528K  
NUMA node0 CPU(s): 0-15  
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault ebx cat_l3 cdp_c3 cio_c0 c0_c3 c3_c0 xtrunc pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault ebx cat_l3 cdp_l3 invpcid_single intel_pmm nsb mba ibrs ibpb stibp ibrs_enhanced fsogbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaveas cqm_11c cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts pkru ospke avx512_vnni md_clear flush_l1d arch_capabilities

/pro/proc/cpuinfo cache data

```  
cache size : 22528 KB
```

From numactl --hardware

**WARNING:** a numactl 'node' might or might not correspond to a physical chip.

```  
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 192036 MB
node 0 free: 181886 MB
node distances:
node 0
```

(Continued on next page)
Dell Inc.
PowerEdge M640 (Intel Xeon Gold 6208U, 2.90 GHz)

SPECspeed®2017_int_base = 11.0
SPECspeed®2017_int_peak = 11.3

Platform Notes (Continued)

0: 10

From /proc/meminfo
MemTotal: 196645020 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.4 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.4"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga
uname -a:
Linux localhost.localdomain 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): KVM: Mitigation: Split huge pages
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitation
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

(Continued on next page)
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<tr>
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</tr>
</thead>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Platform Notes (Continued)**

run-level 3 Nov 5 03:31

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-ic2021.1

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>tmpfs</td>
<td>tmpfs</td>
<td>125G</td>
<td>4.4G</td>
<td>121G</td>
<td>4%</td>
<td>/mnt/ramdisk</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id

**Vendor:** Dell Inc.  
**Product:** PowerEdge M640  
**Product Family:** PowerEdge

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**Memory:**

3x 002C069D002C 36ASF4G72PZ-2G9E2 32 GB 2 rank 2933  
2x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933  
1x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933

**BIOS:**

**BIOS Vendor:** Dell Inc.  
**BIOS Version:** 2.12.2  
**BIOS Date:** 07/12/2021  
**BIOS Revision:** 2.12

(End of data from sysinfo program)

**Compiler Version Notes**

==============================================================================
C       | 600.perlbench_s(peak)  
|---------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)  
|---------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
(Continued on next page)
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| Test Date: Nov-2021 | Hardware Availability: Apr-2019 |
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### Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>Compiler</th>
<th>Benchmark List</th>
</tr>
</thead>
<tbody>
<tr>
<td>C</td>
<td>600.perlbench_s (peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000 Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td></td>
<td>600.perlbench_s (base) 602.gcc_s (base, peak) 605.mcf_s (base, peak) 625.x264_s (base, peak) 657.xz_s (base, peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113 Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>C++</td>
<td>620.omnetpp_s (base, peak) 623.xalancbmk_s (base, peak) 631.deepsjeng_s (base, peak) 641.leela_s (base, peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113 Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>Fortran</td>
<td>648.exchange2_s (base, peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000 Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

### Base Compiler Invocation

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpdx

**Fortran benchmarks:**
- ifort
SPEC CPU®2017 Integer Speed Result

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SPECspeed®2017_int_base = 11.0
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CPU2017 License: 55
Test Sponsor: Dell Inc.
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Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fopenmp -Wl,-z,muldefs -xCORE-AVX512
-03 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -03 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-1qkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -03 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks (except as noted below):
icx

600.perlbench_s: icc

C++ benchmarks:
icpx

(Continued on next page)
Dell Inc. PowerEdge M640 (Intel Xeon Gold 6208U, 2.90 GHz)

SPECspeed®2017_int_base = 11.0
SPECspeed®2017_int_peak = 11.3

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Nov-2021
Tested by: Dell Inc.
Hardware Availability: Apr-2019
Software Availability: May-2021

Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

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PowerEdge M640 (Intel Xeon Gold 6208U, 2.90 GHz)

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SPECspeed®2017_int_peak = 11.3

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Peak Optimization Flags (Continued)

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.4.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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