## Dell Inc.

PowerEdge R750xs (Intel Xeon Silver 4314, 2.40 GHz)

### SPEC CPU®2017 Integer Speed Result

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017_int_base = 11.2</th>
<th>SPECspeed®2017_int_peak = 11.4</th>
</tr>
</thead>
<tbody>
<tr>
<td>600_perlbench_s 32</td>
<td>6.97</td>
<td>8.08</td>
</tr>
<tr>
<td>602_gcc_s 32</td>
<td>10.4</td>
<td>10.8</td>
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<tr>
<td>605_mcf_s 32</td>
<td>19.3</td>
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<tr>
<td>620_omnetpp_s 32</td>
<td>9.37</td>
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<tr>
<td>623_xalancbmk_s 32</td>
<td>13.1</td>
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<tr>
<td>625_x264_s 32</td>
<td>16.4</td>
<td></td>
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<tr>
<td>631_deepsjeng_s 32</td>
<td>5.75</td>
<td></td>
</tr>
<tr>
<td>641_leela_s 32</td>
<td>4.71</td>
<td></td>
</tr>
<tr>
<td>648_exchange2_s 32</td>
<td>18.8</td>
<td></td>
</tr>
<tr>
<td>657_xz_s 32</td>
<td>21.3</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>CPU Name: Intel Xeon Silver 4314</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz: 3400</td>
</tr>
<tr>
<td>Nominal: 2400</td>
</tr>
<tr>
<td>Enabled: 32 cores, 2 chips</td>
</tr>
<tr>
<td>Orderable: 1,2 chips</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2: 1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3: 24 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
<tr>
<td>Memory: 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R, running at 2666)</td>
</tr>
<tr>
<td>Storage: 225 GB on tmpfs</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>OS: Red Hat Enterprise Linux 8.4 (Ootpa)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;</td>
</tr>
<tr>
<td>C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel: Yes</td>
</tr>
<tr>
<td>Firmware: Version 1.2.1 released May-2021</td>
</tr>
<tr>
<td>File System: tmpfs</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Peak Pointers: 64-bit</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.</td>
</tr>
</tbody>
</table>

---

Test Date: Aug-2021
Hardware Availability: Jul-2021
Software Availability: May-2021
Dell Inc.
PowerEdge R750xs (Intel Xeon Silver 4314, 2.40 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.4

Test Date: Aug-2021
Hardware Availability: Jul-2021
Software Availability: May-2021

Results Table

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<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tr>
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<td>255</td>
<td>6.97</td>
<td>252</td>
<td>7.03</td>
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<tr>
<td>602.gcc_s</td>
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<td>383</td>
<td>10.4</td>
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<tr>
<td>605.mcf_s</td>
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<td>620.omnetpp_s</td>
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<tr>
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<td>108</td>
<td>13.2</td>
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<td>631.deepsjeng_s</td>
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<tr>
<td>641.leeola_s</td>
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<td>4.72</td>
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<td>648.exchange2_s</td>
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<td>18.8</td>
<td></td>
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<tr>
<td>657.xz_s</td>
<td>32</td>
<td>287</td>
<td>21.6</td>
<td>290</td>
<td>21.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity= fine,scatter"
- LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches


NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:
- Logical Processor: Disabled
- Virtualization Technology: Disabled
- System Profile: Custom
- CPU Power Management: Maximum Performance
- CIE: Disabled
- C States: Autonomous
- Memory Patrol Scrub: Disabled
- Energy Efficiency Policy: Performance
- CPU Interconnect Bus Link: Power Management: Disabled
- Power Management: Disabled
- PCI ASPM L1 Link: Power Management: Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2021.1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca5c64d
running on r750xs.jzjpm83.inside.dell.com Mon Aug 30 11:08:27 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Silver 4314 CPU @ 2.40GHz
- 2 "physical id"s (chips)
- 32 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 16
  - siblings: 16
  - physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  - physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu from util-linux 2.32.1:
- Architecture: x86_64
Dell Inc.
PowerEdge R750xs (Intel Xeon Silver 4314, 2.40 GHz)

SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.4

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Aug-2021
Hardware Availability: Jul-2021
Software Availability: May-2021

Platform Notes (Continued)

CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Silver 4314 CPU @ 2.40GHz
BIOS Model name: Intel(R) Xeon(R) Silver 4314 CPU @ 2.40GHz
Stepping: 6
CPU MHz: 1837.712
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 24576K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 cli flush dtst acp1 mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp lm constant_tsc archive perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtets64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrp pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid fault ebpx cat_l3 invpcid_single intel_ppp inse sbd mba ibrs ibp bsti bss_enhanced fsgsbase tsc_adjust bm1i hle avx2 smep bm12 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_p t avx512d sha ni avx512bw avx512vl xsaveopt xsave vgetbv x saves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local split_lock_detect wboinvd dterm ida pin pts avx512vbmi umip pku ospe avx512_vbmi2 fni vaes vclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq 1a57 rdpid fsrm md_clear pconfig flush_l1d arch_capabilities

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
 available: 2 nodes (0-1)
 node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30
 node 0 size: 257181 MB
 node 0 free: 255692 MB

(Continued on next page)
Platform Notes (Continued)

node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31
node 1 size: 258004 MB
node 1 free: 248344 MB
node distances:
  node 0 1
  0: 10 20
  1: 20 10

From /proc/meminfo
  MemTotal: 527551056 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.4 (Ootpa)"
    ID="rheil"
    ID_LIKE="fedora"
    VERSION_ID="8.4"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

uname -a:
  Linux r750xs.jzjpm83.inside.dell.com 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling:
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass):
  Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):
  Mitigation: usercopy/swapsgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):
  Mitigation: Enhanced IBRS, IBPB:

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Dell Inc.**  
PowerEdge R750xs (Intel Xeon Silver 4314, 2.40 GHz)  

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 11.2</th>
<th>SPECspeed®2017_int_peak = 11.4</th>
</tr>
</thead>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Aug-2021  
**Hardware Availability:** Jul-2021  
**Software Availability:** May-2021

---

**Platform Notes (Continued)**

- Conditional, RSB filling
- CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
- CVE-2019-11135 (TSX Asynchronous Abort): Not affected

- run-level 3 Aug 27 10:27

- SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-ic2021.1
- Filesystem | Type | Size | Used | Avail | Use% | Mounted on
- tmpfs | tmpfs | 225G | 4.4G | 221G | 2% | /mnt/ramdisk

- From /sys/devices/virtual/dmi/id
  - Vendor: Dell Inc.
  - Product: PowerEdge R750xs
  - Product Family: PowerEdge
  - Serial: JZJPM83

- Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  - Memory: 16x 002C069D002C 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200, configured at 2666

- BIOS:
  - BIOS Vendor: Dell Inc.
  - BIOS Version: 1.2.1
  - BIOS Date: 05/28/2021
  - BIOS Revision: 1.2

(End of data from sysinfo program)

---

**Compiler Version Notes**

---

```
C       | 600.perlbench_s(peak)
-------------------------------
```

---

**Intel(R) C Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000**

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
```

---

**Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,**

(Continued on next page)
Dell Inc.  
PowerEdge R750xs (Intel Xeon Silver 4314, 2.40 GHz)  

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
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<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
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</table>

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.  
CPU2017 License: 55  
Test Date: Aug-2021  
Hardware Availability: Jul-2021  
Software Availability: May-2021

**Compiler Version Notes (Continued)**

```
C       | 600.perlbench_s(peak)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
Fortran | 648.exchange2_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

**Base Compiler Invocation**

C benchmarks:  
icx

C++ benchmarks:  
icpx

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.
PowerEdge R750xs (Intel Xeon Silver 4314, 2.40 GHz)

SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.4

Base Compiler Invocation (Continued)
Fortran benchmarks:
ifort

Base Portability Flags
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags
C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Peak Compiler Invocation
C benchmarks (except as noted below):
icx

(Continued on next page)
Dell Inc.
PowerEdge R750xs (Intel Xeon Silver 4314, 2.40 GHz)

SPECspeed®2017_int_base = 11.2
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CPU2017 License: 55
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Peak Compiler Invocation (Continued)

600.perlbench_s: icc
C++ benchmarks:
icpx
Fortran benchmarks:
ifort

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags
C benchmarks:
600.perlbench_s: -W1,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -W1,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -W1,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes
C++ benchmarks:

(Continued on next page)
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</tr>
</tbody>
</table>

### Peak Optimization Flags (Continued)

620.omnetpp_s: basepeak = yes  
623.xalancbmk_s: basepeak = yes  
631.deepsjeng_s: basepeak = yes  
641.leela_s: basepeak = yes  

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml  
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.4.xml

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