## SPEC CPU®2017 Floating Point Rate Result

**Inspur Corporation**

**Inspur NF5280M6 (Intel Xeon Silver 4309Y)**

**SPECrates**:  
- **SPECrates**\(^{2017\_fp\_peak}\) = 164  
- **SPECrates**\(^{2017\_fp\_base}\) = 163

### CPU2017 License:
3358

### Test Sponsor:
Inspur Corporation

### Tested by:
Inspur Corporation

### Hardware

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate(^{2017_fp_base})</th>
<th>SPECrate(^{2017_fp_peak})</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
<td>209</td>
<td>402</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>32</td>
<td>102</td>
<td>209</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>32</td>
<td>92.9</td>
<td>192</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>16</td>
<td>95.8</td>
<td>190</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>32</td>
<td>154</td>
<td>209</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>32</td>
<td>146</td>
<td>177</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
<td>162</td>
<td>192</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>32</td>
<td>138</td>
<td>172</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>32</td>
<td>146</td>
<td>174</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>32</td>
<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>32</td>
<td>236</td>
<td>252</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>32</td>
<td>144</td>
<td>209</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>16</td>
<td>77.1</td>
<td>84.2</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>OS</th>
<th>Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86_64</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler</td>
<td>C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>No</td>
</tr>
<tr>
<td>Firmware</td>
<td>Version 05.00.02 released May-2021</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage.</td>
</tr>
</tbody>
</table>
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>32</td>
<td>763</td>
<td>420</td>
<td>764</td>
<td>420</td>
<td>16</td>
<td>399</td>
<td>402</td>
<td>399</td>
<td>402</td>
<td>399</td>
<td>402</td>
<td>399</td>
<td>402</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>32</td>
<td>194</td>
<td>209</td>
<td>196</td>
<td>206</td>
<td>32</td>
<td>194</td>
<td>209</td>
<td>196</td>
<td>206</td>
<td>193</td>
<td>209</td>
<td>193</td>
<td>209</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>32</td>
<td>299</td>
<td>102</td>
<td>298</td>
<td>102</td>
<td>32</td>
<td>299</td>
<td>102</td>
<td>298</td>
<td>102</td>
<td>299</td>
<td>102</td>
<td>299</td>
<td>102</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>32</td>
<td>901</td>
<td>92.7</td>
<td>901</td>
<td>92.9</td>
<td>16</td>
<td>424</td>
<td>98.8</td>
<td>423</td>
<td>98.9</td>
<td>424</td>
<td>98.8</td>
<td>424</td>
<td>98.8</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>32</td>
<td>487</td>
<td>154</td>
<td>487</td>
<td>154</td>
<td>32</td>
<td>425</td>
<td>176</td>
<td>421</td>
<td>177</td>
<td>422</td>
<td>177</td>
<td>422</td>
<td>177</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>32</td>
<td>231</td>
<td>146</td>
<td>231</td>
<td>146</td>
<td>32</td>
<td>231</td>
<td>146</td>
<td>232</td>
<td>145</td>
<td>231</td>
<td>146</td>
<td>231</td>
<td>146</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>32</td>
<td>443</td>
<td>162</td>
<td>449</td>
<td>160</td>
<td>16</td>
<td>271</td>
<td>132</td>
<td>273</td>
<td>131</td>
<td>272</td>
<td>132</td>
<td>272</td>
<td>132</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>32</td>
<td>352</td>
<td>139</td>
<td>353</td>
<td>138</td>
<td>32</td>
<td>352</td>
<td>139</td>
<td>353</td>
<td>138</td>
<td>352</td>
<td>138</td>
<td>352</td>
<td>138</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>32</td>
<td>384</td>
<td>146</td>
<td>384</td>
<td>146</td>
<td>32</td>
<td>383</td>
<td>146</td>
<td>384</td>
<td>146</td>
<td>384</td>
<td>146</td>
<td>384</td>
<td>146</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>32</td>
<td>218</td>
<td>365</td>
<td>218</td>
<td>365</td>
<td>32</td>
<td>218</td>
<td>365</td>
<td>218</td>
<td>365</td>
<td>218</td>
<td>365</td>
<td>218</td>
<td>365</td>
</tr>
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<td>32</td>
<td>231</td>
<td>233</td>
<td>228</td>
<td>236</td>
<td>32</td>
<td>224</td>
<td>240</td>
<td>228</td>
<td>236</td>
<td>224</td>
<td>240</td>
<td>224</td>
<td>240</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>32</td>
<td>865</td>
<td>144</td>
<td>868</td>
<td>144</td>
<td>32</td>
<td>865</td>
<td>144</td>
<td>868</td>
<td>144</td>
<td>868</td>
<td>144</td>
<td>868</td>
<td>144</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>32</td>
<td>661</td>
<td>76.9</td>
<td>660</td>
<td>77.1</td>
<td>658</td>
<td>77.2</td>
<td>16</td>
<td>302</td>
<td>84.1</td>
<td>302</td>
<td>84.2</td>
<td>301</td>
<td>84.5</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
SCALING_GOVERNOR set to Performance

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
Inspur Corporation

Inspur NF5280M6 (Intel Xeon Silver 4309Y)

SPECrate®2017_fp_base = 163
SPECrate®2017_fp_peak = 164

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Oct-2021
Hardware Availability: May-2021
Software Availability: Dec-2020

General Notes (Continued)

sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numacli i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5,
and the system compiler gcc 4.8.5;
sources available from jemalloc.net or

Platform Notes

BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
C1E Support set to Disable

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acfc64d
running on localhost.localdomain Sat Oct  9 17:02:39 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Silver 4309Y CPU @ 2.80GHz
    2 "physical id"s (chips)
    32 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 8
    siblings : 16
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:
  Architecture: x86_64

(Continued on next page)
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SPECrate®2017_fp_base = 163
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Test Date: Oct-2021
Hardware Availability: May-2021
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Platform Notes (Continued)

- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 32
- On-line CPU(s) list: 0-31
- Thread(s) per core: 2
- Core(s) per socket: 8
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 106
- Model name: Intel(R) Xeon(R) Silver 4309Y CPU @ 2.80GHz
- Stepping: 6
- CPU MHz: 3400.000
- CPU max MHz: 3600.0000
- CPU min MHz: 800.0000
- BogoMIPS: 5600.00
- Virtualization: VT-x
- L1d cache: 48K
- L1i cache: 32K
- L2 cache: 1280K
- L3 cache: 12288K
- NUMA node0 CPU(s): 0-7,16-23
- NUMA node1 CPU(s): 8-15,24-31
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpte1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs ibpb ibpib ibrs_enhanced tpr_shadow vmi lexplicity etp vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cmqm rdt_a avx512f avx512q rdseed adx smap avx512ifmnea clflushopt clwb intel_pt avx512cd sha_hni avx512bw avx512vl xsaveopt xsaves xgetbv1 xsavec cmqm_llc cmqm_mbm_total cmqm_mbm_local wbnoinvd dtherm ida arat pin pts avx512vbmi umip pku ospe avx512vbmi2 gfnia vaes vpcm1ldq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data
- cache size : 12288 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.

- available: 2 nodes (0-1)
- node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
- node 0 size: 515657 MB
- node 0 free: 504219 MB

(Continued on next page)
### Platform Notes (Continued)

- **node 1 cpus:** 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
- **node 1 size:** 516089 MB
- **node 1 free:** 507109 MB
- **node distances:**
  - node 0 1:
  - 0: 10 20
  - 1: 20 10

From `/proc/meminfo`:
- **MemTotal:** 1056508892 kB
- **HugePages_Total:** 0
- **Hugepagesize:** 2048 kB

/sbin/tuned-adm active
- Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
- performance

From `/etc/*release* /etc/*version`:
- **os-release:**
  - NAME="Red Hat Enterprise Linux"
  - VERSION=\"8.2 (Ootpa)\"
  - ID=\"rhel\"
  - ID_LIKE=\"fedora\"
  - VERSION_ID=\"8.2\"
  - PLATFORM_ID=\"platform:el8\"
  - PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
  - ANSI_COLOR=\"0;31\"
- redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
- system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
- system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
- Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
- x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
- CVE-2018-12207 (iTLB Multihit):
  - Not affected
- CVE-2018-3620 (L1 Terminal Fault):
  - Not affected
- Microarchitectural Data Sampling:
  - Not affected
- CVE-2017-5754 (Meltdown):
  - Not affected
- CVE-2018-3639 (Speculative Store Bypass):
  - Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2017-5753 (Spectre variant 1):
  - Mitigation: usercopy/swapgs

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Inspur NF5280M6 (Intel Xeon Silver 4309Y)

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CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

SPECrater®2017_fp_base = 163
SPECrater®2017_fp_peak = 164

Test Date: Oct-2021
Hardware Availability: May-2021
Software Availability: Dec-2020

Platform Notes (Continued)

barriers and __user pointer sanitation
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling):
CVE-2019-11135 (TSX Asynchronous Abort):

run-level 3 Oct 9 10:39

SPEC is set to: /home/CPU2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 1.5T 106G 1.3T 8% /home

From /sys/devices/virtual/dmi/id
Vendor: Inspur
Product: NF5280M6
Product Family: Family
Serial: 380251214

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
32x Micron 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200, configured at 2666

BIOS:
BIOS Vendor: American Megatrends Inc.
BIOS Version: 05.00.02
BIOS Date: 05/22/2021
BIOS Revision: 5.22

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C                   | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
| 544.nab_r(base, peak)
==============================================================================

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
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SPEC CPU®2017 Floating Point Rate Result

SPECrater®2017_fp_base = 163
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CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Compiler Version Notes (Continued)

C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++ | 511.povray_r(peak)

Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++, C | 511.povray_r(base) 526.blender_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
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C++, C | 511.povray_r(peak)

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C++, C | 511.povray_r(base) 526.blender_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
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Compiler Version Notes (Continued)

(Continued on next page)
## Compiler Version Notes (Continued)

Version 2021.1 Build 20201113
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```
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
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Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
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```
Fortran, C      | 521.wrf_r(peak)
```

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
Fortran, C      | 521.wrf_r(base) 527.cam4_r(base, peak)
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Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
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CPU2017 License: 3358
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Test Date: Oct-2021
Hardware Availability: May-2021
Software Availability: Dec-2020

Compiler Version Notes (Continued)

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Fortran, C      | 521.wrf_r(peak)
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Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
  Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
  64, Version 2021.1 Build 20201112_000000
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
==============================================================================

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort
Inspur Corporation
Inspur NF5280M6 (Intel Xeon Silver 4309Y)

SPEC CPU®2017 Floating Point Rate Result
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SPECrates®2017_fp_base = 163
SPECrates®2017_fp_peak = 164

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,-muldefs -xCORE-AVX512 -Ofast -ffast-math
-ftlo -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
-w -m64 -Wl,-z,-muldefs -xCORE-AVX512 -Ofast -ffast-math -ftlo
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
-w -m64 -Wl,-z,-muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,-muldefs -xCORE-AVX512 -Ofast -ffast-math
-ftlo -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

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SPEC CPU®2017 Floating Point Rate Result

Inspur Corporation

Inspur NF5280M6 (Intel Xeon Silver 4309Y)

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base</th>
<th>SPECrate®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>163</td>
<td>164</td>
</tr>
</tbody>
</table>

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Test Date: Oct-2021
Hardware Availability: May-2021
Tested by: Inspur Corporation
Software Availability: Dec-2020

Base Optimization Flags (Continued)

Benchmarks using both C and C++:
- w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- mbranches-within-32B-boundaries -ljemalloc
- L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:
- w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3
- no-prec-div -qopt-prefetch -ffinite-math-only
- qopt-multiple-gather-scatter-by-shuffles
- mbranches-within-32B-boundaries -nostandard-realloc-lhs
- align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
521.wrf_r: ifort icc
527.cam4_r: ifort icx

Benchmarks using both C and C++:
511.povray_r: icpc icc
526.blender_r: icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort
Inspur Corporation

Inspur NF5280M6 (Intel Xeon Silver 4309Y)

SPECCPU®2017 Floating Point Rate Result

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>3358</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Inspur Corporation</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Inspur Corporation</td>
</tr>
</tbody>
</table>

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SPECrate®2017_fp_base = 163
SPECrate®2017_fp_peak = 164

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes
538.imagick_r: basepeak = yes
544.nab_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-Ofast -qopt-mem-layout-trans=4
-flto -mbranchswithin-32B-boundaries
-l/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

508.namd_r: basepeak = yes
510.parest_r: -w -m64 -Wl,-z,muldefs
-xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranchswithin-32B-boundaries
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -mbranchswithin-32B-boundaries
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

549.fotonik3d_r: basepeak = yes
554.roms_r: Same as 503.bwaves_r

Benchmarks using both Fortran and C:

521.wrf_r: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3
-ipo -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -mbranchswithin-32B-boundaries

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Inspur Corporation

Inspur NF5280M6 (Intel Xeon Silver 4309Y)

SPECrate®2017_fp_base = 163
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CPU2017 License: 3358
Test Sponsor: Inspur Corporation
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Peak Optimization Flags (Continued)

521.wrf_r (continued):
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3
-lipo -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.1.xml

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Tested with SPEC CPU®2017 v1.1.8 on 2021-10-09 17:02:38-0400.
Report generated on 2021-11-10 10:08:02 by CPU2017 PDF formatter v6442.
Originally published on 2021-11-09.