Lenovo Global Technology
ThinkSystem ST650 V2
(2.30 GHz, Intel Xeon Silver 4310T)

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base</th>
<th>161</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Sep-2021  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2020

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base (161)</th>
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<tr>
<td>503.bwaves_r</td>
</tr>
<tr>
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<tr>
<td>508.namd_r</td>
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<tr>
<td>510.parest_r</td>
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<tr>
<td>511.povray_r</td>
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<tr>
<td>519.lbm_r</td>
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<tr>
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<tr>
<td>549.fotonik3d_r</td>
</tr>
<tr>
<td>554.roms_r</td>
</tr>
</tbody>
</table>

**Hardware**

| CPU Name: | Intel Xeon Silver 4310T |
| Max MHz: | 3400 |
| Nominal: | 2300 |
| Enabled: | 20 cores, 2 chips |
| Orderable: | 1.2 chips |
| Cache L1: | 32 KB I + 48 KB D on chip per core |
| L2: | 1.25 MB I+D on chip per core |
| L3: | 15 MB I+D on chip per chip |
| Other: | None |
| Memory: | 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R, running at 2666) |
| Storage: | 1 x 960 GB SATA SSD |
| Other: | None |

**Software**

| OS: | Red Hat Enterprise Linux 8.3 (Ootpa) |
| Kernel 4.18.0-240.el8.x86_64 |
| Compiler: | C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux |
| Parallel: | No |
| Firmware: | Lenovo BIOS Version U8E113E 1.10 released Aug-2021 |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | Not Applicable |
| Other: | jemalloc memory allocator V5.0.1 |
| Power Management: | BIOS and OS set to prefer performance at the cost of additional power usage |
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>20</td>
<td>482</td>
<td>416</td>
<td>483</td>
<td>415</td>
<td>482</td>
<td>416</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>20</td>
<td>115</td>
<td>221</td>
<td>115</td>
<td>220</td>
<td>115</td>
<td>220</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>20</td>
<td>180</td>
<td>106</td>
<td>186</td>
<td>102</td>
<td>179</td>
<td>106</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>20</td>
<td>525</td>
<td>99.6</td>
<td>525</td>
<td>99.7</td>
<td>525</td>
<td>99.7</td>
<td></td>
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<td></td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>20</td>
<td>296</td>
<td>158</td>
<td>293</td>
<td>159</td>
<td>294</td>
<td>159</td>
<td></td>
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</tr>
<tr>
<td>519.lbm_r</td>
<td>20</td>
<td>154</td>
<td>137</td>
<td>153</td>
<td>137</td>
<td>153</td>
<td>138</td>
<td></td>
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<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>20</td>
<td>308</td>
<td>145</td>
<td>309</td>
<td>145</td>
<td>307</td>
<td>146</td>
<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>20</td>
<td>251</td>
<td>122</td>
<td>250</td>
<td>122</td>
<td>251</td>
<td>121</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>20</td>
<td>249</td>
<td>140</td>
<td>249</td>
<td>140</td>
<td>250</td>
<td>140</td>
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<td></td>
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<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>20</td>
<td>136</td>
<td>365</td>
<td>138</td>
<td>360</td>
<td>140</td>
<td>356</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>20</td>
<td>167</td>
<td>202</td>
<td>167</td>
<td>201</td>
<td>168</td>
<td>201</td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
</tr>
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<td>20</td>
<td>540</td>
<td>144</td>
<td>540</td>
<td>144</td>
<td>538</td>
<td>145</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>20</td>
<td>361</td>
<td>88.1</td>
<td>361</td>
<td>88.1</td>
<td>360</td>
<td>88.3</td>
<td></td>
<td></td>
<td></td>
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SPECrate®2017_fp_base = 161  
SPECrate®2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = 
"/home/cpu2017-1.1.8-ic2021.1-revB/lib/intel64:/home/cpu2017-1.1.8-ic202
1.1-revB/j5.0.1-64"

MALLOCC_CONF = "retain:true"

General Notes
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default

(Continued on next page)
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SPECrater®2017_fp_base = 161
SPECrater®2017_fp_peak = Not Run

General Notes (Continued)

Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
    numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
Hyper-Threading set to Disabled

Sysinfo program /home/cpu2017-1.1.8-ic2021.1-revB/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost.localdomain Fri Sep 10 22:00:59 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Silver 4310T CPU @ 2.30GHz
    2 "physical id"s (chips)
    20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 10
    physical 0: cores 0 1 2 3 4 5 6 7 8 9
    physical 1: cores 0 1 2 3 4 5 6 7 8 9

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 20
On-line CPU(s) list: 0-19

(Continued on next page)
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SPECrate®2017_fp_base = 161
SPECrate®2017_fp_peak = Not Run

Platform Notes (Continued)

Thread(s) per core: 1
Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Silver 4310T CPU @ 2.30GHz
Stepping: 6
CPU MHz: 2900.000
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 15360K
NUMA node0 CPU(s): 0-9
NUMA node1 CPU(s): 10-19
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmerf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_pni
ssbd mba ibpb ibrs ibrs Enhanced tp_r_shadow vnmi flexpriority ept vpid ept_ad
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw
avx512vl xsavesopt xsaves xsaveopt xsaves xsaves cqm_llc cqm_occup llc cqm_mbb-total
cqm_mbb_local split_lock_detect wbnoinvd dtherm ida arat pln pts avx512vbmi umip pk
ospe avx512_vmbi2 gfnl vaes vpc1mulqdq avx512_vnni avx512_bitalg tme
avx512_vpoptcntdq la57 rpdlid md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data
cache size : 15360 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9
node 0 size: 509431 MB
node 0 free: 514958 MB
node 1 cpus: 10 11 12 13 14 15 16 17 18 19
node 1 size: 509658 MB
node 1 free: 515527 MB
node distances:
node 0 1
0: 10 20

(Continued on next page)
Lenovo Global Technology

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CPU2017 License: 9017
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Platform Notes (Continued)

1: 20 10

From /proc/meminfo
MemTotal: 1056498256 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: balanced

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass):
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):
CVE-2019-11135 (TSX Asynchronous Abort):

run-level 3 Sep 10 21:59

(Continued on next page)
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SPECrate®2017_fp_base = 161
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Platform Notes (Continued)

SPEC is set to: /home/cpu2017-1.1.8-ic2021.1-revB
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   818G  108G  710G  14% /home

From /sys/devices/virtual/dmi/id
Vendor:         Lenovo
Product:        ThinkSystem ST650V2
Product Family: ThinkSystem
Serial:         1234567890

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
32x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200, configured at 2666

BIOS:
BIOS Vendor:     Lenovo
BIOS Version:    U8E113E-1.10
BIOS Date:       08/31/2021
BIOS Revision:   1.10
Firmware Revision: 1.41

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C               | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++             | 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++, C          | 511.povray_r(base) 526.blender_r(base)
(Continued on next page)
SPEC CPU®2017 Floating Point Rate Result

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Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

(Continued on next page)
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Base Compiler Invocation (Continued)

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-fIto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)
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C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-F.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-F.xml
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
## SPEC CPU® 2017 Floating Point Rate Result

**Lenovo Global Technology**

**ThinkSystem ST650 V2**

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<tbody>
<tr>
<td>Test Sponsor: Lenovo Global Technology</td>
<td>Hardware Availability: Jul-2021</td>
</tr>
<tr>
<td>Tested by: Lenovo Global Technology</td>
<td>Software Availability: Dec-2020</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base =</th>
<th>161</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak =</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-09-10 10:00:58-0400.  
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