Dell Inc.  
PowerEdge R650 (Intel Xeon Platinum 8362, 2.80 GHz)  

<table>
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<tr>
<th>SPECspeed®2017_fp_base = 220</th>
<th>SPECspeed®2017_fp_peak = 224</th>
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**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Jul-2021  
**Hardware Availability:** Sep-2021  
**Software Availability:** Mar-2021  

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<td>70.0</td>
</tr>
<tr>
<td>64</td>
<td>716</td>
<td>718</td>
</tr>
</tbody>
</table>

**Hardware**  
**CPU Name:** Intel Xeon Platinum 8362  
**Max MHz:** 3600  
**Nominal:** 2800  
**Enabled:** 64 cores, 2 chips  
**Orderable:** 1.2 chips  
**Cache L1:** 32 KB I + 48 KB D on chip per core  
**L2:** 1.25 MB I+D on chip per core  
**L3:** 48 MB I+D on chip per chip  
**Other:** None  
**Memory:** 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)  
**Storage:** 125 GB on tmpfs  
**Other:** None  

**Software**  
**OS:** Red Hat Enterprise Linux 8.3 (Ootpa)  
**Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
**Firmware:** Version 1.2.4 released May-2021  
**File System:** tmpfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 64-bit  
**Other:** Jemalloc memory allocator V5.0.1  
**Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
# SPEC CPU®2017 Floating Point Speed Result

**Dell Inc.**

PowerEdge R650 (Intel Xeon Platinum 8362, 2.80 GHz)

CPU2017 License: 55  
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<td>603.bwaves_s</td>
<td>64</td>
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<td>717</td>
<td>82.4</td>
<td>716</td>
<td>64</td>
<td>718</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>64</td>
<td>60.7</td>
<td>274</td>
<td>61.0</td>
<td>273</td>
<td>64</td>
<td>61.0</td>
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<td>619.lbm_s</td>
<td>64</td>
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<td>133</td>
<td>37.5</td>
<td>140</td>
<td>64</td>
<td>37.5</td>
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<td>621.wrf_s</td>
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<td>219</td>
<td>60.2</td>
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<td>627.cam4_s</td>
<td>64</td>
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<td>52.9</td>
<td>167</td>
<td>64</td>
<td>52.9</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>64</td>
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<td>91.9</td>
<td>131</td>
<td>90.8</td>
<td>64</td>
<td>131</td>
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<tr>
<td>638.imagick_s</td>
<td>64</td>
<td>65.4</td>
<td>221</td>
<td>65.5</td>
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<td>64</td>
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<td>649.fotonik3d_s</td>
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<td>113</td>
<td>64</td>
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<tr>
<td>654.roms_s</td>
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<td>56.2</td>
<td>280</td>
<td>57.0</td>
<td>276</td>
<td>64</td>
<td>57.0</td>
</tr>
</tbody>
</table>

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/jemalloc5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

- sync; echo 3 > /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:
- Logical Processor: Disabled
- Virtualization Technology: Disabled
- System Profile: Custom
- CPU Power Management: Maximum Performance
- C1E: Disabled
- C States: Autonomous
- Memory Patrol Scrub: Disabled
- Energy Efficiency Policy: Performance
- CPU Interconnect Bus Link Power Management: Disabled
- PCI ASPM L1 Link Power Management: Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2021.1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acac64d
running on rhel-8-3-amd Fri Jul 2 14:18:36 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz
- 2 "physical id"s (chips)
- 64 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
- cpu cores: 32
- siblings: 32
- physical 0: cores 0 1 2 3 4 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
- physical 1: cores 0 1 2 3 4 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
Dell Inc.
PowerEdge R650 (Intel Xeon Platinum 8362, 2.80 GHz)

**SPEC CPU®2017 Floating Point Speed Result**

**CPU2017 License:** 55  
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---

**SPECspeed®2017_fp_base = 220**  
**SPECspeed®2017_fp_peak = 224**

---

**Platform Notes (Continued)**

From lscpu from util-linux 2.32.1:

- **Architecture:** x86_64
- **CPU op-mode(s):** 32-bit, 64-bit
- **Byte Order:** Little Endian
- **CPU(s):** 64
- **On-line CPU(s) list:** 0-63
- **Thread(s) per core:** 1
- **Core(s) per socket:** 32
- **Socket(s):** 2
- **NUMA node(s):** 2
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 106
- **Model name:** Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz
- **Stepping:** 6
- **CPU MHz:** 785.714
- **BogoMIPS:** 5600.00
- **Virtualization:** VT-x
- **L1d cache:** 48K
- **L1i cache:** 32K
- **L2 cache:** 1280K
- **L3 cache:** 49152K

NUMA node0 CPU(s):
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62

NUMA node1 CPU(s):
1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63

**Flags:**
- fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced fsbgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512sd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cmq_llc cmq_occ_up_llc cmq_mbb_total cmq_mbb_local split_lock_detect wbnoinvd dtherm ida arat pln pts avx512vmbw upi pk uoske avx512_vmbi gfni vaes vclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_lid arch_capabilities

/proc/cpuinfo cache data
- cache size: 49152 KB

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

(Continued on next page)
Dell Inc.
PowerEdge R650 (Intel Xeon Platinum 8362, 2.80 GHz)

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CPU2017 License: 55  
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Test Date: Jul-2021  
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---

Platform Notes (Continued)

available: 2 nodes (0-1)  
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62  
node 0 size: 243727 MB  
node 0 free: 253468 MB  
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51 53 55 57 59 61 63  
node 1 size: 245466 MB  
node 1 free: 245105 MB  
node distances:  
node 0 1  
0: 10 20  
1: 20 10  

From /proc/meminfo  
MemTotal: 527808732 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB

/sbin/tuned-adm active  
Current active profile: throughput-performance

From /etc/*release* /etc/*version*  
os-release:  
NAME="Red Hat Enterprise Linux"  
VERSION="8.3 (Ootpa)"  
ID="rhel"  
ID_LIKE="fedora"  
VERSION_ID="8.3"  
PLATFORM_ID="platform:el8"  
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"  
ANSI_COLOR="0;31"  
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)  
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:  
Linux rhel-8-3-amd 4.18.0-240.22.1.el8_3.x86_64 #1 SMP Thu Mar 25 14:36:04 EDT 2021  
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected  
CVE-2018-3620 (L1 Terminal Fault): Not affected  
Microarchitectural Data Sampling: Not affected  
CVE-2017-5754 (Meltdown): Not affected  
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store

(Continued on next page)
## Dell Inc. PowerEdge R650 (Intel Xeon Platinum 8362, 2.80 GHz)

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### Platform Notes (Continued)

- **CVE-2017-5753 (Spectre variant 1):**
  - Bypass disabled via prctl and seccomp
  - Mitigation: usercopy/swaps barriers and __user pointer sanitization

- **CVE-2017-5715 (Spectre variant 2):**
  - Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

- **CVE-2020-0543 (Special Register Buffer Data Sampling):**
  - Not affected

- **CVE-2019-11135 (TSX Asynchronous Abort):**
  - Not affected

### Additional Information

**Memory:**
- 16x 00AD069D00AD HMAA4GR7A8R8N-XN 32 GB 2 rank 3200
- 16x Not Specified Not Specified

**BIOS:**
- BIOS Vendor: Dell Inc.
- BIOS Version: 1.2.4
- BIOS Date: 05/28/2021
- BIOS Revision: 1.2

(End of data from sysinfo program)

### Compiler Version Notes

C 619.1bm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000

(Continued on next page)
Dell Inc.
PowerEdge R650 (Intel Xeon Platinum 8362, 2.80 GHz)

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Test Date: Jul-2021
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Compiler Version Notes (Continued)

Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.

------------------------------------------------------------------------
C               | 644.nab_s(peak)
------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)
------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64,
Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------
C               | 644.nab_s(peak)
------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64,
Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on

(Continued on next page)
Dell Inc.
PowerEdge R650 (Intel Xeon Platinum 8362, 2.80 GHz)

SPECspeed®2017_fp_base = 220
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Test Sponsor: Dell Inc.
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Compiler Version Notes (Continued)

Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran, C
621.wrf_s(base, peak) 627.cam4_s(base, peak)
628.pop2_s(base, peak)

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactusBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume bytectorl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
**SPEC CPU®2017 Floating Point Speed Result**

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**SPECspeed®2017_fp_base = 220**  
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## Base Optimization Flags

**C benchmarks:**  
- `m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP -mbranches-within-32B-boundaries`

**Fortran benchmarks:**  
- `m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -gopenmp -nostandard-realloc-lhs -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

**Benchmarks using both Fortran and C:**  

**Benchmarks using Fortran, C, and C++:**  

## Peak Compiler Invocation

**C benchmarks (except as noted below):**  
- `icc`
- `644.nab_s: icx`

**Fortran benchmarks:**  
- `ifort`

**Benchmarks using both Fortran and C:**  
- `ifort icc`

**Benchmarks using Fortran, C, and C++:**  
- `icpc icc ifort`
**SPEC CPU®2017 Floating Point Speed Result**

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PowerEdge R650 (Intel Xeon Platinum 8362, 2.80 GHz)  

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**Software Availability:** Mar-2021

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**Peak Portability Flags**

Same as Base Portability Flags

---

**Peak Optimization Flags**

C benchmarks:

619.lbm_s: basepeak = yes
638.imagick_s: basepeak = yes


Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

649.fotonik3d_s: Same as 603.bwaves_s
654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes
627.cam4_s: basepeak = yes
628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes
### SPEC CPU®2017 Floating Point Speed Result

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The flags files that were used to format this result can be browsed at:
- [Intel ic2021 official linux64 revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)
- [Dell Platform Flags PowerEdge Intel ICX rev1.4.html](http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.4.html)

You can also download the XML flags sources by saving the following links:
- [Intel ic2021 official linux64 revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)
- [Dell Platform Flags PowerEdge Intel ICX rev1.4.xml](http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.4.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-07-02 15:18:35-0400.  
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