Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Gold 6314U, 2.30GHz)

| SPECspeed®2017_int_base = 11.5 | SPECspeed®2017_int_peak = 11.7 |

CPU2017 License: 9019  
Test Sponsor: Cisco Systems  
Tested by: Cisco Systems

Test Date: Sep-2021  
Hardware Availability: Apr-2021  
Software Availability: Dec-2020

Threads

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base (11.5)</th>
<th>SPECspeed®2017_int_peak (11.7)</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s 32</td>
<td></td>
</tr>
<tr>
<td>602.gcc_s 32</td>
<td></td>
</tr>
<tr>
<td>605.mcf_s 32</td>
<td></td>
</tr>
<tr>
<td>620.omnetpp_s 32</td>
<td></td>
</tr>
<tr>
<td>623.xalancbmk_s 32</td>
<td></td>
</tr>
<tr>
<td>625.x264_s 32</td>
<td></td>
</tr>
<tr>
<td>631.deepsjeng_s 32</td>
<td></td>
</tr>
<tr>
<td>641.leela_s 32</td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s 32</td>
<td></td>
</tr>
<tr>
<td>657.xz_s 32</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>CPU Name: Intel Xeon Gold 6314U</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz: 3400</td>
</tr>
<tr>
<td>Nominal: 2300</td>
</tr>
<tr>
<td>Enabled: 32 cores, 1 chip</td>
</tr>
<tr>
<td>Orderable: 1 Chip</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2: 1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3: 48 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
<tr>
<td>Memory: 512 GB (8 x 64 GB 2Rx4 PC4-3200V-R)</td>
</tr>
<tr>
<td>Storage: 1 x 240 GB M.2 SSD SATA</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>OS: SUSE Linux Enterprise Server 15 SP2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel: Yes</td>
</tr>
<tr>
<td>Firmware: Version 4.2.1d released Jul-2021</td>
</tr>
<tr>
<td>File System: btrfs</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Peak Pointers: 64-bit</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management: BIOS and OS set to prefer performance at the cost of additional power usage</td>
</tr>
</tbody>
</table>
Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Gold 6314U, 2.30GHz)

SPEC®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Sep-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>32</td>
<td>255</td>
<td>6.96</td>
<td>254</td>
<td>7.00</td>
<td>253</td>
<td>7.00</td>
<td>32</td>
<td>222</td>
<td>8.00</td>
<td>222</td>
<td>7.99</td>
<td>222</td>
<td>8.01</td>
<td></td>
<td></td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>32</td>
<td>374</td>
<td>10.7</td>
<td>374</td>
<td>10.7</td>
<td>374</td>
<td>10.6</td>
<td>32</td>
<td>360</td>
<td>11.1</td>
<td>360</td>
<td>11.1</td>
<td>360</td>
<td>11.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>32</td>
<td>240</td>
<td>19.7</td>
<td>240</td>
<td>19.7</td>
<td>240</td>
<td>19.6</td>
<td>32</td>
<td>240</td>
<td>19.7</td>
<td>240</td>
<td>19.7</td>
<td>240</td>
<td>19.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>32</td>
<td>149</td>
<td>10.9</td>
<td>147</td>
<td>11.1</td>
<td>148</td>
<td>11.0</td>
<td>32</td>
<td>149</td>
<td>10.9</td>
<td>147</td>
<td>11.1</td>
<td>148</td>
<td>11.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>625.x264_s</td>
<td>32</td>
<td>105</td>
<td>16.7</td>
<td>105</td>
<td>16.7</td>
<td>106</td>
<td>16.7</td>
<td>32</td>
<td>101</td>
<td>17.4</td>
<td>101</td>
<td>17.4</td>
<td>101</td>
<td>17.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>32</td>
<td>248</td>
<td>5.78</td>
<td>248</td>
<td>5.77</td>
<td>248</td>
<td>5.78</td>
<td>32</td>
<td>248</td>
<td>5.78</td>
<td>248</td>
<td>5.77</td>
<td>248</td>
<td>5.78</td>
<td></td>
<td></td>
</tr>
<tr>
<td>641.leela_s</td>
<td>32</td>
<td>359</td>
<td>4.75</td>
<td>359</td>
<td>4.75</td>
<td>359</td>
<td>4.75</td>
<td>32</td>
<td>359</td>
<td>4.75</td>
<td>359</td>
<td>4.75</td>
<td>359</td>
<td>4.75</td>
<td></td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td>156</td>
<td>18.8</td>
<td>156</td>
<td>18.8</td>
<td>157</td>
<td>18.7</td>
<td>32</td>
<td>156</td>
<td>18.8</td>
<td>156</td>
<td>18.8</td>
<td>157</td>
<td>18.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>32</td>
<td>286</td>
<td>21.6</td>
<td>286</td>
<td>21.6</td>
<td>287</td>
<td>21.6</td>
<td>32</td>
<td>286</td>
<td>21.6</td>
<td>286</td>
<td>21.6</td>
<td>287</td>
<td>21.6</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7940X CPU + 64GB RAM
memory using openSUSE Leap 15.2
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
General Notes (Continued)

numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
Intel Hyper-Threading Technology set to Disabled
DCU Streamer Prefetch set to Disabled
LLC Dead Line set to Disabled
Memory Refresh Rate set to 1x Refresh
ADDDC Sparing set to Disabled
Patrol Scrub set to Disabled
Energy Efficient Turbo set to Enabled
Processor C6 Report set to Enabled
Processor C1E set to Enabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaf64d
running on localhost Wed Sep  8 14:25:20 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6314U CPU @ 2.30GHz
  1 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 32
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
  25 26 27 28 29 30 31

From lscpu from util-linux 2.33.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
Platform Notes (Continued)

Byte Order: Little Endian
Address sizes: 46 bits physical, 57 bits virtual
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 1
Core(s) per socket: 32
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6314U CPU @ 2.30GHz
Stepping: 6
CPU MHz: 3150.074
CPU max MHz: 3400.000
CPU min MHz: 800.000
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 49152K
NUMA node0 CPU(s): 0-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acp1 mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc copuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 invpcid_single ssbd mba ibrs ibpb ibrs_enhanced tpr_shadow vmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_hir avx512bw avx512vl xsaveopt xsavec xsavec xsavegs cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_vbitalg tme avx512_vpopcntdq 1a57 rdpid md_clear pconfig flush_lid arch_capabilities

/cache/cpuinfo cache data
cache size: 49152 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 1 nodes (0)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
    node 0 size: 515105 MB

(Continued on next page)
Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Gold 6314U, 2.30GHz)

SPECspeed®2017_int_base = 11.5
SPECspeed®2017_int_peak = 11.7

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Test Date: Sep-2021
Tested by: Cisco Systems
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Platform Notes (Continued)

node 0 free: 514589 MB
node distances:
node 0
0: 10

From /proc/meminfo
MemTotal: 527468196 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
NAME="SLES"
VERSION="15-SP2"
VERSION_ID="15.2"
PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp2"

uname -a:
Linux localhost 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2): Not affected
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Sep 8 14:24

(Continued on next page)
Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Gold 6314U, 2.30GHz)

SPECspeed\textsuperscript{\textregistered}2017\_int\_base = 11.5
SPECspeed\textsuperscript{\textregistered}2017\_int\_peak = 11.7

Copyright 2017-2021 Standard Performance Evaluation Corporation

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Sep-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Platform Notes (Continued)

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 btrfs 222G 9.1G 212G 5% /home

From /sys/devices/virtual/dmi/id
Vendor: Cisco Systems Inc
Product: UCSB-B200-M6
Serial: FCH24097576

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
8x 0xCE00 M393A8G40AB2-CWE 64 GB 2 rank 3200
24x NO DIMM NO DIMM

BIOS:
BIOS Vendor: Cisco Systems, Inc.
BIOS Version: B200M6.4.2.id.0.0730210924
BIOS Date: 07/30/2021
BIOS Revision: 5.22

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
<table>
<thead>
<tr>
<th>600.perlbench_s(peak)</th>
</tr>
</thead>
</table>
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
|  600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak) |
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
|  600.perlbench_s(peak) |
Page 6

## SPEC CPU® 2017 Integer Speed Result

**Cisco Systems**  
Cisco UCS B200 M6 (Intel Xeon Gold 6314U, 2.30GHz)

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>Test Date:</th>
<th>SPECspeed®2017_int_base</th>
<th>Hardware Availability:</th>
<th>Test Sponsor:</th>
</tr>
</thead>
<tbody>
<tr>
<td>9019</td>
<td>Sep-2021</td>
<td>11.5</td>
<td>Apr-2021</td>
<td>Cisco Systems</td>
</tr>
</tbody>
</table>

**SPECspeed®2017_int_peak = 11.7**

**Tested by:** Cisco Systems  
**Software Availability:** Dec-2020

### Compiler Version Notes (Continued)

---

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

<table>
<thead>
<tr>
<th>C</th>
<th>600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)</th>
</tr>
</thead>
</table>

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

<table>
<thead>
<tr>
<th>C++</th>
<th>620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)</th>
</tr>
</thead>
</table>

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

<table>
<thead>
<tr>
<th>Fortran</th>
<th>648.exchange2_s(base, peak)</th>
</tr>
</thead>
</table>

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

### Base Compiler Invocation

**C benchmarks:**
`icx`

**C++ benchmarks:**
`icpx`

**Fortran benchmarks:**
`ifort`
## SPEC CPU®2017 Integer Speed Result

**Cisco Systems**
Cisco UCS B200 M6 (Intel Xeon Gold 6314U, 2.30GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>11.5</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>11.7</td>
</tr>
</tbody>
</table>

CPU2017 License: 9019  
Test Sponsor: Cisco Systems  
Tested by: Cisco Systems  
Test Date: Sep-2021  
Hardware Availability: Apr-2021  
Software Availability: Dec-2020

### Base Portability Flags

- 600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
- 602.gcc_s: -DSPEC_LP64
- 605.mcf_s: -DSPEC_LP64
- 620.omnetpp_s: -DSPEC_LP64
- 623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
- -DSPEC_OPENMP  
  -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512  
  -O3 -ffast-math -flto -mfpmath=sse -funroll-loops  
  -qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries  
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**C++ benchmarks:**
- -DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math  
  -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
  -mbranches-within-32B-boundaries  
  -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/  
  -lqkmalloc

**Fortran benchmarks:**
- -m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4  
  -nostandard-realloc-lhs -align array32byte -auto  
  -mbranches-within-32B-boundaries

### Peak Compiler Invocation

**C benchmarks (except as noted below):**
- icx  
  600.perlbench_s: icc

**C++ benchmarks:**
- icpx

(Continued on next page)
Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Gold 6314U, 2.30GHz)

SPEC CPU®2017 Integer Speed Result

SPECspeed®2017_int_base = 11.5
SPECspeed®2017_int_peak = 11.7

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Sep-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
600.perlb benchmark_s: -Wl,-z, muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z, muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z, muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:
620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes

(Continued on next page)
Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Gold 6314U, 2.30GHz)

**SPEC CPU**

**2017 Integer Speed Result**

Copyright 2017-2021 Standard Performance Evaluation Corporation

Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Gold 6314U, 2.30GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 11.5</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak = 11.7</td>
</tr>
</tbody>
</table>

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Sep-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

**Peak Optimization Flags (Continued)**

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-09-08 17:25:19-0400.
Report generated on 2021-09-29 12:24:49 by CPU2017 PDF formatter v6442.
Originally published on 2021-09-28.