**SPEC CPU®2017 Integer Rate Result**

Tyrone Systems  
(Test Sponsor: Netweb Pte Ltd)  
Tyrone Camarero IDI100C2R-28  
(2.90 GHz, Intel Xeon Gold 6326)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base = 247</th>
<th>SPECrate®2017_int_peak = 255</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2017 License: 006042</td>
<td>Test Date: Aug-2021</td>
</tr>
<tr>
<td>Tested by: Tyrone Systems</td>
<td>Hardware Availability: Apr-2021</td>
</tr>
</tbody>
</table>

### Copies

<table>
<thead>
<tr>
<th>Copy</th>
<th>64</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>169</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>193</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>224</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td></td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>400</td>
</tr>
<tr>
<td>525.x264_r</td>
<td></td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>197</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>149</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>139</td>
</tr>
</tbody>
</table>

### Hardware

**CPU Name:** Intel Xeon Gold 6326  
**Max MHz:** 3500  
**Nominal:** 2900  
**Enabled:** 32 cores, 2 chips, 2 threads/core  
**Orderable:** 1.2 Chips  
**Cache L1:** 32 KB I + 48 KB D on chip per core  
**L2:** 1.25 MB I+D on chip per core  
**L3:** 24 MB I+D on chip per chip  
**Memory:** 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R)  
**Storage:** 1 x 250 GB SATA SSD  
**Other:** None  

### Software

**OS:** CentOS Linux release 8.4.2105  
**Kernel:** 4.18.0-305.3.1.el8.x86_64  
**Compiler:**  
C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux  
**Parallel:** No  
**Firmware:** Version SE5C620.86B.01.01.0003.2104260124 released Apr-2021  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 32/64-bit  
**Other:** Jemalloc memory allocator V5.0.1  
**Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
## SPEC CPU®2017 Integer Rate Result

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds Base</th>
<th>Ratio Base</th>
<th>Seconds Peak</th>
<th>Ratio Peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>64</td>
<td>603</td>
<td>169</td>
<td>604</td>
<td>169</td>
</tr>
<tr>
<td></td>
<td></td>
<td>605</td>
<td>168</td>
<td>605</td>
<td>168</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>64</td>
<td>473</td>
<td>192</td>
<td>469</td>
<td>193</td>
</tr>
<tr>
<td></td>
<td></td>
<td>470</td>
<td>193</td>
<td>470</td>
<td>193</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>64</td>
<td>259</td>
<td>400</td>
<td>258</td>
<td>400</td>
</tr>
<tr>
<td></td>
<td></td>
<td>255</td>
<td>401</td>
<td>258</td>
<td>400</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>64</td>
<td>565</td>
<td>149</td>
<td>563</td>
<td>149</td>
</tr>
<tr>
<td></td>
<td></td>
<td>563</td>
<td>149</td>
<td>563</td>
<td>149</td>
</tr>
<tr>
<td>523.xalanbmk_r</td>
<td>64</td>
<td>227</td>
<td>298</td>
<td>227</td>
<td>298</td>
</tr>
<tr>
<td></td>
<td></td>
<td>227</td>
<td>300</td>
<td>227</td>
<td>298</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>64</td>
<td>217</td>
<td>517</td>
<td>217</td>
<td>517</td>
</tr>
<tr>
<td></td>
<td></td>
<td>219</td>
<td>512</td>
<td>219</td>
<td>512</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>64</td>
<td>373</td>
<td>197</td>
<td>372</td>
<td>197</td>
</tr>
<tr>
<td></td>
<td></td>
<td>373</td>
<td>197</td>
<td>373</td>
<td>197</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>64</td>
<td>554</td>
<td>191</td>
<td>551</td>
<td>192</td>
</tr>
<tr>
<td></td>
<td></td>
<td>553</td>
<td>192</td>
<td>553</td>
<td>192</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>64</td>
<td>317</td>
<td>529</td>
<td>316</td>
<td>531</td>
</tr>
<tr>
<td></td>
<td></td>
<td>317</td>
<td>530</td>
<td>317</td>
<td>530</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>64</td>
<td>494</td>
<td>140</td>
<td>497</td>
<td>139</td>
</tr>
<tr>
<td></td>
<td></td>
<td>497</td>
<td>139</td>
<td>497</td>
<td>139</td>
</tr>
</tbody>
</table>

**Specrate®2017_int_base = 247**

**Specrate®2017_int_peak = 255**

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```bash
LD_LIBRARY_PATH = 
"/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

### General Notes

Binaries compiled locally by Netweb
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
```bash
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
```

(Continued on next page)
### General Notes (Continued)

`numactl --interleave=all runcpu <etc>`

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

### Platform Notes

**BIOS Settings:**
- Power Technology set to Custom
- Power Performance Tuning set to BIOS Controls EPB
- ENERGY_PERF_BIAS_CFG mode set to Performance
- LLC Dead Line Alloc set to Disable

**Sysinfo program** /home/cpu2017/bin/sysinfo
Revisions: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaf64d
running on localhost.localdomain Thu Aug 26 07:19:40 2021

**SUT (System Under Test) info as seen by some common utilities.**
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From `/proc/cpuinfo`

- `model name : Intel(R) Xeon(R) Gold 6326 CPU @ 2.90GHz`
- `2 "physical id"s (chips)`
- `64 "processors"`

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from `/proc/cpuinfo might not be reliable. Use with caution.``
- `cpu cores : 16`
- `siblings : 32`
- `physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15`
- `physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15`

From `lscpu` from `util-linux 2.32.1:`
- `Architecture: x86_64`
- `CPU op-mode(s):` 32-bit, 64-bit
- `Byte Order: Little Endian`
- `CPU(s):` 64
- `On-line CPU(s) list: 0-63`
- `Thread(s) per core: 2`

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero ID1100C2R-28
(2.90 GHz, Intel Xeon Gold 6326)

Copyright 2017-2021 Standard Performance Evaluation Corporation

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

SPECrate®2017_int_base = 247
SPECrate®2017_int_peak = 255

Test Date: Aug-2021
Hardware Availability: Apr-2021
Software Availability: Jun-2021

Platform Notes (Continued)

Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6326 CPU @ 2.90GHz
BIOS Model name: Intel(R) Xeon(R) Gold 6326 CPU @ 2.90GHz
Stepping: 6
CPU MHz: 1376.346
CPU max MHz: 3500.0000
CPU min MHz: 800.0000
BogoMIPS: 5800.00
Virtualization: VT-x
L1d cache: 48K
L1l cache: 32K
L2 cache: 1280K
L3 cache: 24576K
NUMA node0 CPU(s): 0-7, 32-39
NUMA node1 CPU(s): 8-15, 40-47
NUMA node2 CPU(s): 16-23, 48-55
NUMA node3 CPU(s): 24-31, 56-63
Flags: fpu vme de pse mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aerperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrnr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popbe pmste tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_1 invpcid_single ssbd
mka ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms
invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsave xgetbv1 xsaves cqm_llc
ckmg_occup_llc cqm_mbb_total cqm_mbb_local split_lock_detect wbinvd dtherm ida
arat pln pts hwp hwp_act_window hwp_epp hwp_pkreq avx512bwvimi umip pku ospke
avx512_vbmi2 gfnv vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq
la57 rdpid fsrmd md_clear pconfig flush_l1d arch_capabilities

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 32 33 34 35 36 37 38 39
node 0 size: 128611 MB
node 0 free: 127906 MB
node 1 cpus: 8 9 10 11 12 13 14 15 40 41 42 43 44 45 46 47

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero ID1100C2R-28
(2.90 GHz, Intel Xeon Gold 6326)

SPECrate®2017_int_base = 247
SPECrate®2017_int_peak = 255

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Test Date: Aug-2021
Hardware Availability: Apr-2021
Software Availability: Jun-2021

Platform Notes (Continued)

node 1 size: 129020 MB
node 1 free: 128698 MB
node 2 cpus: 16 17 18 19 20 21 22 23 48 49 50 51 52 53 54 55
node 2 size: 129020 MB
node 2 free: 128695 MB
node 3 cpus: 24 25 26 27 28 29 30 31 56 57 58 59 60 61 62 63
node 3 size: 129017 MB
node 3 free: 128751 MB
node distances:

node 0 1 2 3
0:  10  11  20  20
1:  11  10  20  20
2:  20  20  10  11
3:  20  20  11  10

From /proc/meminfo
MemTotal:       528046460 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
centos-release: CentOS Linux release 8.4.2105
centos-release-upstream: Derived from Red Hat Enterprise Linux 8.4
os-release:
   NAME="CentOS Linux"
   VERSION="8"
   ID="centos"
   ID_LIKE="rhel fedora"
   VERSION_ID="8"
   PLATFORM_ID="platform:el8"
   PRETTY_NAME="CentOS Linux 8"
   ANSI_COLOR="0;31"
redhat-release: CentOS Linux release 8.4.2105
system-release: CentOS Linux release 8.4.2105
system-release-cpe: cpe:/o:centos:centos:8

uname -a:
Linux localhost.localdomain 4.18.0-305.3.1.el8.x86_64 #1 SMP Tue Jun 1 16:14:33 UTC 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero ID1100C2R-28
(2.90 GHz, Intel Xeon Gold 6326)

SPECrate®2017_int_base = 247
SPECrate®2017_int_peak = 255

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Test Date: Aug-2021
Hardware Availability: Apr-2021
Software Availability: Jun-2021

Platform Notes (Continued)

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store
CVE-2018-3639 (Speculative Store Bypass): Bypass disabled via prctl and
CVE-2017-5753 (Spectre variant 1): seccomp
CVE-2017-5715 (Spectre variant 2): Mitigation: usercopy/swapgs
CVE-2020-0543 (Special Register Buffer Data Sampling): barriers and __user pointer
CVE-2019-11135 (TSX Asynchronous Abort): sanitization
Not affected

run-level 3 Aug 26 07:18
SPEC is set to: /home/cpu2017
From /sys/devices/virtual/dmi/id
    Vendor: Intel Corporation
    Product: WHITLEY
    Product Family: Family
    Serial: UNKNOWN

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you
interpret this section. The 'dmidecode' program reads system data which is "intended to
allow hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
    16x Micron 18ASF4G72PZ-3G2E1 32 GB 1 rank 3200

BIOS:
    BIOS Vendor: Intel Corporation
    BIOS Version: SE5C620.86B.01.01.0003.2104260124
    BIOS Date: 04/26/2021

(End of data from sysinfo program)

Compiler Version Notes

C | 500.perlbench_r(peak) 557.xz_r(peak)

(Continued on next page)
### Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>C</th>
<th>500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>C</th>
<th>500.perlbench_r(peak) 557.xz_r(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>C</th>
<th>502.gcc_r(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

### SPEC CPU®2017 Integer Rate Result

**SPECrate®2017_int_base = 247**

**SPECrate®2017_int_peak = 255**
## Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>C</th>
<th>500.perlbench_r(peak) 557.xz_r(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
</tr>
<tr>
<td></td>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>---------</td>
<td>-------------------------------------</td>
</tr>
<tr>
<td>C</td>
<td>502.gcc_r(peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113</td>
</tr>
<tr>
<td></td>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>---------</td>
<td>-------------------------------------</td>
</tr>
<tr>
<td>C</td>
<td>500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113</td>
</tr>
<tr>
<td></td>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>---------</td>
<td>-------------------------------------</td>
</tr>
<tr>
<td>C++</td>
<td>520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113</td>
</tr>
<tr>
<td></td>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>---------</td>
<td>-------------------------------------</td>
</tr>
<tr>
<td>Fortran</td>
<td>548.exchange2_r(base, peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
</tr>
<tr>
<td></td>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>
SPECCPU®2017 Integer Rate Result
Copyright 2017-2021 Standard Performance Evaluation Corporation

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero IDI100C2R-28
(2.90 GHz, Intel Xeon Gold 6326)

SPECrate®2017_int_base = 247
SPECrate®2017_int_peak = 255

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Test Date: Aug-2021
Hardware Availability: Apr-2021
Software Availability: Jun-2021

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

(Continued on next page)

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-fflt -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero IDI100C2R-28
(2.90 GHz, Intel Xeon Gold 6326)

SPECrate®2017_int_base = 247
SPECrate®2017_int_peak = 255

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Test Date: Aug-2021
Hardware Availability: Apr-2021
Software Availability: Jun-2021

Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icx
500.perlbench_r: icc
557.xz_r: icc

C++ benchmarks:
icpx

Peak Portability Flags

C benchmarks:
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-fopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries

(Continued on next page)
Peak Optimization Flags (Continued)

500.perlbench_r (continued):
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-O3 -ffast-math -qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revI.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revI.xml
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero IDI100C2R-28
(2.90 GHz, Intel Xeon Gold 6326)

SPECrate®2017_int_base = 247
SPECrate®2017_int_peak = 255

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Test Date: Aug-2021
Hardware Availability: Apr-2021
Software Availability: Jun-2021

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-08-26 07:19:39-0400.
Originally published on 2021-09-28.