



# SPEC CPU®2017 Floating Point Speed Result

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## Fujitsu

PRIMERGY CX2550 M6, Intel Xeon Gold 5318Y,  
2.10GHz

SPECspeed®2017\_fp\_base = 171

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 19

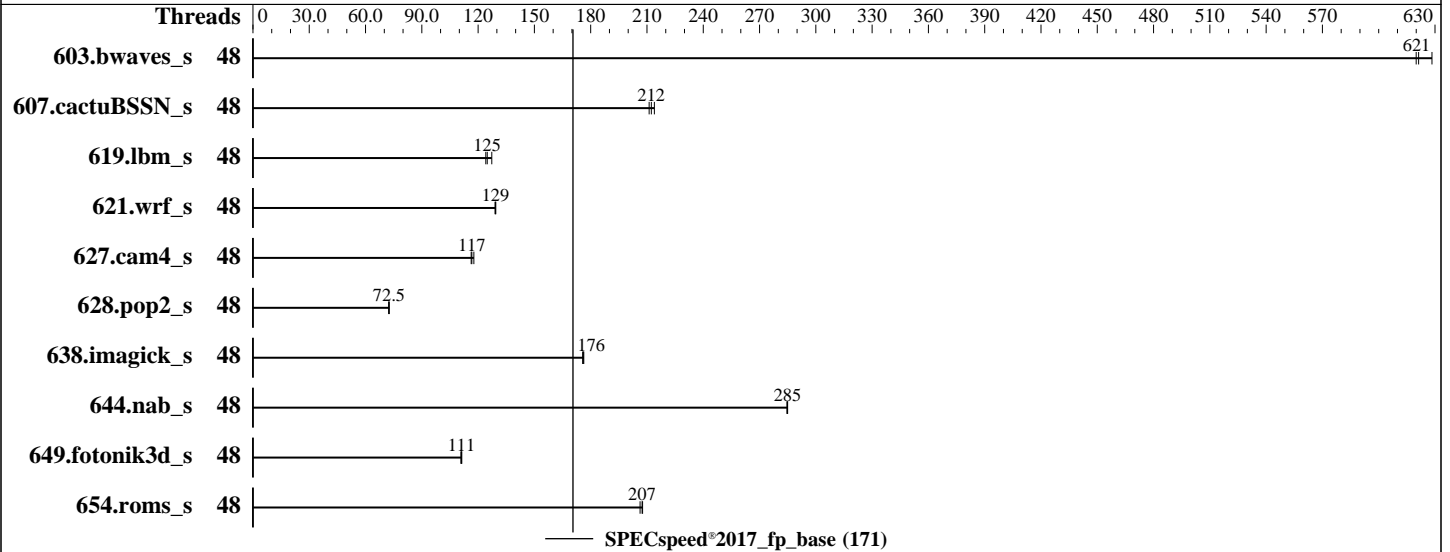
Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Aug-2021

Hardware Availability: Oct-2021

Software Availability: Dec-2020



### Hardware

CPU Name: Intel Xeon Gold 5318Y  
 Max MHz: 3400  
 Nominal: 2100  
 Enabled: 48 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1.25 MB I+D on chip per core  
 L3: 36 MB I+D on chip per chip  
 Other: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R, running at 2933)  
 Storage: 1 x SATA M.2 SSD, 480GB  
 Other: None

### Software

OS: Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86\_64  
 Compiler: Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
 C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux  
 Parallel: Yes  
 Firmware: Fujitsu BIOS Version V1.0.0.0 R1.23.0 for D3893-A1x. Released Oct-2021  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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## Results Table

| Benchmark       | Base    |             |            |             |             |             |            | Peak    |         |       |         |       |         |       |
|-----------------|---------|-------------|------------|-------------|-------------|-------------|------------|---------|---------|-------|---------|-------|---------|-------|
|                 | Threads | Seconds     | Ratio      | Seconds     | Ratio       | Seconds     | Ratio      | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 603.bwaves_s    | 48      | 93.9        | 628        | <u>95.0</u> | <u>621</u>  | 95.2        | 620        |         |         |       |         |       |         |       |
| 607.cactuBSSN_s | 48      | <u>78.5</u> | <u>212</u> | 77.9        | 214         | 78.9        | 211        |         |         |       |         |       |         |       |
| 619.lbm_s       | 48      | 42.2        | 124        | <u>41.9</u> | <u>125</u>  | 41.1        | 127        |         |         |       |         |       |         |       |
| 621.wrf_s       | 48      | 103         | 129        | 102         | 129         | <u>102</u>  | <u>129</u> |         |         |       |         |       |         |       |
| 627.cam4_s      | 48      | <u>76.0</u> | <u>117</u> | 76.2        | 116         | 75.3        | 118        |         |         |       |         |       |         |       |
| 628.pop2_s      | 48      | 164         | 72.6       | <u>164</u>  | <u>72.5</u> | 164         | 72.3       |         |         |       |         |       |         |       |
| 638.imagick_s   | 48      | 81.8        | 176        | <u>81.9</u> | <u>176</u>  | 82.1        | 176        |         |         |       |         |       |         |       |
| 644.nab_s       | 48      | 61.4        | 285        | <u>61.3</u> | <u>285</u>  | 61.3        | 285        |         |         |       |         |       |         |       |
| 649.fotonik3d_s | 48      | 81.9        | 111        | 82.3        | 111         | <u>82.0</u> | <u>111</u> |         |         |       |         |       |         |       |
| 654.roms_s      | 48      | 76.3        | 206        | <u>75.9</u> | <u>207</u>  | 75.8        | 208        |         |         |       |         |       |         |       |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH =
"/home/Benchmark/SPECCPU2021.1-a_1/lib/intel64:/home/Benchmark/SPECCPU20
21.1-a_1/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

```

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

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### General Notes (Continued)

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS configuration:  
Hyper Threading = Disabled

Sysinfo program /home/Benchmark/SPECCPU2021.1-a\_1/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d  
running on localhost.localdomain Mon Aug 30 15:15:54 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz
 2 "physical id"s (chips)
48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 24
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
```

```
From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 1
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz
Stepping: 6
CPU MHz: 2827.293
CPU max MHz: 3400.0000
CPU min MHz: 800.0000
```

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### Platform Notes (Continued)

BogoMIPS: 4200.00  
Virtualization: VT-x  
L1d cache: 48K  
L1i cache: 32K  
L2 cache: 1280K  
L3 cache: 36864K  
NUMA node0 CPU(s): 0-23  
NUMA node1 CPU(s): 24-47

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp  
lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc cpuid  
aperfmpperf pni pclmulqdq dtes64 monitor ds\_cpl vmx smx est tm2 ssse3 sdbg fma cx16  
xtpr pdcm pcid dca sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes xsave  
avx f16c rdrand lahf\_lm abm 3dnowprefetch cpuid\_fault epb cat\_l3 invpcid\_single ssbd  
mba ibrs ibpb stibp ibrs\_enhanced tpr\_shadow vnmi flexpriority ept vpid fsgsbase  
tsc\_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt\_a avx512f avx512dq  
rdseed adx smap avx512ifma clflushopt clwb intel\_pt avx512cd sha\_ni avx512bw  
avx512vl xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total  
cqm\_mbm\_local wbnoinvd dtherm ida arat pln pts hwp hwp\_act\_window hwp\_epp  
hwp\_pkg\_req avx512vbmi umip pku ospke avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni  
avx512\_bitalg tme avx512\_vpopcntdq la57 rdpid md\_clear pconfig flush\_l1d  
arch\_capabilities

```
/proc/cpuinfo cache data
cache size : 36864 KB
```

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
node 0 size: 257463 MB
node 0 free: 256650 MB
node 1 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
node 1 size: 258012 MB
node 1 free: 257686 MB
node distances:
node  0  1
  0:  10  20
  1:  20  10
```

From /proc/meminfo

```
MemTotal: 527847928 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/sbin/tuned-adm active
Current active profile: throughput-performance
```

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### Platform Notes (Continued)

/sys/devices/system/cpu/cpu\*/cpufreq/scaling\_governor has performance

From /etc/\*release\* /etc/\*version\*

os-release:

```
NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR="0;31"
```

redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:8.2:ga

uname -a:

```
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported
CVE-2019-11135 (TSX Asynchronous Abort): Not affected
```

run-level 3 Aug 30 15:12

```
SPEC is set to: /home/Benchmark/SPECCPU2021.1-a_1
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda5 xfs 350G 33G 318G 10% /home
```

From /sys/devices/virtual/dmi/id  
Vendor: FUJITSU

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### Platform Notes (Continued)

Product: PRIMERGY CX2550 M6  
Product Family: SERVER  
Serial: A000000028

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
16x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200, configured at 2933

BIOS:  
BIOS Vendor: FUJITSU  
BIOS Version: V1.0.0.0 R1.23.0 for D3893-A1x  
BIOS Date: 07/29/2021  
BIOS Revision: 1.23  
Firmware Revision: 3.23

(End of data from sysinfo program)

### Compiler Version Notes

=====  
C | 619.lbm\_s(base) 638.imagick\_s(base) 644.nab\_s(base)

-----  
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base)

-----  
Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====  
Fortran | 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base)

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Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====

-----  
Fortran, C | 621.wrf\_s(base) 627.cam4\_s(base) 628.pop2\_s(base)  
-----  
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:  
icc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
ifort icc

Benchmarks using Fortran, C, and C++:  
icpc icc ifort

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
627.cam4\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
628.pop2\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
-assume byterecl  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64

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## Base Portability Flags (Continued)

654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-m64 -std=c11 -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP  
-mbranches-within-32B-boundaries
```

Fortran benchmarks:

```
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp  
-nostandard-realloc-lhs -mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp  
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp  
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-ICL-RevA.html>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-ICL-RevA.xml>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)

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