**Supermicro**

SuperServer SYS-620P-TRT  
(X12DPI-NT6, Intel Xeon Gold 6336Y)

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**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

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**copies**

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base = 350</th>
</tr>
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<tbody>
<tr>
<td>SPECrate®2017_int_peak = Not Run</td>
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<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
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<tbody>
<tr>
<td>CPU Name: Intel Xeon Gold 6336Y</td>
<td>OS: Red Hat Enterprise Linux release 8.3 (Ootpa) 4.18.0-240.el8.x86_64</td>
</tr>
<tr>
<td>Max MHz: 3600</td>
<td>Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;</td>
</tr>
<tr>
<td>Nominal: 2400</td>
<td>Firmware: version 1.1b released Jul-2021</td>
</tr>
<tr>
<td>Enabled: 48 cores, 2 chips, 2 threads/core</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>Orderable: 1.2 chips</td>
<td>System State: Run level 3 (Multi-user)</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 48 KB D on chip per core</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>L2: 1.25 MB I+D on chip per core</td>
<td>Peak Pointers: Not Applicable</td>
</tr>
<tr>
<td>L3: 36 MB I+D on chip per chip</td>
<td>Other: None</td>
</tr>
<tr>
<td>Other: None</td>
<td>Power Management: BIOS set to prefer performance at the cost of additional power usage</td>
</tr>
<tr>
<td>Memory: 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)</td>
<td></td>
</tr>
<tr>
<td>Storage: 1.92 TB SATA SSD</td>
<td></td>
</tr>
</tbody>
</table>

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**Supermicro**

---

**Test Date:** Aug-2021  
**Hardware Availability:** May-2021  
**Software Availability:** Dec-2020

---

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_int_base (350)</th>
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<tbody>
<tr>
<td>500.perlbench_r 96</td>
<td>242</td>
</tr>
<tr>
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<td>282</td>
</tr>
<tr>
<td>505.mcf_r 96</td>
<td>578</td>
</tr>
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<td>417</td>
</tr>
<tr>
<td>523.xalancbmk_r 96</td>
<td>725</td>
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<tr>
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</tr>
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<td>557.xz_r 96</td>
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SPEC CPU®2017 Integer Rate Result

Supermicro
SuperServer SYS-620P-TRT
(X12DPI-NT6, Intel Xeon Gold 6336Y)

SPECrate®2017_int_base = 350
SPECrate®2017_int_peak = Not Run

Results Table

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<tr>
<th>Benchmark</th>
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</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/lib/intel64:/home/lib/ia32:/home/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.3
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
   sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
   numactl --interleave=all runcpu <etc>

(Continued on next page)
Supermicro
SuperServer SYS-620P-TRT
(X12DPI-NT6, Intel Xeon Gold 6336Y)

SPECrate®2017_int_base = 350
SPECrate®2017_int_peak = Not Run

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
Energy Performance BIAS Setting = Maximum Performance
SNC = Enable
Stale Atos = Disable
ADDDC Sparing = Disable
Patrol Scrub = Disable

Sysinfo program /home/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aef64d

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6336Y CPU @ 2.40GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 24
  siblings : 48
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2

(Continued on next page)
<table>
<thead>
<tr>
<th>Specification</th>
<th>Result</th>
</tr>
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<tbody>
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<td></td>
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<td>X12DPi-NT6, Intel Xeon Gold 6336Y</td>
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**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Test Date:** Aug-2021  
**Hardware Availability:** May-2021  
**Tested by:** Supermicro  
**Software Availability:** Dec-2020

**Platform Notes (Continued)**

NUMA node(s): 4  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 106  
Model name: Intel(R) Xeon(R) Gold 6336Y CPU @ 2.40GHz  
Stepping: 6  
CPU MHz: 3000.591  
CPU max MHz: 3600.0000  
CPU min MHz: 800.0000  
BogoMIPS: 4800.00  
Virtualization: VT-x  
L1d cache: 48K  
L1i cache: 32K  
L2 cache: 1280K  
L3 cache: 36864K  
NUMA node0 CPU(s): 0-11,48-59  
NUMA node1 CPU(s): 12-23,60-71  
NUMA node2 CPU(s): 24-35,72-83  
NUMA node3 CPU(s): 36-47,84-95  
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpica mmxfxsr sse sse2 ss ht tm pbe syscall nx pdp64 ldtsc lp mem constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpl pgo bit7_czoom aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abal Olm ab xsavecfg xsaves xbcrd xsegment xsaveopt xsaves xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveopt xsaveo

/proc/cpuinfo cache data  
  cache size : 36864 KB  

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59
  node 0 size: 125439 MB
  node 0 free: 127611 MB
  node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59
  node 1 size: 126116 MB
  node 1 free: 128577 MB
  node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71
  node 2 size: 126020 MB

(Continued on next page)
Platform Notes (Continued)

node 2 free: 128714 MB
node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 84 85 86 87 88 89 90 91 92 93 94 95
node 3 size: 126227 MB
node 3 free: 128702 MB
node distances:
  node 0   1   2   3
  0:  10  11  20  20
  1:  11  10  20  20
  2:  20  20  10  11
  3:  20  20  11  10

From /proc/meminfo
  MemTotal:       527735284 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.3 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.3"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
    ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
  Linux 147-163.pnet 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020 x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store

(Continued on next page)
Supermicro
SuperServer SYS-620P-TRT
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SPECRATE®2017_int_base = 350
SPECRATE®2017_int_peak = Not Run

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Platform Notes (Continued)

Bypass disabled via prctl and seccomp
Mitigation: usercopy/swapsgs barriers and __user pointer sanitization
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
Not affected
Not affected

run-level 3 Aug 25 23:35

SPEC is set to: /home
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 1.7T 177G 1.5T 11% /home

From /sys/devices/virtual/dmi/id
Vendor: Supermicro
Product: X12DPi-N(T)6
Product Family: SMC X12
Serial: 123456789

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x Hynix HMA84GR7CJR4N-XN 32 GB 2 rank 3200
2x NO DIMM NO DIMM

BIOS:
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 1.1b
BIOS Date: 07/20/2021
BIOS Revision: 5.22

(End of data from sysinfo program)

Compiler Version Notes

===================================
C 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)
   525.x264_r(base) 557.xz_r(base)
===================================

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113

(Continued on next page)
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Test Date: Aug-2021
Hardware Availability: May-2021
Software Availability: Dec-2020

Compiler Version Notes (Continued)
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------------------------
C++
520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)
-----------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------------------------
Fortran
548.exchange2_r(base)
-----------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------------------------

Base Compiler Invocation
C benchmarks:
icx
C++ benchmarks:
icpx
Fortran benchmarks:
ifort

Base Portability Flags
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
SUPERMICO

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Base Optimization Flags

C benchmarks:
- -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
- -lqkmalloc

C++ benchmarks:
- -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
- -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
- -lqkmalloc

Fortran benchmarks:
- -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
- -qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
- -auto -mbranches-within-32B-boundaries
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
- -lqkmalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-CLX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-CLX-revH.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-08-25 23:38:10-0400.
Report generated on 2021-09-14 19:18:46 by CPU2017 PDF formatter v6442.
Originally published on 2021-09-14.