Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero DIT400TR-28RL
(3.0 GHz, Intel Xeon Gold 6248R)

SPECspeed\textsuperscript{\textregistered} 2017\_fp\_base = 146
SPECspeed\textsuperscript{\textregistered} 2017\_fp\_peak = 148

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Table:

<table>
<thead>
<tr>
<th>Thread</th>
<th>SPECspeed\textsuperscript{\textregistered} 2017_fp_base</th>
<th>SPECspeed\textsuperscript{\textregistered} 2017_fp_peak</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>48</td>
<td>175</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>48</td>
<td>175</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>48</td>
<td>175</td>
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<tr>
<td>621.wrf_s</td>
<td>48</td>
<td>175</td>
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<tr>
<td>627.cam4_s</td>
<td>48</td>
<td>175</td>
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<tr>
<td>628.pop2_s</td>
<td>48</td>
<td>175</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>48</td>
<td>175</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>48</td>
<td>175</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>48</td>
<td>175</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>48</td>
<td>175</td>
</tr>
</tbody>
</table>

Hardware:

CPU Name: Intel Xeon Gold 6248R
Max MHz: 4000
Nominal: 3000
Enabled: 48 cores, 2 chips, 2 threads/core
Orderable: 1.2 Chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 35.75 MB I+D on chip per chip
Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2933P-R)
Storage: 1 x 480 GB SATA SSD
Other: None

Software:

OS: CentOS Linux release 8.4.2105
Compiler: C/C++, Version 2021.1 of Intel oneAPI DPC++/C++
Compiler Build 20201113 for Linux;
Fortran: Version 2021.1 of Intel Fortran Compiler
Classic Build 20201112 for Linux;
C/C++: Version 2021.1 of Intel C/C++ Compiler
Classic Build 20201112 for Linux;
Parallel: Yes
Firmware: Version V8.102 released Jun-2020
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage.
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>519</td>
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<td>607.cactuBSSN_s</td>
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<td>619.ibm_s</td>
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<td>53.6</td>
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<td>55.7</td>
<td>94.1</td>
<td>56.3</td>
<td>93.0</td>
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<tr>
<td>621.wrf_s</td>
<td>48</td>
<td>97.0</td>
<td>136</td>
<td>97.1</td>
<td>136</td>
<td>97.0</td>
<td>136</td>
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<tr>
<td>627.cam4_s</td>
<td>48</td>
<td>76.4</td>
<td>116</td>
<td>76.1</td>
<td>117</td>
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<td>116</td>
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<tr>
<td>628.pop2_s</td>
<td>48</td>
<td>177</td>
<td>67.1</td>
<td>176</td>
<td>67.4</td>
<td>177</td>
<td>67.2</td>
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<tr>
<td>638.imagick_s</td>
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<td>115</td>
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<td>115</td>
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<tr>
<td>644.nab_s</td>
<td>48</td>
<td>59.3</td>
<td>294</td>
<td>59.5</td>
<td>293</td>
<td>59.7</td>
<td>293</td>
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<tr>
<td>649.fotonik3d_s</td>
<td>48</td>
<td>104</td>
<td>87.5</td>
<td>106</td>
<td>85.9</td>
<td>106</td>
<td>86.4</td>
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<tr>
<td>654.roms_s</td>
<td>48</td>
<td>94.1</td>
<td>167</td>
<td>94.4</td>
<td>167</td>
<td>93.8</td>
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<tr>
<td>SPECspeed®2017_fp_base = 146</td>
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</tr>
<tr>
<td>SPECspeed®2017_fp_peak = 148</td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain: true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled locally by Netweb
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3 > /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
    built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero DIT400TR-28RL
(3.0 GHz, Intel Xeon Gold 6248R)

SPECspeed®2017_fp_base = 146
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<tr>
<th>CPU2017 License: 006042</th>
<th>Test Date: Jul-2021</th>
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<tbody>
<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Feb-2020</td>
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<tr>
<td>Tested by: Tyrone Systems</td>
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Tyrone Systems
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Tyrone Camarero DIT400TR-28RL
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General Notes (Continued)


Platform Notes

BIOS Settings:
Power Technology set to Custom
Power Performance Tuning set to BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode set to Performance
LLC Dead Line Alloc set to Disable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b5589ef0e16acaf64d
running on spec Wed Jul 28 18:00:48 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6248R CPU @ 3.00GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 6 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6248R CPU @ 3.00GHz
BIOS Model name: Intel(R) Xeon(R) Gold 6248R CPU @ 3.00GHz
Stepping: 7

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CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

CPU MHz: 3466.832
CPU max MHz: 4000.0000
CPU min MHz: 1200.0000
BogoMIPS: 6000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-23,48-71
NUMA node1 CPU(s): 24-47,72-95
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc pae mce cx8 apic sep mtrr pge mca cmov

/platforms/core-0
/meminfo
MemTotal: 394842900 kB
MemFree: 307758872 kB
Buffers: 2041692 kB
Cached: 32202720 kB

From /proc/meminfo

MemTotal: 394842900 kB
MemFree: 307758872 kB
Buffers: 2041692kB
Cached: 32202720 kB
HugePages_Total: 0
HugePages_Free: 0
Hugepagesize: 2048 kB

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

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Platform Notes (Continued)

/sbin/tuned-adm active
  Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
  centos-release: CentOS Linux release 8.4.2105
  centos-release-upstream: Derived from Red Hat Enterprise Linux 8.4
  os-release:
    NAME="CentOS Linux"
    VERSION="8"
    ID="centos"
    ID_LIKE="rhel fedora"
    VERSION_ID="8"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="CentOS Linux 8"
    ANSI_COLOR="0;31"
  redhat-release: CentOS Linux release 8.4.2105
  system-release: CentOS Linux release 8.4.2105
  system-release-cpe: cpe:/o:centos:centos:8

uname -a:
  Linux spec 4.18.0-305.3.1.el8.x86_64 #1 SMP Tue Jun 1 16:14:33 UTC 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):
  KVM: Mitigation: Split huge pages

CVE-2018-3620 (L1 Terminal Fault):
  Not affected

Microarchitectural Data Sampling:
  Not affected

CVE-2017-5754 (Meltdown):
  Not affected

CVE-2018-3639 (Speculative Store Bypass):
  Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2017-5753 (Spectre variant 1):
  Mitigation: usercopy/swapps barriers and __user pointer sanitization

CVE-2017-5715 (Spectre variant 2):
  Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling):
  Not affected

CVE-2019-11135 (TSX Asynchronous Abort):
  Mitigation: TSX disabled

run-level 3 Jul 27 03:19

SPEC is set to: /home/cpu2017

(Continued on next page)
**SPEC CPU®2017 Floating Point Speed Result**

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**Platform Notes (Continued)**

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/mapper/cl-home</td>
<td>xfs</td>
<td>372G</td>
<td>197G</td>
<td>176G</td>
<td>53%</td>
<td>/home</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id
- Vendor: Tyrone Systems
- Product: Tyrone Camarero DIT400TR-28R
- Product Family: empty
- Serial: empty

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**Memory:**
- 12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

**BIOS:**
- BIOS Vendor: American Megatrends Inc.
- BIOS Version: V8.102
- BIOS Date: 06/09/2020
- BIOS Revision: 5.14
- Firmware Revision: 7.0

(End of data from sysinfo program)

---

**Compiler Version Notes**

```
==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
| 644.nab_s(base)
==============================================================================
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
==============================================================================
C               | 644.nab_s(peak)
==============================================================================
Intel(R) oneAPI DPC+/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
```

(Continued on next page)
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CPU2017 License: 006042
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Tested by: Tyrone Systems

Compiler Version Notes (Continued)

| 644.nab_s(base)
------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------

| 644.nab_s(peak)
------------------------------------------------------------------
C Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------

| 607.cactuBSSN_s(base, peak)
------------------------------------------------------------------
C++, C, Fortran Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------

| 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
------------------------------------------------------------------
Fortran Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------

| 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
------------------------------------------------------------------
Fortran, C Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
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------------------------------------------------------------------

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Compiler Version Notes (Continued)
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Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries

Fortran benchmarks:
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3
-no-prec-div -qopt-prefetch -ffinite-math-only

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Base Optimization Flags (Continued)

Fortran benchmarks (continued):
- -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
- -mbranches-within-32B-boundaries -L/usr/local/je5.0.1-64/lib -ljemalloc

Benchmarks using both Fortran and C:
- -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
- -DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
- -L/usr/local/je5.0.1-64/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
- -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
- -DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
- -L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

644.nab_s: icx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

(Continued on next page)
Peak Optimization Flags (Continued)

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -fopenmp
-DSPEC_OPENMP -qopt-mem-layout-trans=4
-fimf-accuracy-bits=14:sqrt
-mbranches-within-32B-boundaries
-L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512
-O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries
-L/usr/local/je5.0.1-64/lib -ljemalloc

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revI.html
<table>
<thead>
<tr>
<th>SPEC CPU®2017 Floating Point Speed Result</th>
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<tbody>
<tr>
<td><strong>Tyrone Systems</strong></td>
</tr>
<tr>
<td>(Test Sponsor: Netweb Pte Ltd)</td>
</tr>
<tr>
<td><strong>Tyrone Camarero DIT400TR-28RL</strong></td>
</tr>
<tr>
<td>(3.0 GHz, Intel Xeon Gold 6248R)</td>
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<td>SPECspeed®2017_fp_peak = 148</td>
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<tr>
<td>Test Date: Jul-2021</td>
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<tr>
<td>Hardware Availability: Feb-2020</td>
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<tr>
<td>Software Availability: Jun-2021</td>
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<td>Test Sponsor: Netweb Pte Ltd</td>
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You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-rev1.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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