Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10 Plus
(2.80 GHz, Intel Xeon Platinum 8362)

SPECspeed®2017_int_base = 12.2
SPECspeed®2017_int_peak = 12.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Aug-2021
Hardware Availability: Jun-2021
Software Availability: Dec-2020

Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspec®2017_int_base</th>
<th>SPECspec®2017_int_peak</th>
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<tr>
<td>perlbench</td>
<td>64</td>
<td>7.39</td>
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<td>gcc</td>
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<td>mcf</td>
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<td>omnetpp</td>
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<tr>
<td>xalancbmk</td>
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<td>13.8</td>
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<tr>
<td>x264</td>
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<tr>
<td>deepsjeng</td>
<td>64</td>
<td>6.08</td>
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<td>leela</td>
<td>64</td>
<td>4.99</td>
<td></td>
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<tr>
<td>exchange2</td>
<td>64</td>
<td>19.9</td>
<td></td>
</tr>
<tr>
<td>xz</td>
<td>64</td>
<td>24.5</td>
<td></td>
</tr>
</tbody>
</table>

Hardware
CPU Name: Intel Xeon Platinum 8362
Max MHz: 3600
Nominal: 2800
Enabled: 64 cores, 2 chips
Orderable: 1, 2 chip(s)
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 48 MB I+D on chip per chip
Other: None
Memory: 2 TB (32 x 64 GB 2Rx4 PC4-3200AA-R)
Storage: 1 x 400 GB SAS SSD, RAID 0
Other: None

Software
OS: Red Hat Enterprise Linux 8.3 (Ootpa)
Kernel 4.18.0-240.el8.x86_64
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
Compiler Build 20201113 for Linux;
Fortran: Version 2021.1 of Intel Fortran Compiler
Classic Build 20201112 for Linux;
C/C++: Version 2021.1 of Intel C/C++ Compiler
Classic Build 20201112 for Linux
Parallel: Yes
Firmware: HPE BIOS Version U46 v1.50 05/27/2021 released May-2021
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage
Hewlett Packard Enterprise  
(Test Sponsor: HPE)  
ProLiant DL380 Gen10 Plus  
(2.80 GHz, Intel Xeon Platinum 8362)

SPECspeed®2017_int_base = 12.2
SPECspeed®2017_int_peak = 12.4

Results Table

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<tr>
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<th>Peak</th>
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<td>105</td>
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<tr>
<td>625.x264_s</td>
<td>64</td>
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<td>17.5</td>
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<td>631.deepsjeng_s</td>
<td>64</td>
<td>236</td>
<td>6.08</td>
<td>235</td>
<td>6.09</td>
<td>236</td>
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<tr>
<td>641.leela_s</td>
<td>64</td>
<td>342</td>
<td>4.99</td>
<td>342</td>
<td>4.99</td>
<td>342</td>
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<tr>
<td>648.exchange2_s</td>
<td>64</td>
<td>148</td>
<td>19.9</td>
<td>148</td>
<td>19.9</td>
<td>148</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>64</td>
<td>251</td>
<td>24.7</td>
<td>252</td>
<td>24.5</td>
<td>252</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3>/proc/sys/vm/drop_caches

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "~/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
SPEC CPU®2017 Integer Speed Result
Copyright 2017-2021 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10 Plus
(2.80 GHz, Intel Xeon Platinum 8362)

SPECspeed®2017_int_base = 12.2
SPECspeed®2017_int_peak = 12.4

General Notes (Continued)


Platform Notes

The system ROM used for this result contains Intel microcode version 0xd0002a0 for
the Intel Xeon Platinum 8362 processor.

BIOS Configuration:
Workload Profile set to General Peak Frequency Compute
Intel Hyper-Threading set to Disabled
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
Advanced Memory Protection set to Advanced ECC
Last Level Cache (LLC) Prefetch set to Enabled
Last Level Cache (LLC) Dead Line Allocation set to Disabled
Enhanced Processor Performance set to Enabled
Workload Profile set to Custom
Energy/Performance Bias set to Balanced Power
DCU Stream Prefetcher set to Disabled
Adjacent Sector Prefetch set to Disabled
Minimum Processor Idle Power Package C-State set to No Package State
Numa Group Size Optimization set to Flat

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost.localdomain Sat Aug 14 05:05:36 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz
  2  "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 32
siblings : 32
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
  25 26 27 28 29 30 31
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
  25 26 27 28 29 30 31

From lscpu from util-linux 2.32.1:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
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SPECspeed®2017_int_base = 12.2
SPECspeed®2017_int_peak = 12.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

<table>
<thead>
<tr>
<th>Platform Notes (Continued)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Byte Order: Little Endian</td>
</tr>
<tr>
<td>CPU(s): 64</td>
</tr>
<tr>
<td>On-line CPU(s) list: 0-63</td>
</tr>
<tr>
<td>Thread(s) per core: 1</td>
</tr>
<tr>
<td>Core(s) per socket: 32</td>
</tr>
<tr>
<td>Socket(s): 2</td>
</tr>
<tr>
<td>NUMA node(s): 2</td>
</tr>
<tr>
<td>Vendor ID: GenuineIntel</td>
</tr>
<tr>
<td>CPU family: 6</td>
</tr>
<tr>
<td>Model: 106</td>
</tr>
<tr>
<td>Model name: Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz</td>
</tr>
<tr>
<td>Stepping: 6</td>
</tr>
<tr>
<td>CPU MHz: 3495.491</td>
</tr>
<tr>
<td>BogoMIPS: 5600.00</td>
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<tr>
<td>Virtualization: VT-x</td>
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<tr>
<td>L1d cache: 48K</td>
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<tr>
<td>L1i cache: 32K</td>
</tr>
<tr>
<td>L2 cache: 1280K</td>
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<tr>
<td>L3 cache: 49152K</td>
</tr>
<tr>
<td>NUMA node0 CPU(s): 0-31</td>
</tr>
<tr>
<td>NUMA nodel CPU(s): 32-63</td>
</tr>
<tr>
<td>Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmid hel avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsave xsetbv1 xsaves cqm llc cqm_occup llc cqm_mbb total cqm_mbb_local split_lock_detect wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku ospte avx512_vbmi2 gfnl vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities</td>
</tr>
</tbody>
</table>

/proc/cpuinfo cache data

cache size : 49152 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 0 size: 975904 MB
node 0 free: 1030964 MB
node 1 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10 Plus
(2.80 GHz, Intel Xeon Platinum 8362)

SPECspeed®2017_int_base = 12.2
SPECspeed®2017_int_peak = 12.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

node 1 size: 971473 MB
node 1 free: 1031587 MB
node distances:
node 0 1
 0: 10 20
 1: 20 10

From /proc/meminfo
  MemTotal: 2113488840 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.3 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.3"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
  Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):
  Not affected
CVE-2018-3620 (L1 Terminal Fault):
  Not affected
Microarchitectural Data Sampling:
  Not affected
CVE-2017-5754 (Meltdown):
  Not affected
CVE-2018-3639 (Speculative Store Bypass):
  Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):
  Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):
  Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

(Continued on next page)
### SPEC CPU®2017 Integer Speed Result

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
ProLiant DL380 Gen10 Plus  
(2.80 GHz, Intel Xeon Platinum 8362)

- **SPECspeed®2017_int_base = 12.2**  
- **SPECspeed®2017_int_peak = 12.4**

<table>
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<tr>
<th>SPEC2017 License: 3</th>
<th>Test Date: Aug-2021</th>
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<tbody>
<tr>
<td>Test Sponsor: HPE</td>
<td>Hardware Availability: Jun-2021</td>
</tr>
<tr>
<td>Tested by: HPE</td>
<td>Software Availability: Dec-2020</td>
</tr>
</tbody>
</table>

#### Platform Notes (Continued)

- CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected  
- CVE-2019-11135 (TSX Asynchronous Abort): Not affected  
- run-level 3 Aug 13 19:32  
- SPEC is set to: /home/cpu2017  
- Filesystem Type Size Used Avail Use% Mounted on  
  /dev/mapper/rhel-home xfs 297G 101G 197G 34% /home  
- From /sys/devices/virtual/dmi/id  
  Vendor: HPE  
  Product: ProLiant DL380 Gen10 Plus  
  Product Family: ProLiant  
  Serial: CN70110BZV  
- Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
  Memory: 32x Micron 36ASF8G72PZ-3G2B2 64 GB 2 rank 3200  
- BIOS:  
  BIOS Vendor: HPE  
  BIOS Version: U46  
  BIOS Date: 05/27/2021  
  BIOS Revision: 1.50  
  Firmware Revision: 2.50  

(End of data from sysinfo program)

#### Compiler Version Notes

```
C          | 600.perlbench_s(peak)
```

---

```
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

---

```
C  | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)  
   | 625.x264_s(base, peak) 657.xz_s(base, peak)
```

---

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
```

(Continued on next page)
# SPEC CPU®2017 Integer Speed Result

## Hewlett Packard Enterprise
*(Test Sponsor: HPE)*

- **ProLiant DL380 Gen10 Plus**
  - **(2.80 GHz, Intel Xeon Platinum 8362)**

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
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**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE  
**Test Date:** Aug-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Dec-2020

### Compiler Version Notes (Continued)

Version 2021.1 Build 20201113  
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<table>
<thead>
<tr>
<th>C</th>
<th>600.perlbench_s(peak)</th>
</tr>
</thead>
<tbody>
<tr>
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Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
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<th>600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)</th>
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
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---

<table>
<thead>
<tr>
<th>C++</th>
<th>620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)</th>
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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<table>
<thead>
<tr>
<th>Fortran</th>
<th>648.exchange2_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
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</tbody>
</table>

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

### Base Compiler Invocation

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpx

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Hewlett Packard Enterprise**
(Test Sponsor: HPE)
ProLiant DL380 Gen10 Plus
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**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

---

**Base Compiler Invocation (Continued)**

Fortran benchmarks:
ifort

---

**Base Portability Flags**

- 600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
- 602.gcc_s: -DSPEC_LP64
- 605.mcf_s: -DSPEC_LP64
- 620.omnetpp_s: -DSPEC_LP64
- 623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

---

**Base Optimization Flags**

**C benchmarks:**
- -DSPEC_OPENMP -std=c11 -m64 -ffopenmp -Wl,-z,muldefs -xCORE-AVX512
- -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
- -qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
- -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**C++ benchmarks:**
- -DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
  -lgmalloc

**Fortran benchmarks:**
- -m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
- -nostandard-realloc-lhs -align array32byte -auto
- -mbranches-within-32B-boundaries

---

**Peak Compiler Invocation**

C benchmarks (except as noted below):
icx

(Continued on next page)
Peak Compiler Invocation (Continued)

600.perlbench_s: icc

C++ benchmarks:
  icpx

Fortran benchmarks:
  ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
  -xCORE-AVX512 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=4 -fno-strict-overflow
  -mbranches-within-32B-boundaries
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
  -fprofile-use=default.profdump(context=pass 2) -xCORE-AVX512 -flto
  -Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
  -mbranches-within-32B-boundaries
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
  -xCORE-AVX512 -flto -O3 -ffast-math
  -qopt-mem-layout-trans=4 -fno-alias
  -mbranches-within-32B-boundaries
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:
SPEC CPU®2017 Integer Speed Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10 Plus
(2.80 GHz, Intel Xeon Platinum 8362)

SPECspeed®2017_int_base = 12.2
SPECspeed®2017_int_peak = 12.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Aug-2021
Hardware Availability: Jun-2021
Software Availability: Dec-2020

Peak Optimization Flags (Continued)

620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes
Fortran benchmarks:
648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revE.xml
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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