**SPEC CPU®2017 Integer Rate Result**

**New H3C Technologies Co., Ltd.**

**H3C UniServer R4900 G3 (Intel Xeon Silver 4208)**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9066</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>New H3C Technologies Co., Ltd.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>New H3C Technologies Co., Ltd.</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Aug-2021</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Jun-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Dec-2020</td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon Silver 4208</td>
</tr>
<tr>
<td>Max MHz:</td>
<td>3200</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2100</td>
</tr>
<tr>
<td>Enabled:</td>
<td>16 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>11 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R, running at 2400)</td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 480GB SATA SSD</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Component</th>
<th>Version/Build</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS:</td>
<td>Red Hat Enterprise Linux release 8.2 (Ootpa)</td>
</tr>
<tr>
<td></td>
<td>4.18.0-193.el8.x86_64</td>
</tr>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 2021.1 of Intel oneAPI DPC++/C++</td>
</tr>
<tr>
<td></td>
<td>Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler</td>
</tr>
<tr>
<td></td>
<td>Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler</td>
</tr>
<tr>
<td></td>
<td>Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Version 2.00.49 released Apr-2021 BIOS</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS set to prefer performance at the cost of additional power usage</td>
</tr>
</tbody>
</table>

### Results

<table>
<thead>
<tr>
<th>Test</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>9066</td>
<td>89.8</td>
<td>92.6</td>
</tr>
</tbody>
</table>

### Benchmarks

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_r</td>
<td>69.9</td>
<td>71.2</td>
</tr>
<tr>
<td>gcc_r</td>
<td>80.5</td>
<td></td>
</tr>
<tr>
<td>mcf_r</td>
<td>61.8</td>
<td></td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>158</td>
<td></td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>120</td>
<td></td>
</tr>
<tr>
<td>x264_r</td>
<td>169</td>
<td></td>
</tr>
<tr>
<td>deepsjeng_r</td>
<td>69.7</td>
<td></td>
</tr>
<tr>
<td>leela_r</td>
<td>65.8</td>
<td></td>
</tr>
<tr>
<td>exchange2_r</td>
<td>163</td>
<td></td>
</tr>
<tr>
<td>xz_r</td>
<td>53.7</td>
<td>54.3</td>
</tr>
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## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>32</td>
<td>848</td>
<td>60.1</td>
<td>845</td>
<td>60.3</td>
<td>850</td>
<td>59.9</td>
<td>32</td>
<td>728</td>
<td>69.9</td>
<td>727</td>
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<tr>
<td>502.gcc_r</td>
<td>32</td>
<td>635</td>
<td>71.4</td>
<td>637</td>
<td>71.2</td>
<td>639</td>
<td>70.9</td>
<td>32</td>
<td>563</td>
<td>80.5</td>
<td>564</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>32</td>
<td>331</td>
<td>156</td>
<td>327</td>
<td>158</td>
<td>326</td>
<td>159</td>
<td>32</td>
<td>331</td>
<td>156</td>
<td>327</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>32</td>
<td>679</td>
<td>61.8</td>
<td>677</td>
<td>62.0</td>
<td>680</td>
<td>61.8</td>
<td>32</td>
<td>679</td>
<td>61.8</td>
<td>677</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>32</td>
<td>281</td>
<td>120</td>
<td>284</td>
<td>119</td>
<td>282</td>
<td>120</td>
<td>32</td>
<td>281</td>
<td>120</td>
<td>284</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>32</td>
<td>333</td>
<td>168</td>
<td>323</td>
<td>173</td>
<td>332</td>
<td>169</td>
<td>32</td>
<td>333</td>
<td>168</td>
<td>321</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>32</td>
<td>526</td>
<td>69.7</td>
<td>526</td>
<td>69.7</td>
<td>525</td>
<td>69.8</td>
<td>32</td>
<td>526</td>
<td>69.7</td>
<td>526</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>32</td>
<td>808</td>
<td>65.6</td>
<td>805</td>
<td>65.8</td>
<td>802</td>
<td>66.1</td>
<td>32</td>
<td>808</td>
<td>65.6</td>
<td>805</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>32</td>
<td>514</td>
<td>163</td>
<td>514</td>
<td>163</td>
<td>513</td>
<td>163</td>
<td>32</td>
<td>514</td>
<td>163</td>
<td>514</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>32</td>
<td>642</td>
<td>53.8</td>
<td>646</td>
<td>53.5</td>
<td>644</td>
<td>53.7</td>
<td>32</td>
<td>638</td>
<td>54.2</td>
<td>636</td>
</tr>
</tbody>
</table>

**SPECrate®2017_int_base = 89.8**

**SPECrate®2017_int_peak = 92.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =
"/home/speccpu/lib/intel64:/home/speccpu/lib/ia32:/home/speccpu/je5.0.1-32"

MALLOC_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Silver 4208)

SPECrate®2017_int_base = 89.8
SPECrate®2017_int_peak = 92.6

CPU2017 License: 9066  
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2021  
Hardware Availability: Jun-2019
Software Availability: Dec-2020

General Notes (Continued)

is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

Set SNC to Enabled
Set IMC Interleaving to 1-way Interleave
Set Patrol Scrub to Disabled
Set XPT Prefetcher to Enabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aaca64d4
running on localhost.localdomain Thu Aug 5 03:21:02 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Silver 4208 CPU @ 2.10GHz
  2  "physical id"s (chips)
  32 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 8
  siblings : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 32
  On-line CPU(s) list: 0-31
  Thread(s) per core: 2
  Core(s) per socket: 8
  Socket(s): 2

(Continued on next page)
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Platform Notes (Continued)

NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4208 CPU @ 2.10GHz
Stepping: 6
CPU MHz: 2499.990
CPU max MHz: 3200.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0-7, 16-23
NUMA node1 CPU(s): 8-15, 24-31
Flags: fpu vme de pse tsc msr pae mca cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref perfmr perfection cpuid pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpmr pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdrand lahf_lm abm xsave cmpGsbase tsc_adjust bmi1 hle avx2 smep bm12 emms invpcid rtm cmx mpx rdt_a
avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaves opt xsave xsavec xsaveopt xgetbv xsavec qmmllc qm_occup醴 qm_mbb_total qm_mbb_local
dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni
md_clear flush_lld arch_capabilities

From /proc/cpuinfo cache data

   cache size : 11264 KB

WARNING: a numacl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 192769 MB
node 0 free: 192224 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 193531 MB
node 1 free: 192523 MB
dnode distances:

0: 10 21
1: 21 10

(Continued on next page)
New H3C Technologies Co., Ltd.

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Platform Notes (Continued)

From /proc/meminfo
MemTotal:       395572564 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga
uname -a:
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):
KVM: Mitigation: Split huge pages
Not affected

CVE-2018-3620 (L1 Terminal Fault):
Not affected

Microarchitectural Data Sampling:
Not affected

CVE-2017-5754 (Meltdown):
Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2018-3639 (Speculative Store Bypass):
Mitigation: usercopy/swapsgs barriers and __user pointer sanitization

CVE-2017-5753 (Spectre variant 1):

CVE-2017-5715 (Spectre variant 2):
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling):
No status reported

CVE-2019-11135 (TSX Asynchronous Abort):
Mitigation: Clear CPU buffers; SMT vulnerable

(Continued on next page)
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Platform Notes (Continued)

SPEC is set to: /home/speccpu
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 392G 94G 298G 25% /home

From /sys/devices/virtual/dmi/id
Vendor: New H3C Technologies Co., Ltd.
Product: UniServer R4900 G3
Product Family: Rack
Serial: 210235A3TKH19B000002

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
12x Hynix HMA84GR7CJR4N-WM 32 GB 2 rank 2933, configured at 2400
12x NO DIMM NO DIMM

BIOS:
BIOS Vendor: American Megatrends Inc.
BIOS Version: 2.00.49
BIOS Date: 04/16/2021
BIOS Revision: 5.14

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C       | 500.perlbench_r(peak) 557.xz_r(peak) |
| Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000 |
| Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

==============================================================================
| C       | 502.gcc_r(peak) |
| Intel(R) oneAPI DPC+/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113 |
| Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

(Continued on next page)
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Compiler Version Notes (Continued)

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<tr>
<th>C</th>
<th>500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base)</th>
</tr>
</thead>
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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<table>
<thead>
<tr>
<th>C</th>
<th>500.perlbench_r(peak) 502.gcc_r(peak) 505.mcf_r(peak, peak) 525.x264_r(peak, peak) 557.xz_r(peak)</th>
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Intel(R) C Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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<table>
<thead>
<tr>
<th>C</th>
<th>502.gcc_r(peak)</th>
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113  
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<table>
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<tr>
<th>C</th>
<th>500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base)</th>
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<table>
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<th>C</th>
<th>500.perlbench_r(peak) 502.gcc_r(peak)</th>
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Intel(R) C Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
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<table>
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113  
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(Continued on next page)
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Compiler Version Notes (Continued)

C benchmarks:
icx
C++ benchmarks:
icpx
Fortran benchmarks:
ifort

Base Compiler Invocation

Base Portability Flags

500.perlbearc_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64

(Continued on next page)
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Base Portability Flags (Continued)

505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-fflat -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icx

500.perlbench_r: icx
557.xz_r: icx

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New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Silver 4208)

SPECrate®2017_int_base = 89.8
SPECrate®2017_int_peak = 92.6

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2021
Hardware Availability: Jun-2019
Software Availability: Dec-2020

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

(Continued on next page)

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-O3 -ffast-math -qopt-mem-layout-trans=4 -fno-alias

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Silver 4208)  

SPECrater®2017_int_base = 89.8  
SPECrater®2017_int_peak = 92.6

CPU2017 License: 9066  
Test Sponsor: New H3C Technologies Co., Ltd.  
Tested by: New H3C Technologies Co., Ltd.  
Test Date: Aug-2021  
Hardware Availability: Jun-2019  
Software Availability: Dec-2020

Peak Optimization Flags (Continued)

525.x264_r(continued):
-mbranches-within-32B-boundaries  
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin  
-lqkmalloc

557.xz_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries  
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin  
-lqkmalloc

C++ benchmarks:
520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

Fortran benchmarks:
548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml  
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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