## SPEC CPU®2017 Floating Point Rate Result

### New H3C Technologies Co., Ltd.

**H3C UniServer R4900 G3 (Intel Xeon Gold 6252)**

![SPECrate®2017_fp_base = 257](image)

### SPECrate®2017_fp_peak = 271

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Test Date:** Aug-2021  
**Hardware Availability:** Jun-2019  
**Software Availability:** Dec-2020

<table>
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<th>Benchmark</th>
<th>Copies</th>
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<th>SPECrate®2017_fp_peak</th>
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<tr>
<td>554.roms_r</td>
<td>48</td>
<td>101</td>
<td>114</td>
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---

### Hardware

- **CPU Name:** Intel Xeon Gold 6252  
- **Max MHz:** 3700  
- **Nominal:** 2100  
- **Enabled:** 48 cores, 2 chips, 2 threads/core  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 35.75 MB I+D on chip per chip  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)  
- **Storage:** 1 x 240GB SATA SSD  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux release 8.2 (Ootpa)  
  4.18.0-193.el8.x86_64  
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++  
  Compiler Build 20201113 for Linux;  
  Fortran: Version 2021.1 of Intel Fortran Compiler  
  Classic Build 20201112 for Linux;  
  C/C++: Version 2021.1 of Intel C/C++ Compiler  
  Classic Build 20201112 for Linux
- **Firmware:** No  
- **Parallel:** Version 2.00.51 released Jul-2021 BIOS  
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6252)

<table>
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<td>101</td>
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<td>101</td>
<td>1513</td>
<td>101</td>
</tr>
</tbody>
</table>

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOC_CONF = "retain:true"

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

(Continued on next page)
General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
umactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS settings:
Set SNC to Enabled
Set IMC Interleaving to 1-way Interleave
Set Patrol Scrub to Disabled
Set XPT Prefetcher to Enabled

Sysinfo program /home/spec/cpu/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca64d4
running on localhost.localdomain Mon Aug 2 16:19:42 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name: Intel(R) Xeon(R) Gold 6252 CPU @ 2.10GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores: 24
siblings: 48
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 6 8 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95

(Continued on next page)
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SPECrate®2017_fp_peak = 271

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Aug-2021
Tested by: New H3C Technologies Co., Ltd.
Hardware Availability: Jun-2019
Software Availability: Dec-2020

Platform Notes (Continued)

Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6252 CPU @ 2.10GHz
Stepping: 6
CPU MHz: 2800.105
CPU max MHz: 3700.0000
CPU min MHz: 1000.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-3,7-9,13-15,19,20,48-51,55-57,61-63,67,68
NUMA node1 CPU(s): 4-6,10-12,16-18,21-23,52-54,58-60,64-66,69-71
NUMA node2 CPU(s): 24-27,31,32,36-38,42-44,72-75,79,80,84-86,90-92
NUMA node3 CPU(s): 28-30,33-35,39-41,45-47,76-78,81-83,87-89,93-95
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdread rdwrite lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single
intel_pcin int13 tsc_terminate tsc_return nonstop_tsc tsc_deadline_timer aes xsave
avx f16c rdseed rdrand rdvnid vpid vmo fma1 fma2 cmov0 cmov1 cmov2 cmov3 cmov4
cmov5 cmov6 cmov7 cmov8 cmov9 cmov10 cmov11 cmov12 cmov13 cmov14 cmov15
cmov16 cmov17 cmov18 cmov19 cmov20 cmov21 cmov22 cmov23 cmov24 cmov25
cmov26 cmov27 cmov28 cmov29 cmov30 cmov31 cmov32 cmov33 cmov34 cmov35
cmov36 cmov37 cmov38 cmov39 cmov40 cmov41 cmov42 cmov43 cmov44 cmov45
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cmov231 cmov232 cmov233 cmov234 cmov235 cmov236 cmov237 cmov238 cmov239
cmov240 cmov241 cmov242 cmov243 cmov244 cmov245 cmov246 cmov247 cmov248
cmov249 cmov250 cmov251 cmov252 cmov253 cmov254 cmov255

/cache data

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 7 8 9 13 14 15 19 20 48 49 50 51 55 56 57 61 62 63 67 68
node 0 size: 95072 MB
node 0 free: 82409 MB
node 1 cpus: 4 5 6 10 11 12 16 17 18 21 22 23 52 53 54 58 59 60 64 65 66 69 70 71
node 1 size: 96735 MB
node 1 free: 86693 MB

(Continued on next page)
New H3C Technologies Co., Ltd.

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CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
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Software Availability: Dec-2020
Hardware Availability: Jun-2019

---

Platform Notes (Continued)

node 2 cpus: 24 25 26 27 31 32 36 37 38 42 43 44 72 73 74 75 79 80 84 85 86 90 91 92
node 2 size: 96762 MB
node 2 free: 85615 MB
node 3 cpus: 28 29 30 33 34 35 39 40 41 45 46 47 76 77 78 81 82 83 87 88 89 93 94 95
node 3 size: 96762 MB
node 3 free: 86739 MB
node distances:
node 0 1 2 3
 0:  10  11  21  21
 1:  11  10  21  21
 2:  21  21  10  11
 3:  21  21  11  10

From /proc/meminfo
MemTotal:       394580660 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
/sbin/tuned-adm active
      Current active profile: throughput-performance
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release*/etc/*version*
  os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.2 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.2"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
      ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
    Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):
    KVM: Mitigation: Split huge pages
CVE-2018-3620 (L1 Terminal Fault):
    Not affected
Microarchitectural Data Sampling:
    Not affected

(Continued on next page)
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**SPEC CPU®2017 Floating Point Rate Result**

**Copyright 2017-2021 Standard Performance Evaluation Corporation**

**SPECrate®2017_fp_base = 257**  
**SPECrate®2017_fp_peak = 271**

---

**Platform Notes (Continued)**

CVE-2017-5754 (Meltdown): Not affected  
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapsgs barriers and __user pointer sanitization

CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported  
CVE-2019-1135 (TSX Asynchronous Abort): Mitigation: Clear CPU buffers; SMT vulnerable

---

**run-level 3 Aug 2 07:02**

**SPEC is set to: /home/speccpu**

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<tr>
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<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
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</thead>
<tbody>
<tr>
<td>/dev/mapper/rhel-home</td>
<td>xfs</td>
<td>168G</td>
<td>81G</td>
<td>88G</td>
<td>48%</td>
<td>/home</td>
</tr>
</tbody>
</table>

From `/sys/devices/virtual/dmi/id`

<table>
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<th>Vendor:</th>
<th>Unis Huashan Technologies Co., Ltd.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Product:</td>
<td>UniServer R4900 G3</td>
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<tr>
<td>Product Family:</td>
<td>Rack</td>
</tr>
<tr>
<td>Serial:</td>
<td>210200A00QH177000025</td>
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</table>

Additional information from `dmidecode` 3.2 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**Memory:**

12x Hynix HMA84GR7CJR4N-WM 32 GB 2 rank 2933
12x NO DIMM NO DIMM

**BIOS:**

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<th>American Megatrends Inc.</th>
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*(End of data from sysinfo program)*

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**Compiler Version Notes**

```
C       519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
```

*(Continued on next page)*
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Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++         | 508.namd_r(base, peak) 510.parest_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++, C      | 511.povray_r(peak)

Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++, C      | 511.povray_r(base) 526.blender_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
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C++, C      | 511.povray_r(peak)

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C++, C      | 511.povray_r(peak)

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(Continued on next page)
### Compiler Version Notes (Continued)

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<th>Compiler</th>
<th>Fortran</th>
<th>C++/C</th>
<th>Fortran/C++</th>
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<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113</td>
<td>Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113</td>
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Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

**Fortran, C**  |  521.wrf_r(peak)

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Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
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Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
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**Fortran, C**  |  521.wrf_r(base) 527.cam4_r(base, peak)

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Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
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### Base Compiler Invocation

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpx

**Fortran benchmarks:**
- ifort

**Benchmarks using both Fortran and C:**
- ifort icx

(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 6252)

SPECrate®2017_fp_base = 257
SPECrate®2017_fp_peak = 271

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Aug-2021
Tested by: New H3C Technologies Co., Ltd.
Hardware Availability: Jun-2019
Software Availability: Dec-2020

Base Compiler Invocation (Continued)

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries -ljemalloc

(Continued on next page)
**SPEC CPU®2017 Floating Point Rate Result**

New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6252)

SPECrater®2017_fp_base = 257  
SPECrater®2017_fp_peak = 271

CPU2017 License: 9066  
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Test Date: Aug-2021  
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**Base Optimization Flags (Continued)**

Fortran benchmarks (continued):
- `-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both Fortran and C:
- `-w` `-m64` `-std=c11` `-Wl,-z,muldefs` `-xCORE-AVX512` `-Ofast` `-ffast-math`  
- `-flto` `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4` `-O3` `-ipo`  
- `-no-prec-div` `-qopt-prefetch` `-ffinite-math-only`  
- `-qopt-multiple-gather-scatter-by-shuffles`  
- `-mbranches-within-32B-boundaries` `-nostandard-realloc-lhs`  
- `-align array32byte` `-auto` `-ljemalloc` `-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both C and C++:
- `-w` `-m64` `-std=c11` `-Wl,-z,muldefs` `-xCORE-AVX512` `-Ofast` `-ffast-math`  
- `-flto` `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4`  
- `-mbranches-within-32B-boundaries` `-ljemalloc`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using Fortran, C, and C++:
- `-w` `-m64` `-std=c11` `-Wl,-z,muldefs` `-xCORE-AVX512` `-Ofast` `-ffast-math`  
- `-flto` `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4` `-O3`  
- `-no-prec-div` `-qopt-prefetch` `-ffinite-math-only`  
- `-qopt-multiple-gather-scatter-by-shuffles`  
- `-mbranches-within-32B-boundaries` `-nostandard-realloc-lhs`  
- `-align array32byte` `-auto` `-ljemalloc` `-L/usr/local/jemalloc64-5.0.1/lib`

**Peak Compiler Invocation**

C benchmarks:
- `icx`

C++ benchmarks:
- `icpx`

Fortran benchmarks:
- `ifort`

Benchmarks using both Fortran and C:
- `521.wrf_r: ifort icc`
- `527.cam4_r: ifort icx`

Benchmarks using both C and C++:

(Continued on next page)
New H3C Technologies Co., Ltd. | SPECrate®2017_fp_base = 257
H3C UniServer R4900 G3 (Intel Xeon Gold 6252) | SPECrate®2017_fp_peak = 271

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2021
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Peak Compiler Invocation (Continued)

511.povray_r: icpc icc
526.blender_r: icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes
538.imagick_r: basepeak = yes
544.nab_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-Ofast -qopt-mem-layout-trans=4
-fimf-accuracy-bits=14:sqrt
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

508.namd_r: basepeak = yes
510.parest_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -mbranches-within-32B-boundaries
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)
New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 6252)

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Peak Optimization Flags (Continued)

549.fotonik3d_r: basepeak = yes

554.roms_r: Same as 503.bwaves_r

Benchmarks using both Fortran and C:

521.wrf_r: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3
-ipo -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3
-ipo -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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