Lenovo Global Technology
ThinkSystem SR630 V2
(2.10 GHz, Intel Xeon Gold 5318Y)

| SPECspeed®2017_fp_base = | 174 |
| SPECspeed®2017_fp_peak = | Not Run |

**CPU2017 License:** 9017  
**Test Date:** Jul-2021  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2020

| Threads | 0  | 30.0 | 60.0 | 90.0 | 120 | 150 | 180 | 210 | 240 | 270 | 300 | 330 | 360 | 390 | 420 | 450 | 480 | 510 | 540 | 570 | 600 | 630 | 660 | 690 | 720 | 750 | 780 | 810 | 840 | 870 | 900 | 930 | 960 | 1090 | 1200 |
|---------|----|------|------|------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 603.bwaves_s | 48 |       |       |       |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 607.cactuBSSN_s | 48 |       |       |       |     | 214 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 619.lbm_s | 48 |       |       |       |     | 131 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 621.wrf_s | 48 |       |       |       |     | 130 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 627.cam4_s | 48 |       |       |       |     | 120 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 628.pop2_s | 48 |       |       |       |     | 73.3 |    |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 638.imagick_s | 48 |       |       |       |     | 180 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 644.nab_s | 48 |       |       |       |     | 284 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 649.fotonik3d_s | 48 |       |       |       |     | 141 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 654.roms_s | 48 |       |       |       |     | 216 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |

**Threads**

| SPECspeed®2017_fp_base (174) |

**Hardware**

<table>
<thead>
<tr>
<th>Field</th>
<th>Value</th>
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<tbody>
<tr>
<td><strong>CPU Name:</strong></td>
<td>Intel Xeon Gold 5318Y</td>
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<tr>
<td><strong>Max MHz:</strong></td>
<td>3400</td>
</tr>
<tr>
<td><strong>Nominal:</strong></td>
<td>2100</td>
</tr>
<tr>
<td><strong>Enabled:</strong></td>
<td>48 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td><strong>Orderable:</strong></td>
<td>1.2 chips</td>
</tr>
<tr>
<td><strong>Cache L1:</strong></td>
<td>32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td><strong>L2:</strong></td>
<td>1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td><strong>L3:</strong></td>
<td>36 MB I+D on chip per chip</td>
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<tr>
<td><strong>Other:</strong></td>
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<tr>
<td><strong>Memory:</strong></td>
<td>1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)</td>
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<tr>
<td><strong>Storage:</strong></td>
<td>1 x 960 GB SATA SSD</td>
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<tr>
<td><strong>Other:</strong></td>
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**Software**

<table>
<thead>
<tr>
<th>Field</th>
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<tr>
<td><strong>OS:</strong></td>
<td>Red Hat Enterprise Linux 8.3 (Ootpa)</td>
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<tr>
<td><strong>Kernel:</strong></td>
<td>4.18.0-240.el8.x86_64</td>
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<td><strong>Compiler:</strong></td>
<td>Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
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<tr>
<td><strong>Parallel:</strong></td>
<td>Yes</td>
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<tr>
<td><strong>Firmware:</strong></td>
<td>Lenovo BIOS Version AFE111A 1.02 released May-2021</td>
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<tr>
<td><strong>File System:</strong></td>
<td>xfs</td>
</tr>
<tr>
<td><strong>System State:</strong></td>
<td>Run level 3 (multi-user)</td>
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<tr>
<td><strong>Base Pointers:</strong></td>
<td>64-bit</td>
</tr>
<tr>
<td><strong>Peak Pointers:</strong></td>
<td>Not Applicable</td>
</tr>
<tr>
<td><strong>Other:</strong></td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td><strong>Power Management:</strong></td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage</td>
</tr>
</tbody>
</table>
**SPEC CPU®2017 Floating Point Speed Result**

**Lenovo Global Technology**
ThinkSystem SR630 V2
(2.10 GHz, Intel Xeon Gold 5318Y)

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>48</td>
<td>92.2</td>
<td>640</td>
<td>92.5</td>
<td>638</td>
<td>91.8</td>
<td>643</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>48</td>
<td>78.4</td>
<td>213</td>
<td>77.5</td>
<td>215</td>
<td>77.8</td>
<td>214</td>
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<tr>
<td>619.lbm_s</td>
<td>48</td>
<td>40.3</td>
<td>130</td>
<td>39.6</td>
<td>132</td>
<td>39.9</td>
<td>131</td>
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<tr>
<td>621.wrf_s</td>
<td>48</td>
<td>103</td>
<td>129</td>
<td>102</td>
<td>130</td>
<td>102</td>
<td>130</td>
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<td>627.cam4_s</td>
<td>48</td>
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<td>120</td>
<td>74.5</td>
<td>119</td>
<td>74.0</td>
<td>120</td>
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<tr>
<td>628.pop2_s</td>
<td>48</td>
<td>162</td>
<td>73.3</td>
<td>163</td>
<td>72.9</td>
<td>162</td>
<td>73.3</td>
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<tr>
<td>638.imagick_s</td>
<td>48</td>
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<td>180</td>
<td>80.0</td>
<td>180</td>
<td>80.9</td>
<td>178</td>
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<tr>
<td>644.nab_s</td>
<td>48</td>
<td>61.5</td>
<td>284</td>
<td>61.4</td>
<td>285</td>
<td>61.4</td>
<td>284</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>48</td>
<td>82.0</td>
<td>111</td>
<td>82.3</td>
<td>111</td>
<td>82.4</td>
<td>111</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>48</td>
<td>72.9</td>
<td>216</td>
<td>73.0</td>
<td>216</td>
<td>73.3</td>
<td>215</td>
</tr>
</tbody>
</table>

**SPECspeed®2017_fp_base = 174**  
**SPECspeed®2017_fp_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,compact,1,0"
- LD_LIBRARY_PATH = "/home/cpu2017-1.1.8-ic2021.1-revB/lib/intel64:/home/cpu2017-1.1.8-ic2021.1-revB/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
- sync; echo 3 > /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
MONITOR/MWAIT set to Enabled
C-States set to Legacy
UPI Prefetcher set to Disabled
LLC Prefetch set to Enable

Sysinfo program /home/cpu2017-1.1.8-ic2021.1-revB/bin/sysinfo
Rev: r6622 of 2021-04-07 9b2a61ec0915b55891ef0e16acac64d
running on localhost.localdomain Thu Jul 8 18:54:16 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name: Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores: 24
siblings: 48
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz

(Continued on next page)
## SPEC CPU®2017 Floating Point Speed Result

**Lenovo Global Technology**

**ThinkSystem SR630 V2**  
(2.10 GHz, Intel Xeon Gold 5318Y)

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<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
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</tbody>
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**SPECSpeed®2017_fp_base** = 174  
**SPECSpeed®2017_fp_peak** = Not Run

<table>
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<tr>
<th>Test Date:</th>
<th>Jul-2021</th>
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<td>Hardware Availability:</td>
<td>Jul-2021</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Dec-2020</td>
</tr>
</tbody>
</table>

### Platform Notes (Continued)

```
Stepping:            6  
CPU MHz:             2048.588  
BogoMIPS:            4200.00  
Virtualization:      VT-x  
L1d cache:           48K  
L1i cache:           32K  
L2 cache:            1280K  
L3 cache:            36864K  
NUMA node0 CPU(s):   0-23,48-71  
NUMA node1 CPU(s):   24-47,72-95  
Flags:               fpu vme de pse tsc msr pae mce c8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp  
                     lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid  
aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 sse3 sdbg fma cx16  
                     xtrr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave  
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single  
                     intel_pni ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmm flexpriority ept  
vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2  
                     erms invpcid cmn rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb  
                     intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsavec xsaveopt xsavevm xsave  
                     xstate xtrunc xsaveopt xsavec xsavevm xsaves cmn_llc cmn_occup_llc cmn_mbb_total  
                     cmn_mbb_local split_lock_detect wbnoinvd dtherm ida arat pin pts avx512vbm umip pku  
                     ospke avx512_vbmi2 gfn vaes vpcmulldq avx512_vnni avx512_bitalg tme  
                     avx512_vpopcntdq la57 rdpid md_clear pconfid flush_l1d arch_capabilities
```

```
/cache data
cache size : 36864 KB
```

```
From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 48 49 50 51  
            52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71  
node 0 size: 485933 MB  
node 0 free: 514848 MB  
node 1 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 72  
            73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95  
node 1 size: 485444 MB  
node 1 free: 515353 MB  
node distances:
  node 0   1
  0:  20  20
  1:  20  10
```

```
From /proc/meminfo
MemTotal:       1056481976 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630 V2
(2.10 GHz, Intel Xeon Gold 5318Y)

SPECspeed®2017_fp_base = 174
SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Jul-2021
Tested by: Lenovo Global Technology
Hardware Availability: Jul-2021
Software Availability: Dec-2020

Platform Notes (Continued)

/sbin/tuned-adm active
  Current active profile: throughput-performance

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.3 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.3"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
  Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swaps barriers and __user pointer sanitation

CVE-2017-5735 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2): Not affected
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jul 8 18:48

SPEC is set to: /home/cpu2017-1.1.8-ic2021.1-revB
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/sdb4      xfs  819G  92G  727G  12% /home

From /sys/devices/virtual/dmi/id
  Vendor: Lenovo

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630 V2
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SPECspeed®2017_fp_base = 174
SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Platform Notes (Continued)

Product: ThinkSystem SR630 V2 MB
Product Family: ThinkSystem
Serial: 1234567890

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
32x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200, configured at 2933

BIOS:
BIOS Vendor: Lenovo
BIOS Version: AFE111A-1.02
BIOS Date: 05/07/2021
BIOS Revision: 1.2
Firmware Revision: 1.10

(End of data from sysinfo program)

Compiler Version Notes

C | 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
-------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++, C, Fortran | 607.cactuBSSN_s(base)
-------------------------------
Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran | 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)

(Continued on next page)
## Lenovo Global Technology

ThinkSystem SR630 V2
(2.10 GHz, Intel Xeon Gold 5318Y)

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>174</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak</td>
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</table>

### Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran, C | 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

### Base Compiler Invocation

C benchmarks:
- icc

Fortran benchmarks:
- ifort

Benchmarks using both Fortran and C:
- ifort icc

Benchmarks using Fortran, C, and C++:
- icpc icc ifort

### Base Portability Flags

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- -assume byterecl
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64

(Continued on next page)
Lenovo Global Technology
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SPECspeed®2017_fp_base = 174
SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2021
Hardware Availability: Jul-2021
Software Availability: Dec-2020

Base Portability Flags (Continued)

654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -std=c11 -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries

Fortran benchmarks:
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-nostandard-realloc-lhs -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-F.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-F.xml
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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