# Lenovo Global Technology

**ThinkSystem SR650 V2**  
(3.60 GHz, Intel Xeon Gold 6334)

## CPU2017 License:
9017

## Test Sponsor:
Lenovo Global Technology

## Tested by:
Lenovo Global Technology

## Test Date:
Jul-2021

## Hardware Availability:
Jul-2021

## Software Availability:
Dec-2020

### SPECrate® 2017 Floating Point Rate Result

**SPECrate® 2017_fp_base = 179**  
**SPECrate® 2017_fp_peak = Not Run**

<table>
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<th>SPECrate® 2017_fp_peak</th>
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<td>220</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>32</td>
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<tr>
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<tr>
<td>554.roms_r</td>
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</tr>
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</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6334  
- **Max MHz:** 3700  
- **Nominal:** 3600  
- **Enabled:** 16 cores, 2 chips, 2 threads/core  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 1.25 MB I+D on chip per core  
- **L3:** 18 MB I+D on chip per chip  
- **Memory:** 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 15 SP2 (x86_64)  
  Kernel 5.3.18-22-default
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++  
  Compiler Build 20201113 for Linux;  
  Fortran: Version 2021.1 of Intel Fortran Compiler  
  Classic Build 20201112 for Linux
- **Parallel:** No
- **Firmware:** Lenovo BIOS Version AFE111A 1.02 released May-2021
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage
**Lenovo Global Technology**  
ThinkSystem SR650 V2  
(3.60 GHz, Intel Xeon Gold 6334)

---

**SPECRate®2017_fp_base = 179**  
**SPECRate®2017_fp_peak = Not Run**

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### Results Table

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</tr>
</tbody>
</table>

---

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

---

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:

```
```

```
MALLOC_CONF = "retain:true"
```

---

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7940X CPU + 64GB RAM  
memory using openSUSE Leap 15.2  
Transparent Huge Pages enabled by default
Lenovo Global Technology
ThinkSystem SR650 V2
(3.60 GHz, Intel Xeon Gold 6334)

**CPU2017 License:** 9017
**Test Sponsor:** Lenovo Global Technology
**Tested by:** Lenovo Global Technology

### SPECrate®2017_fp_base = 179
**Test Date:** Jul-2021
**Hardware Availability:** Jul-2021
**Software Availability:** Dec-2020

### SPECrate®2017_fp_peak = Not Run

---

**General Notes (Continued)**

Prior to runcpu invocation
Filesystem page cache synced and cleared with:
```
sync; echo 3 > /proc/sys/vm/drop_caches
```
runcpu command invoked through numactl i.e.:
```
numactl --interleave=all runcpu <etc>
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

### Platform Notes

**BIOS configuration:**
Choose Operating Mode set to Maximum Performance
DCU Streamer Prefetcher set to Disabled
DCU IP Prefetcher set to Disabled
SNC set to Enabled
Patrol Scrub set to Disabled

**Sysinfo program**
```
/home/cpu2017-1.1.8-ic2021.1-revA-update1/bin/sysinfo
```
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca6c64d
running on localhost Thu Jul 15 05:00:11 2021

**SUT (System Under Test)** info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
```
model name : Intel(R) Xeon(R) Gold 6334 CPU @ 3.60GHz
      2 "physical id"s (chips)
     32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
```

From lscpu from util-linux 2.33.1:
```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
```

(Continued on next page)
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**Test Date:** Jul-2021  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2020

**Platform Notes (Continued)**

- Byte Order: Little Endian
- Address sizes: 46 bits physical, 57 bits virtual
- CPU(s): 32
- On-line CPU(s) list: 0-31
- Thread(s) per core: 2
- Core(s) per socket: 8
- Socket(s): 2
- NUMA node(s): 4
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 106
- Model name: Intel(R) Xeon(R) Gold 6334 CPU @ 3.60GHz
- Stepping: 6
- CPU MHz: 3600.000
- BogoMIPS: 7200.00
- Virtualization: VT-x
- L1d cache: 48K
- L1i cache: 32K
- L2 cache: 1280K
- L3 cache: 18432K
- NUMA node0 CPU(s): 0-3,16-19
- NUMA node1 CPU(s): 4-7,20-23
- NUMA node2 CPU(s): 8-11,24-27
- NUMA node3 CPU(s): 12-15,28-31
- Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1_pwd mce mmxplus famine七星豹 avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs ibpb ibrsenhancement tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 4ms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaveas cmqm_llc cmqm_occupp_llc cmqm_mbb_total cmqm_mbb_local wboinvd dtc q finishing ida arat plc pts avx512vbmi umip pk m p she avx512_vbmi2 gfn vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

From numactl --hardware

**WARNING:** numactl 'node' might or might not correspond to a physical chip.

- available: 4 nodes (0-3)
- node 0 cpus: 0 1 2 3 16 17 18 19
- node 0 size: 257602 MB
- node 0 free: 257328 MB

(Continued on next page)
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SPEC CPU®2017 Floating Point Rate Result

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CPU2017 License: 9017
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Platform Notes (Continued)

node 1 cpus: 4 5 6 7 20 21 22 23
node 1 size: 258045 MB
node 1 free: 257804 MB
node 2 cpus: 8 9 10 11 24 25 26 27
node 2 size: 258045 MB
node 2 free: 257813 MB
node 3 cpus: 12 13 14 15 28 29 30 31
node 3 size: 258043 MB
node 3 free: 257730 MB
node distances:
node 0 1 2 3
0: 10 11 20 20
1: 11 10 20 20
2: 20 20 10 11
3: 20 20 11 10

From /proc/meminfo
MemTotal: 1056498900 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 15 SP2

From /etc/*release* /etc/*version*

os-release:
NAME="SLES"
VERSION="15-SP2"
VERSION_ID="15.2"
PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp2"

uname -a:
Linux localhost 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and

(Continued on next page)
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Platform Notes (Continued)

CVE-2017-5753 (Spectre variant 1):
Mitigation: usercopy/swapgs barriers and __user pointer sanitation

CVE-2017-5715 (Spectre variant 2):
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling):
Not affected
CVE-2019-11135 (TSX Asynchronous Abort):
Not affected

run-level 3 Jul 15 04:30

SPEC is set to: /home/cpu2017-1.1.8-ic2021.1-revA-update1

Filesystem  Type Size  Used Avail Use% Mounted on
/dev/sda3      xfs   891G   54G  838G   6% /

From /sys/devices/virtual/dmi/id
Vendor: Lenovo
Product: ThinkSystem SR650 V2 MB
Product Family: ThinkSystem
Serial: 1234567890

Memory:
32x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200

BIOS:
BIOS Vendor: Lenovo
BIOS Version: AFE111A-1.02
BIOS Date: 05/07/2021
BIOS Revision: 1.2
Firmware Revision: 1.10

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C               | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base) |
|------------------------------------------------------------------------------|
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,   |
| Version 2021.1 Build 20201113                                               |
| Copyright (C) 1985-2020 Intel Corporation. All rights reserved.             |
==============================================================================
Lenovo Global Technology
ThinkSystem SR650 V2
(3.60 GHz, Intel Xeon Gold 6334)

SPECrate®2017_fp_base = 179
SPECrate®2017_fp_peak = Not Run

Compiler Version Notes (Continued)

C++ | 508.namd_r(base) 510.parest_r(base)
-----------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------

C++, C | 511.povray_r(base) 526.blender_r(base)
-----------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------

C++, C, Fortran | 507.cactuBSSN_r(base)
-----------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------

Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
-----------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------

Fortran, C | 521.wrf_r(base) 527.cam4_r(base)
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Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
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Intel (R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
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Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
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C benchmarks:
- `-w` `-std=c11` `-m64` `-Wl,-z,muldefs` `-xCORE-AVX512` `-Ofast` `-ffast-math`
- `-flto` `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries` `-ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

C++ benchmarks:
- `-w` `-m64` `-Wl,-z,muldefs` `-xCORE-AVX512` `-Ofast` `-ffast-math` `-flto`
- `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries` `-ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

Fortran benchmarks:
- `-w` `-m64` `-Wl,-z,muldefs` `-xCORE-AVX512` `-O3` `-ipo` `-no-prec-div`
- `-qopt-prefetch` `-ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles` `-qopt-mem-layout-trans=4`
- `-nostandard-realloc-lhs` `-align array32byte` `-auto`
- `-mbranches-within-32B-boundaries` `-ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both Fortran and C:
- `-w` `-m64` `-std=c11` `-Wl,-z,muldefs` `-xCORE-AVX512` `-Ofast` `-ffast-math`
- `-flto` `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4` `-O3` `-ipo`
- `-no-prec-div` `-qopt-prefetch` `-ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles`
- `-mbranches-within-32B-boundaries` `-nostandard-realloc-lhs`
- `-align array32byte` `-auto` `-ljemalloc` `-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both C and C++:
- `-w` `-m64` `-std=c11` `-Wl,-z,muldefs` `-xCORE-AVX512` `-Ofast` `-ffast-math`
- `-flto` `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries` `-ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using Fortran, C, and C++:
- `-w` `-m64` `-std=c11` `-Wl,-z,muldefs` `-xCORE-AVX512` `-Ofast` `-ffast-math`
- `-flto` `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4` `-O3`
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- `-align array32byte` `-auto` `-ljemalloc` `-L/usr/local/jemalloc64-5.0.1/lib`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-F.html
## Lenovo Global Technology

**ThinkSystem SR650 V2**  
*(3.60 GHz, Intel Xeon Gold 6334)*

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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Jul-2021  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2020  

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You can also download the XML flags sources by saving the following links:


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